## **Excellent Integrated System Limited**

Stocking Distributor

Click to view price, real time Inventory, Delivery & Lifecycle Information:

<u>Vishay Semiconductor/Diodes Division</u> <u>V10P20-M3/87A</u>

For any questions, you can email us directly: <a href="mailto:sales@integrated-circuit.com">sales@integrated-circuit.com</a>

Datasheet of V10P20-M3/87A - DIODE SCHOTTKY 200V 2.4A TO277A

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## V10P20

HALOGEN

**FREE** 

Vishay General Semiconductor

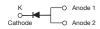
## **High Current Density Surface Mount** Trench MOS Barrier Schottky Rectifier

Ultra Low  $V_F = 0.59 \text{ V}$  at  $I_F = 5 \text{ A}$ 

#### TMBS® eSMP® Series



#### **TO-277A (SMPC)**



PRIMARY CHARACTERISTICS			
I <sub>F(AV)</sub>	10 A		
V <sub>RRM</sub>	200 V		
I <sub>FSM</sub>	180 A		
V <sub>F</sub> at I <sub>F</sub> = 10 A	0.67 V		
T <sub>J</sub> max.	150 °C		
Package	TO-277A		
Diode variation	Single		

#### **FEATURES**

- Very low profile typical height of 1.1 mm
- Ideal for automated placement
- Trench MOS Schottky technology
- · Low forward voltage drop, low power losses
- High efficiency operation
- Meets MSL level 1, per J-STD-020, LF maximum peak of
- Material categorization: For definitions of compliance please see www.vishay.com/doc?99912

#### **TYPICAL APPLICATIONS**

For use in low voltage high frequency inverters, freewheeling, DC/DC converters and polarity protection applications.

#### **MECHANICAL DATA**

Case: TO-277A (SMPC)

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant, and commercial grade

Terminals: Matte tin plated leads, solderable per

J-STD-002 and JESD 22-B102

M3 suffix meets JESD 201 class 2 whisker test

MAXIMUM RATINGS (T <sub>A</sub> = 25 °C unless otherwise noted)				
PARAMETER	SYMBOL	V10P20	UNIT	
Device marking code		V1020		
Maximum repetitive peak reverse voltage	$V_{RRM}$	200	V	
Maximum average forward rectified current (fig. 1)	I <sub>F</sub> <sup>(1)</sup>	10	А	
	I <sub>F</sub> <sup>(2)</sup>	2.4		
Peak forward surge current 10 ms single half sine-wave superimposed on rated load	I <sub>FSM</sub>	180	А	
Voltage rate of change (rated V <sub>R</sub> )	dV/dt	10 000	V/µs	
Operating junction and storage temperature range	T <sub>J</sub> , T <sub>STG</sub>	- 40 to + 150	°C	

- (1) Mounted on 30 mm x 30 mm pad areas aluminum PCB
- (2) Free air, mounted on recommended copper pad area

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### V10P20

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<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>A</sub> = 25 °C unless otherwise noted)						
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT
Instantaneous forward voltage	I <sub>F</sub> = 5.0 A	T <sub>A</sub> = 25 °C	V <sub>F</sub> <sup>(1)</sup>	0.78	-	V
	I <sub>F</sub> = 10 A			0.98	1.34	
	I <sub>F</sub> = 5.0 A	T <sub>A</sub> = 125 °C		0.59	-	
	I <sub>F</sub> = 10 A			0.67	0.75	
Reverse current		T <sub>A</sub> = 25 °C	I <sub>R</sub> (2)	3.6	-	μΑ
		T <sub>A</sub> = 125 °C		3.5	-	mA
		T <sub>A</sub> = 25 °C		8.6	400	μΑ
		T <sub>A</sub> = 125 °C		5.8	30	mA

#### Notes

- (1) Pulse test: 300 µs pulse width, 1 % duty cycle
- (2) Pulse test: Pulse width ≤ 40 ms

THERMAL CHARACTERISTICS (T <sub>A</sub> = 25 °C unless otherwise noted)				
PARAMETER SYMBOL V10P20			UNIT	
Typical thermal registance	R <sub>θJA</sub> <sup>(1)</sup>	80	°C/W	
Typical thermal resistance	$R_{\theta JM}$ (2)	4		

#### **Notes**

- $^{(1)}$  Free air, mounted on recommended copper pad area; thermal resistance  $R_{\theta JA}$  junction to ambient
- $^{(2)}$  Mounted on 30 mm x 30 mm Al PCB; thermal resistance  $R_{\theta JM}$  junction to mount

ORDERING INFORMATION (Example)					
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE	
V10P20-M3/86A	0.10	86A	1500	7" diameter plastic tape and reel	
V10P20-M3/87A	0.10	87A	6500	13" diameter plastic tape and reel	

#### **RATINGS AND CHARACTERISTICS CURVES**

(T<sub>A</sub> = 25 °C unless otherwise noted)

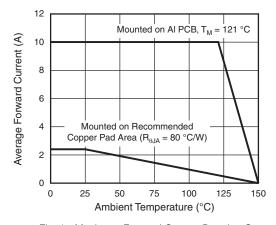


Fig. 1 - Maximum Forward Current Derating Curve

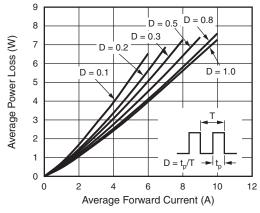


Fig. 2 - Forward Power Loss Characteristics

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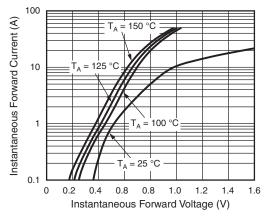


Fig. 3 - Typical Instantaneous Forward Characteristics

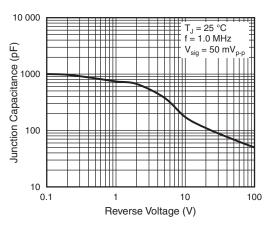


Fig. 5 - Typical Junction Capacitance

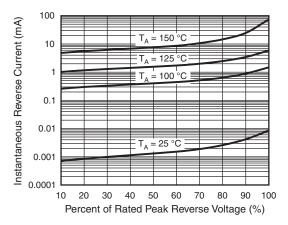


Fig. 4 - Typical Reverse Characteristics

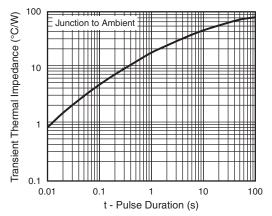


Fig. 6 - Typical Transient Thermal Impedance

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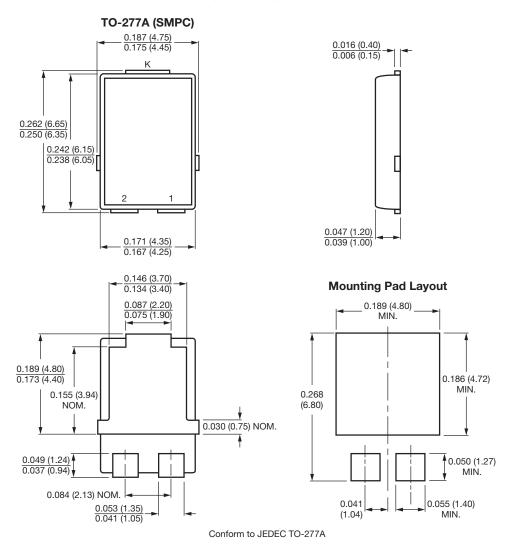
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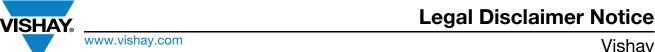
#### **PACKAGE OUTLINE DIMENSIONS** in inches (millimeters)





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