

# **Excellent Integrated System Limited**

Stocking Distributor

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Vishay Semiconductor/Diodes Division VBUS54DD-HS4-G4-08

For any questions, you can email us directly: <u>sales@integrated-circuit.com</u>



**FEATURES** 

· Low leakage current

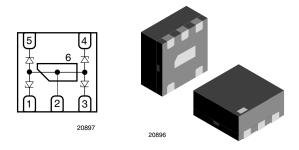




## VBUS54DD-HS4

Vishay Semiconductors

# **4-Line BUS-Port ESD Protection**



#### MARKING (example only)



Dot = pin 1 marking

X = date code

Y = type code (see table below)

ORDERING INFORMATION						
DEVICE NAME	ICE NAME ORDERING CODE		MINIMUM ORDER QUANTITY			
VBUS54DD-HS4	VBUS54DD-HS4-G4-08	5000	5000			

PACKAGE DATA						
DEVICE NAME	PACKAGE NAME	TYPE CODE	WEIGHT	MOLDING COMPOUND FLAMMABILITY RATING	MOISTURE SENSITIVITY LEVEL	SOLDERING CONDITIONS
VBUS54DD-HS4	LLP1010-6M	D	1.07 mg	UL 94 V-0	MSL level 1 (according J-STD-020)	260 °C/10 s at terminals

ABSOLUTE MAXIMUM RATINGS VBUS54DD-HS4						
PARAMETER	TEST CONDITIONS	SYMBOL	VALUE	UNIT		
Peak pulse current	Pin 1, 3, 4 or 5 to pin 2 or 6 acc. IEC 61000-4-5; t <sub>p</sub> = 8/20 μs; single shot	I <sub>PPM</sub>	3	А		
Peak pulse power	Pin 1, 3, 4 or 5 to pin 2 or 6 acc. IEC 61000-4-5; $t_p = 8/20 \ \mu s$ ; single shot	P <sub>PP</sub>	57	W		
ESD immunity	Contact discharge acc. IEC 61000-4-2; 10 pulses	N	± 15	kV		
	Air discharge acc. IEC 61000-4-2; 10 pulses	V <sub>ESD</sub>	± 15	kV		
Operating temperature	Junction temperature	TJ	-40 to +125	°C		
Storage temperature		T <sub>STG</sub>	-55 to +150	°C		

1 For technical questions, contact: <u>ESDprotection@vishay.com</u> RoHS COMPLIANT HALOGEN FREE GREEN (5-2008)

<u>GR</u> (5-

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• Pin plating NiPdAu (e4) no whisker growth

Ultra compact LLP1010-6M package
Low package height < 0.4 mm</li>
4-line USB ESD-protection

Low load capacitance C<sub>D</sub> = 0.8 pF

• ESD-protection acc. IEC 61000-4-2

± 15 kV contact discharge

± 15 kV air discharge

 Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>



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# VBUS54DD-HS4

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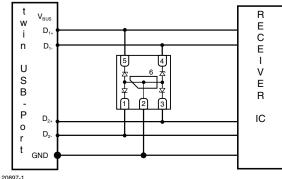
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<b>ELECTRICAL CHARACTERISTICS VBUS54DD-HS4</b> (Pin 1, 3, 4, or 5 to pin 2 or 6) (T <sub>amb</sub> = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITIONS/REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT	
Protection paths	Number of lines which can be protected	N <sub>channel</sub>	-	-	4	lines	
Reverse stand-off voltage		V <sub>RWM</sub>	-	-	5.5	V	
Reverse current	at $V_{IN} = V_{RWM} = 5.5 V$	I <sub>R</sub>	-	< 0.01	0.1	μA	
Reverse breakdown voltage	at I <sub>R</sub> = 1 mA	V <sub>BR</sub>	6.9	8	8.7	V	
Reverse clamping voltage	at I <sub>PP</sub> = 3 A acc. IEC 61000-4-5	V <sub>C</sub>	-	16	19	V	
Forward clamping voltage	at I <sub>F</sub> = 3 A acc. IEC 61000-4-5	V <sub>F</sub>	-	3.5	4.5	V	
Capacitance	V <sub>IN</sub> = 0 V; any other I/O pin at 3.3 V	<u> </u>	-	0.8	1	pF	
	$V_{IN}$ = 2.5 V; any other I/O pin at 3.3 V	C <sub>D</sub>	-	0.5	0.8	pF	

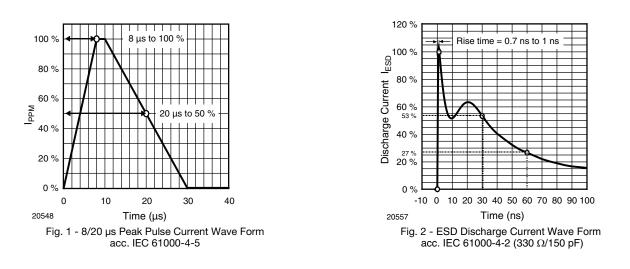
#### APPLICATION NOTE

With the **VBUS54DD-HS4** a double, high speed USB-port or up to 4 other high speed signal or data lines can be protected against transient voltage signals. Negative transients will be clamped close below the ground level while positive transients will be clamped close above the working range. The high speed data lines,  $D_{1+}$ ,  $D_{2+}$ ,  $D_{1-}$  and  $D_{2-}$ , are connected to pin **1**, **3**, **4**, and **5**, pin **2 or 6** are connected to ground. As long as the signal voltage on the data lines is between the ground- and the break down level, the low input capacitance of each channel offers a very high isolation to ground and to the other data lines. But as soon as any transient signal exceeds this working range, the VBUS54DD-HS4 clamps the transient to ground or to the avalanche break down voltage level.



#### TYPICAL CHARACTERISTICS

T<sub>amb</sub> = 25 °C, unless otherwise specified



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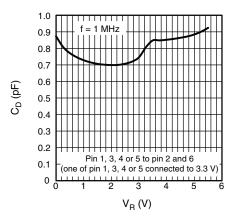


Fig. 3 - Typical Capacitance  $C_D$  vs. Reverse Voltage  $V_R$ 

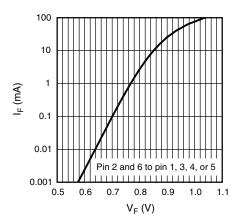


Fig. 4 - Typical Forward Current I<sub>F</sub> vs. Forward Voltage V<sub>F</sub>

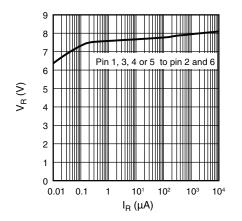


Fig. 5 - Typical Reverse Voltage V<sub>R</sub> vs. Reverse Current I<sub>R</sub>

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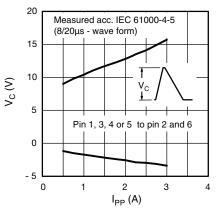


Fig. 6 - Typical Peak Clamping Voltage  $V_C$  vs. Peak Pulse Current IPP

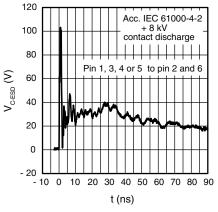
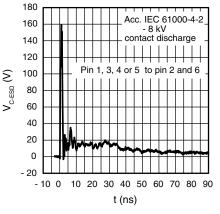
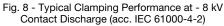


Fig. 7 - Typical Clamping Performance at + 8 kV Contact Discharge (acc. IEC 61000-4-2)





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3 For technical questions, contact: ESDprotection@vishay.com

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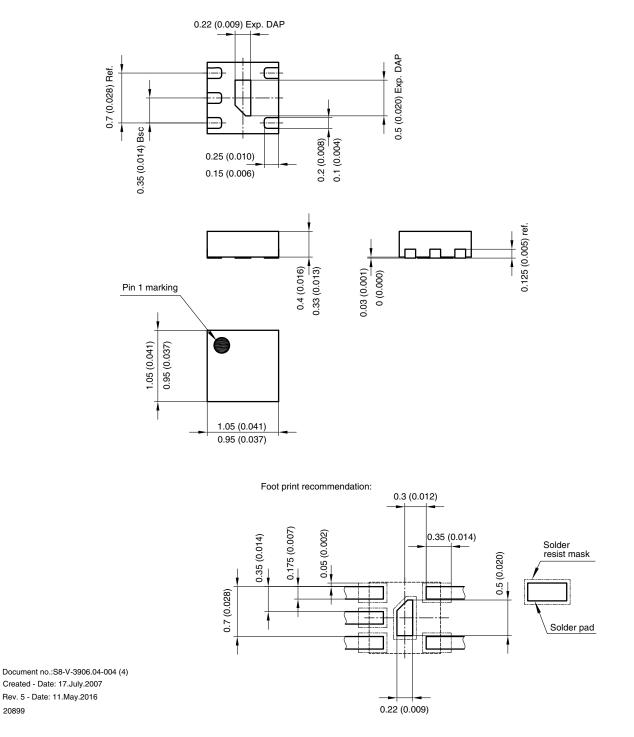


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#### PACKAGE DIMENSIONS in millimeters (inches): LLP1010-6M



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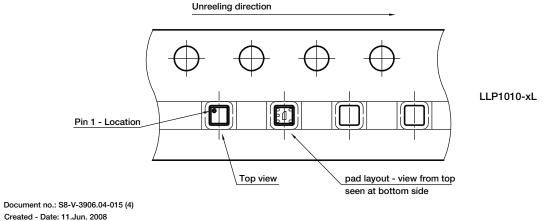


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#### **ORIENTATION IN CARRIER TAPE - LLP1010-xL**



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