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Diodes Incorporated DMG302PU-13

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DMG302PU

25V P-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

$V_{(BR)DSS}$	R _{DS(ON)}	Ι _D T _A = +25°C
-25V	10Ω @ V _{GS} = -4.5V	-0.17A
-25V	13Ω @ V _{GS} = -2.7V	-0.15A

Description

This new generation MOSFET has been designed to minimize the onstate resistance ($R_{DS(ON)}$) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

- DC-DC Converters
- Power Management Functions

Features

- Low On-Resistance
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Small Surfaced Mount Package
- ESD Protected Gate (>6kV Human Body Model)
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

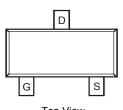
Mechanical Data

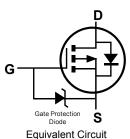
- Case: SOT23
- Case Material: Molded Plastic. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Solderable per MIL-STD-202, Method 208 ⁽³⁾
- Lead Free Plating (Matte Tin Finish annealed over Alloy 42 leadframe).
- Terminal Connections: See Diagram
- Weight: 0.008 grams (approximate)





Top View





Top View Pin Configuration

Ordering Information (Note 4)

Part Number	Compliance	Case	Packaging
DMG302PU-7	Standard	SOT23	3,000/Tape & Reel
DMG302PU-13	Standard	SOT23	10,000/Tape & Reel

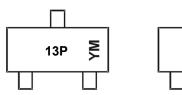
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.

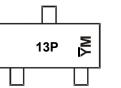
2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

Alalogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and
<1000ppm antimony compounds.

4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information





13P = Product Type Marking Code <u>YM</u> = Date Code Marking for SAT (Shanghai Assembly/ Test site) <u>YM</u> = Date Code Marking for CAT (Chengdu Assembly/ Test site) Y or <u>Y</u> = Year (ex: A = 2013) M = Month (ex: 9 = September)

Date Code Key

Notes:

Year	201	1	2012		2013	20	14	2015		2016	2	2017
Code	Y		Z		А	E	3	С		D		E
Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D

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DMG302PU

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units V		
Drain-Source Voltage	V _{DSS}	-25			
Gate-Source Voltage		V _{GSS}	-8	V	
Continuous Drain Current (Note 6) V _{GS} = -4.5V	Steady State	T _A = +25°C T _A = +70°C	ID	-0.17 -0.14	А
Continuous Drain Current (Note 6) V _{GS} = -2.7V	۱ _D	-0.15 -0.12	А		
Pulsed Drain Current T _P ≤ 300µs, Duty Cycle = 2%)	I _{DM}	-0.5	А		

Thermal Characteristics

Characteristic	Symbol	Value	Units		
Total Dawar Dissinction	(Note 5)	D	0.33	W	
Total Power Dissipation	(Note 6)	P _D	0.45	vv	
Thermal Desistance Junction to Ambient	(Note 5)	P	376		
Thermal Resistance, Junction to Ambient	(Note 6)	R _{θJA}	275	°C/W	
Thermal Resistance, Junction to Case	(Note 6)	R _{θJC}	81		
Operating and Storage Temperature Range		T _{J,} T _{STG}	-55 to +150	°C	

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

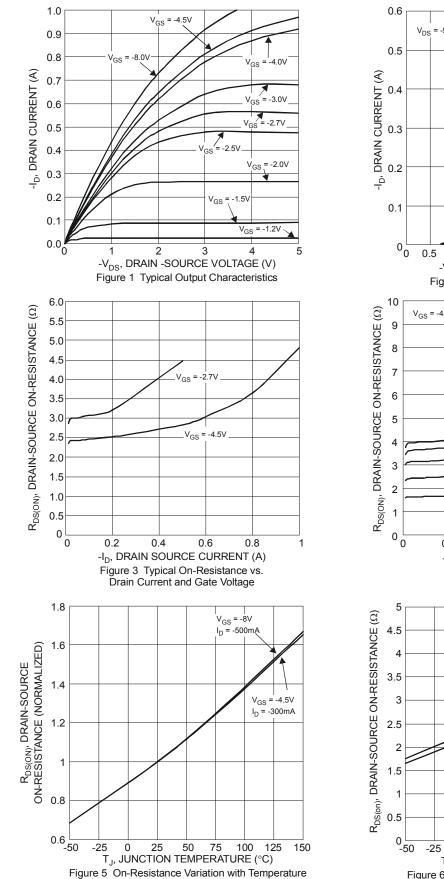
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)			_		_	
Drain-Source Breakdown Voltage	BV _{DSS}	-25	—	_	V	V_{GS} = 0V, I_{D} = -250µA
Zero Gate Voltage Drain Current	I _{DSS}	_	—	-1	μA	V _{DS} = -20V, V _{GS} = 0V
Gate-Source Leakage	I _{GSS}	_	—	-100	nA	V_{GS} = -8V, V_{DS} = 0V
ON CHARACTERISTICS (Note 7)					_	
Gate Threshold Voltage	V _{GS(th)}	-0.65	-0.96	-1.5	V	V_{DS} = V_{GS} , I_D = -250 μ A
Static Drain-Source On-Resistance	Б	_	2.5	10	0	V _{GS} = -4.5V, I _D = -0.2A
	R _{DS(ON)}	_	3	13	Ω	V_{GS} = -2.7V, I_{D} = -0.05A
Forward Transfer Admittance	Y _{fs}	_	189	_	ms	V _{DS} = -5V, I _D = -0.2A
Diode Forward Voltage (Note 7)	V _{SD}	_	_	-1.5	V	V _{GS} = 0V, I _S = -0.2A
DYNAMIC CHARACTERISTICS (Note 8)			_			
Input Capacitance	C _{iss}	_	27.2	_		V _{DS} = -10V, V _{GS} = 0V f = 1.0MHz
Output Capacitance	C _{oss}	_	6.1	_	pF	
Reverse Transfer Capacitance	C _{rss}	_	1.7	_		
Total Gate Charge	Qg	_	0.35	_		
Gate-Source Charge	Q _{gs}	_	0.08	_	nC	V_{DS} = -5V, I _D = -0.2A, V_{GS} = -4.5V,
Gate-Drain Charge	Q _{gd}	_	0.06			
Turn-On Delay Time	t _{d(on)}		4.5			
Rise Time	tr		2.3		V _{GS} = -4.5V, V _{DD} = -6V	
Turn-Off Delay Time	t _{d(off)}		24.1		ns	$I_{\rm D}$ = -0.2A, $R_{\rm G}$ = 50 Ω
Fall Time	t _f	_	11.0		1	

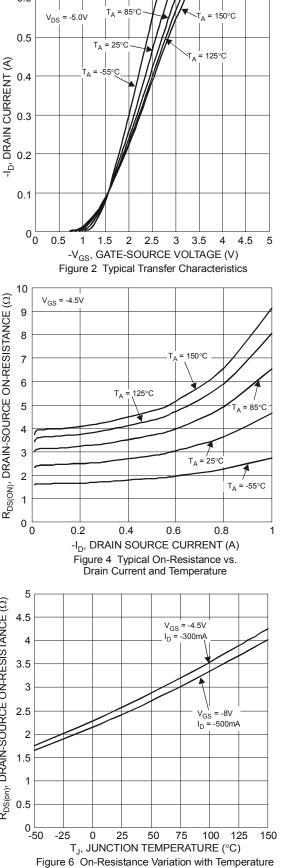
Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.
Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper pad layout
Short duration pulse test used to minimize self-heating effect.

8. Guaranteed by design. Not subject to production testing.



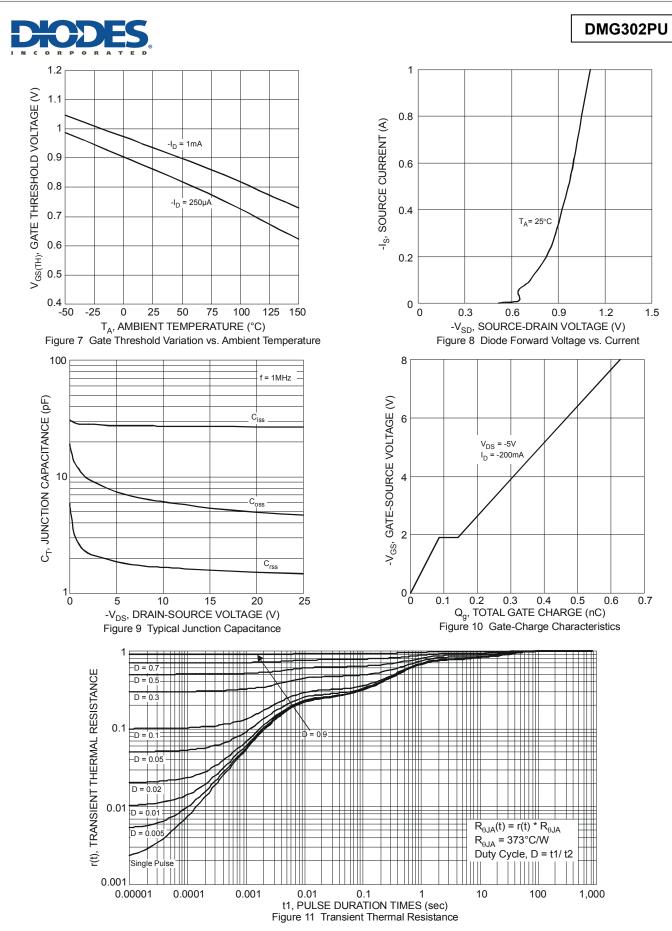
DMG302PU





DMG302PU Document number: DS36227 Rev. 2 - 2 3 of 6 www.diodes.com May 2014 © Diodes Incorporated





NEW PRODUCT

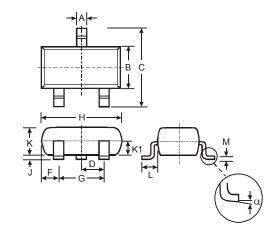


N C O R P O R A T E D

DMG302PU

Package Outline Dimensions

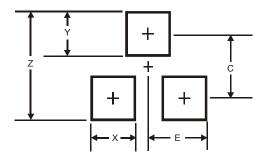
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.



	SO	T23			
Dim	Min	Max	Тур		
Α	0.37	0.51	0.40		
В	1.20	1.40	1.30		
С	2.30	2.50	2.40		
D	0.89	1.03	0.915		
F	0.45	0.45 0.60 0.5			
G	1.78	2.05	1.83		
Н	2.80	3.00	2.90		
J	0.013	0.10	0.05		
Κ	0.903 1.10 1.		1.00		
K1	-	-	0.400		
L	0.45	0.61	0.55		
М	0.085	0.18	0.11		
α	0°	8°	-		
All	Dimens	ions in	mm		

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
Z	2.9
Х	0.8
Y	0.9
С	2.0
E	1.35





DMG302PU

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