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Vishay Semiconductor/Diodes Division VS-UFB170FA60

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VS-UFB170FA60

Vishay Semiconductors

Insulated Ultrafast Rectifier Module, 170 A



PRODUCT SUMMARY

VR

 $I_{F(AV)}$ per module at $T_C = 76 \ ^{\circ}C$

t_{rr}

Туре

Package

FEATURES

- Two fully independent diodes
- · Fully insulated package
- Ultrafast, soft reverse recovery, with high COMPLIANT operation junction temperature (T_J max. = 175 °C)
- Very low forward voltage drop
- · Optimized for power conversion: welding and industrial SMPS applications
- · Easy to use and parallel
- Industry standard outline
- UL approved file E78996
- Designed and qualified for industrial level
- · Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

DESCRIPTION / APPLICATIONS

The VS-UFB170FA60 insulated modules integrate two state of the art ultrafast recovery rectifiers in the compact, industry standard SOT-227 package. The diodes structure, and its life time control, provide an ultrasoft recovery current shape, together with the best overall performance, ruggedness and reliability characteristics.

These devices are thus intended for high frequency applications in which the switching energy is designed not to be predominant portion of the total energy, such as in the output rectification stage of welding machines, SMPS, DC/DC converters. Their extremely optimized stored charge and low recovery current reduce both over dissipation in the switching elements (and snubbers) and EMI/RFI.

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS	
Cathode to anode voltage	V _R		600	V	
Continuous forward current per diode	۱ _F	T _C = 90 °C	94	٨	
Single pulse forward current per diode	I _{FSM}	T _C = 25 °C	850	A	
Maximum power dissipation per module	PD	T _C = 90 °C	233	W	
RMS isolation voltage	V _{ISOL}	Any terminal to case, t = 1 minute	2500	V	
Operating junction and storage temperatures	T _J , T _{Stg}		-55 to +175	°C	

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600 V

170 A

60 ns

Modules - Diode FRED Pt®

SOT-227



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VS-UFB170FA60

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ELECTRICAL SPECIFICATIONS PER DIODE ($T_J = 25$ °C unless otherwise specified)						
PARAMETER	SYMBOL	L TEST CONDITIONS		TYP.	MAX.	UNITS
Cathode to anode breakdown voltage	V _{BR}	I _R = 100 μA	600	-	-	
Forward voltage, per leg	V _{FM}	I _F = 50 A	-	1.02	1.19	
		I _F = 50 A, T _J = 175 °C	-	0.87	-	V
		I _F = 100 A	-	1.17	1.43	
		I _F = 100 A, T _J = 175 °C	-	1.03	-	
Reverse leakage current, per leg	I _{RM}	$V_{\rm R} = V_{\rm R}$ rated	-	0.5	50	μA
neverse leakage current, per leg		$T_J = 175 \text{ °C}, V_R = V_R \text{ rated}$	-	0.1	2	mA
Junction capacitance, per leg	CT	V _R = 600 V	-	43	-	pF

DYNAMIC RECOVERY CHARACTERISTICS ($T_J = 25$ °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
		$T_J = 25 \text{ °C}, I_F = 1 \text{ A } dI_F/dt = 200 \text{ A/}\mu\text{s } V_R = 30 \text{ V}$		-	60	-	
Reverse recovery time, per leg	t _{rr}	T _J = 25 °C		-	170	-	ns
		T _J = 125 °C		-	270	-	
Poak recovery ourrent per leg	1	T _J = 25 °C	I _F = 25 A dI _F /dt = 500 A/μs V _R = 200 V	-	40	-	A
Peak recovery current, per leg	I _{RRM}	T _J = 125 °C		-	54	-	
	0	T _J = 25 °C		-	3.4	-	μC
Reverse recovery charge, per leg	Q _{rr}	T _J = 125 °C		-	6.8	-	
Reverse recovery time, per leg	+	T _J = 25 °C	I _F = 50 A dI _F /dt = 500 A/μs V _R = 200 V	-	220	-	ns
neverse recovery time, per leg	t _{rr}	T _J = 125 °C		-	300	-	
Peak recovery current, per leg		T _J = 25 °C		-	47	-	A
	I _{RRM}	T _J = 125 °C		-	61	-	
	0	T _J = 25 °C		-	5.2	-	μC
Reverse recovery charge, per leg	Q _{rr}	T _J = 125 °C		-	9.1	-	

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Junction to case, single leg conducting			-	-	0.73	
Junction to case, both leg conducting	R _{thJC}		-	-	0.365	°C/W
Case to heatsink	R _{thCS}	Flat, greased surface	-	0.10	-	
Weight			-	30	-	g
Mounting torque		Torque to terminal	-	-	1.1 (9.7)	Nm (lbf.in)
Mounting torque		Torque to heatsink	-	-	1.3 (11.5)	Nm (lbf.in)
Case style				S	OT-227	

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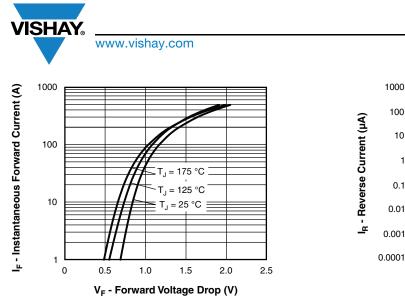


Fig. 1 - Typical Forward Voltage Drop Characteristics (Per Leg)

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T_{.1} = 175 °C

T₁ = 125 °C

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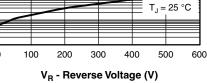


Fig. 2 - Typical Reverse Current vs. Reverse Voltage (Per Leg)

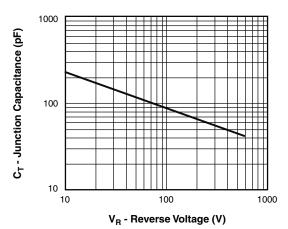


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

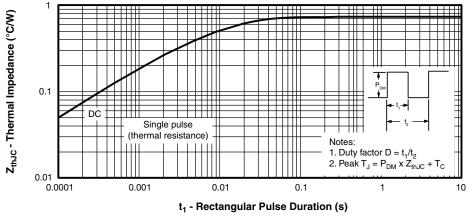


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

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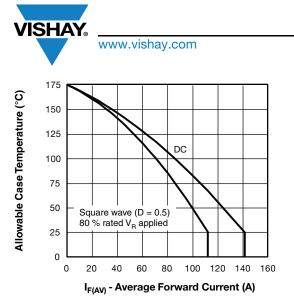


Fig. 5 - Allowable Case Temperature vs. Average Forward Current (Per Leg)

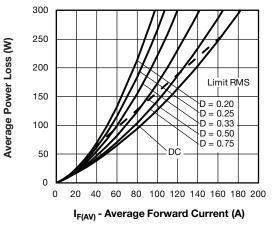


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

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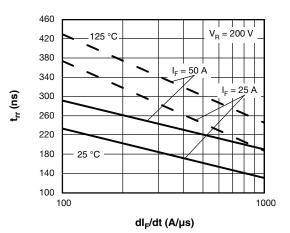


Fig. 7 - Typical Reverse Recovery Time vs. dl_F/dt (Per Leg)

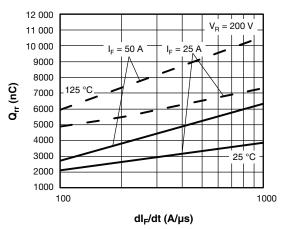


Fig. 8 - Typical Stored Charge vs. dl_F/dt (Per Leg)

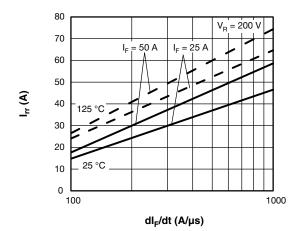


Fig. 9 - Typical Recovery Current vs. dl_F/dt (Per Leg)

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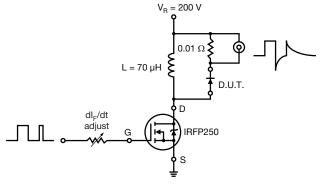
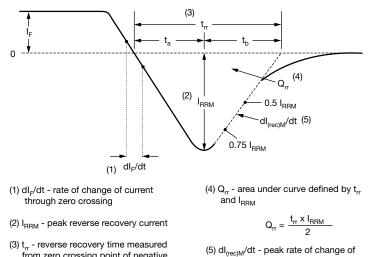
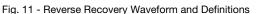


Fig. 10 - Reverse Recovery Parameter Test Circuit



from zero crossing point of negative going I_F to point where a line passing through 0.75 I_{BBM} and 0.50 I_{BBM} extrapolated to zero current.



current during t_b portion of t_{rr}

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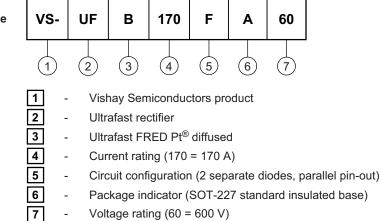
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ORDERING INFORMATION TABLE

Device code



CIRCUIT CONFIGURATION					
CIRCUIT	CIRCUIT CONFIGURATION CODE	CIRCUIT DRAWING			
2 separate diodes, parallel pin-out	F	Lead Assignment 4 0 0 3 4 1 0 0 2 1 1 0 0 2 1 1 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0			

LINKS TO RELATED DOCUMENTS					
Dimensions www.vishay.com/doc?95423					
Packaging information	www.vishay.com/doc?95425				

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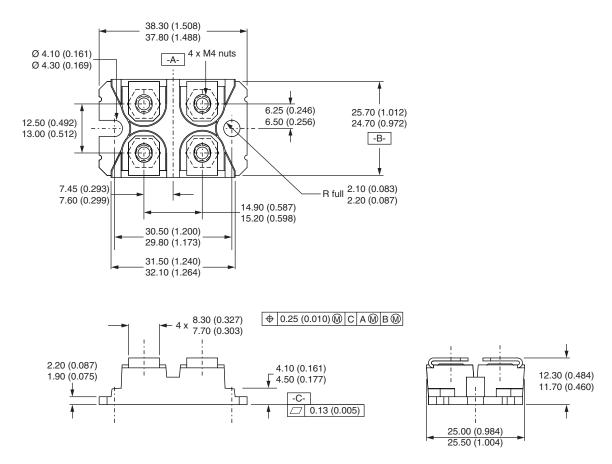
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Outline Dimensions

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SOT-227 Generation II

DIMENSIONS in millimeters (inches)



Note

• Controlling dimension: millimeter

Revision: 02-Aug-12

Document Number: 95423





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