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Vishay Semiconductor/Diodes Division VS-10MQ100-M3/5AT

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Distributor of Vishay Semiconductor/Diodes Division: Excellent Integrated System Limite

Datasheet of VS-10MQ100-M3/5AT - DIODE SCHOTTKY 100V 1A DO214AC Contact us: sales@integrated-circuit.com Website: www.integrated-circuit.com



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### VS-10MQ100-M3

Vishay Semiconductors

## Schottky Rectifier, 1 A



Cathode Anode

DO-214AC (SMA)

PRODUCT SUMMARY		
Package	DO-214AC (SMA)	
I <sub>F(AV)</sub>	1 A	
V <sub>R</sub>	100 V	
V <sub>F</sub> at I <sub>F</sub>	0.63 V	
I <sub>RM</sub>	1 mA at 125 °C	
T <sub>J</sub> max.	150 °C	
Diode variation	Single die	
E <sub>AS</sub>	1.0 mJ	

#### FEATURES

definition

• Low forward voltage drop



- Guard ring for enhanced ruggedness and long term reliability
  COMPLIANT
- Halogen-free according to IEC 61249-2-21
  HALOGEN
  FREE
- Small foot print, surface mountable
- High frequency operation
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260  $^\circ\mathrm{C}$
- Compliant to RoHS Directive 2002/95/EC

#### DESCRIPTION

The VS-10MQ100-M3 surface mount Schottky rectifier has been designed for applications requiring low forward drop and very small foot prints on PC boards. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I <sub>F(AV)</sub>	DC	1	А		
V <sub>RRM</sub>		100	V		
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	120	А		
V <sub>F</sub>	1.5 Apk, T <sub>J</sub> = 125 °C	0.68	V		
TJ	Range	- 55 to 150	°C		

VOLTAGE RATINGS				
PARAMETER	SYMBOL	VS-10MQ100-M3	UNITS	
Maximum DC reverse voltage	V <sub>R</sub>	100	V	
Maximum working peak reverse voltage	V <sub>RWM</sub>	100	v	

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDI	TIONS	VALUES	UNITS
Maximum average forward current		50 % duty cycle at $T_L$ = 126 °C, rectangular waveform On PC board 9 mm <sup>2</sup> island (0.013 mm thick copper pad area)		1.5	
See fig. 4	I <sub>F(AV)</sub>	50 % duty cycle at $T_L$ = 135 °C, rectangular waveform On PC board 9 mm <sup>2</sup> island (0.013 mm thick copper pad area)		1	А
Maximum peak one cycle non-repetitive surge current, T <sub>J</sub> = 25 °C	1	5 µs sine or 3 µs rect. pulse	Following any rated load condition and with	120	
See fig. 6	I <sub>FSM</sub>	10 ms sine or 6 ms rect. pulse	rated $V_{RRM}$ applied	30	
Non-repetitive avalanche energy	E <sub>AS</sub>	$T_J = 25 \text{ °C}, I_{AS} = 0.5 \text{ A}, L = 8 \text{ mH}$		1.0	mJ
Repetitive avalanche current	I <sub>AR</sub>	$\begin{tabular}{lllllllllllllllllllllllllllllllllll$		А	

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ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
		1 A	T <sub>.1</sub> = 25 °C	0.78	V
Maximum forward voltage drop	V <sub>FM</sub> <sup>(1)</sup>	1.5 A	- IJ=25 C	0.85	
See fig. 1	V FM (")	1 A	T - 105 °C	0.63	
		1.5 A	T <sub>J</sub> = 125 °C	0.68	
Maximum reverse leakage current		T <sub>J</sub> = 25 °C	V <sub>R</sub> = Rated V <sub>R</sub>	0.1	mA
See fig. 2	I <sub>RM</sub>	M T <sub>J</sub> = 125 °C		1	ШA
Threshold voltage	V <sub>F(TO)</sub>	T <sub>1</sub> = T <sub>1</sub> maximum		0.52	V
Forward slope resistance	r <sub>t</sub>			mΩ	
Typical junction capacitance	CT	$V_R = 10 V_{DC}, T_J = 25 \text{ °C}, \text{ test signal} = 1 \text{ MHz}$ 38 pF		pF	
Typical series inductance	L <sub>S</sub>	Measured lead to lead 5 mm from package body 2.0 nH		nH	
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub> 10 000 V/µs		V/µs	

Note

<sup>(1)</sup> Pulse width = 300  $\mu$ s, duty cycle = 2 %

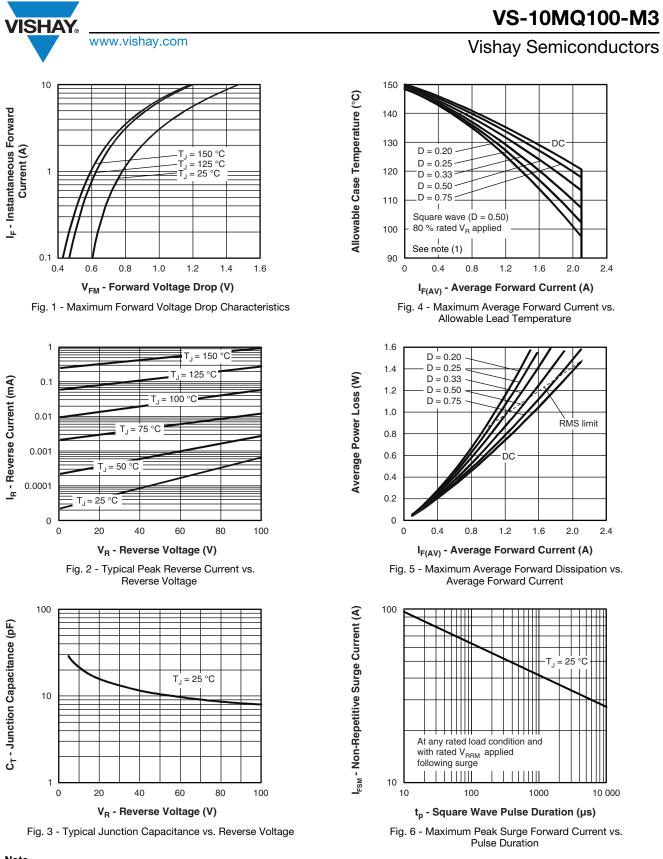
THERMAL - MECHANICAL SPECIFICATIONS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range	$T_{J}$ <sup>(1)</sup> , $T_{Stg}$		- 55 to 150	°C
Maximum thermal resistance, junction to ambient	R <sub>thJA</sub>	DC operation	80	°C/W
Approximate weight			0.07	g
Approximate weight			0.002	oz.
Marking device		Case style SMA (similar D-64)	1.	J

Note

 $\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}} \quad \text{thermal runaway condition for a diode on its own heatsink}$ (1)

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#### Note

<sup>(1)</sup> Formula used:  $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$ ;

Pd = Forward power loss = I<sub>F(AV)</sub> x V<sub>FM</sub> at (I<sub>F(AV)</sub>/D) (see fig. 6); Pd<sub>REV</sub> = Inverse power loss = V<sub>R1</sub> x I<sub>R</sub> (1 - D); I<sub>R</sub> at V<sub>R1</sub> = 80 % rated V<sub>R</sub>

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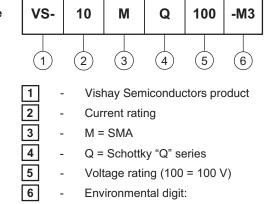
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## VS-10MQ100-M3

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#### ORDERING INFORMATION TABLE

Device code



-M3 = Halogen-free, RoHS compliant and terminations lead (Pb)-free

ORDERING INFORMATION (Example)					
PREFERRED P/N	PREFERRED PACKAGE CODE MINIMUM ORDER QUANTITY PACKAGING DESCRIPTION				
VS-10MQ100-M3/5AT	5AT	7500	13" diameter plastic tape and reel		

LINKS TO RELATED DOCUMENTS		
Dimensions www.vishay.com/doc?95400		
Part marking information	www.vishay.com/doc?95403	
Packaging information	www.vishay.com/doc?95404	

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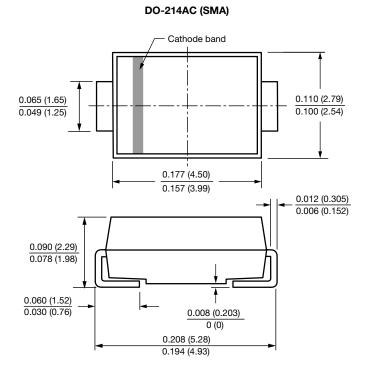


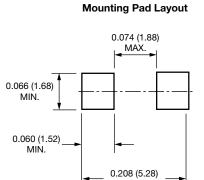
#### **Outline Dimensions**

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**SMA** 

#### **DIMENSIONS** in inches (millimeters)





REF.





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