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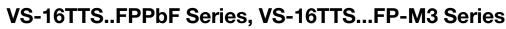
<u>Vishay Semiconductor/Diodes Division</u> <u>VS-16TTS08FP-M3</u>

For any questions, you can email us directly: sales@integrated-circuit.com

VISHAY

Distributor of Vishay Semiconductor/Diodes Division: Excellent Integrated System Limite

Datasheet of VS-16TTS08FP-M3 - SCR 800V 16A TO-220AB FP Contact us: sales@integrated-circuit.com Website: www.integrated-circuit.com

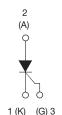


www.vishay.com

Vishay Semiconductors

High Voltage Phase Control Thyristor, 16 A





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PRODUCT SUMMARY				
Package	TO-220AB FP			
Diode variation	Single SCR			
I _{T(AV)}	10 A			
V_{DRM}/V_{RRM}	800 V, 1200 V			
V_{TM}	1.4 V			
I _{GT}	60 mA			
T,	- 40 °C to 125 °C			

FEATURES

- Designed and qualified for industrial level
- Fully isolated package (V_{INS} = 2500 V_{RMS})
- UL E78996 approved
- 125 °C max. operating junction temperature
- Material categorization: For definitions of compliance please see www.vishay.com/doc?99912





HALOGEN FREE

APPLICATIONS

• Typical usage is in input rectification crowbar (soft start) and AC switch in motor control, UPS, welding, and battery

DESCRIPTION

The VS-16TTS..FP... high voltage series of silicon controlled rectifiers are specifically designed for medium power switching and phase control applications. The glass passivation technology used has reliable operation up to 125 °C junction temperature.

OUTPUT CURRENT IN TYPICAL APPLICATIONS					
APPLICATIONS	SINGLE-PHASE BRIDGE	THREE-PHASE BRIDGE	UNITS		
Capacitive input filter T _A = 55 °C, T _J = 125 °C, common heatsink of 1 °C/W	13.5	17	А		

MAJOR RATINGS AND CHARACTERISTICS				
PARAMETER	TEST CONDITIONS	VALUES	UNITS	
I _{T(AV)}	Sinusoidal waveform	10	^	
I _{RMS}		16	Α	
V _{DRM} /V _{RRM}		800/1200	V	
I _{TSM}		200	А	
V _T	10 A, T _J = 25 °C	1.4	V	
dV/dt		500	V/µs	
dl/dt		150	A/µs	
T _J	Range	- 40 to 125	°C	

VOLTAGE RATINGS					
PART NUMBER	V _{RRM} , MAXIMUM PEAK REVERSE VOLTAGE V	V _{DRM} , MAXIMUM PEAK DIRECT VOLTAGE V	I _{RRM} /I _{DRM} AT 125 °C mA		
VS-16TTS08FPPbF, VS-16TTS08FP-M3	800	800	10		
VS-16TTS12FPPbF, VS-16TTS12FP-M3	1200	1200	10		

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PARAMETER	SYMBOL		TEST CONDITIONS	TYP.	MAX.	UNITS
Maximum average on-state current	I _{T(AV)}	$T_{\rm C} = 70 {\rm ^{\circ}C}, {\rm ^{\circ}}$	180° conduction, half sine wave	1	0	
Maximum RMS on-state current	I _{RMS}			1	6	Α
Maximum peak, one-cycle,	1	10 ms sine p	oulse, rated V _{RRM} applied	17	70	_ A
non-repetitive surge current	I _{TSM}	10 ms sine p	oulse, no voltage reapplied	20	00	
Maximum I ² t for fusing	I ² t	10 ms sine p	10 ms sine pulse, rated V _{RRM} applied		144	
	I-I	10 ms sine pulse, no voltage reapplied		200		A ² s
Maximum I ² √t for fusing	I ² √t	t = 0.1 to 10	t = 0.1 to 10 ms, no voltage reapplied		00	A²√s
Maximum on-state voltage drop	V_{TM}	10 A, T _J = 25 °C		1.	.4	V
On-state slope resistance	r _t	T., = 125 °C		24	1.0	mΩ
Threshold voltage	V _{T(TO)}	1J = 125 °C		1.	.1	V
Maximum rayaraa and direct laakaga ayrrant	1 /1	T _J = 25 °C	V - Patad V - A/	0.	.5	
Maximum reverse and direct leakage current	I _{RM} /I _{DM}	$T_{\rm J} = 125 ^{\circ}{\rm C}$ $V_{\rm R} = {\rm Rated} {\rm V}_{\rm RRM} / {\rm V}_{\rm DRM}$		1	0	
Holding current	I _H	Anode supply = 6 V, resistive load, initial I _T = 1 A 16TTS08FP, 16TTS12FP, T _J = 25 °C		150	mA	
Maximum latching current	ΙL	Anode supply = 6 V, resistive load, T _J = 25 °C		20	00	
Maximum rate of rise of off-state voltage	dV/dt	$T_J = T_J \text{ max., linear to } 80 \text{ %, } V_{DRM} = R_g - k = Open$ 500		00	V/µs	
Maximum rate of rise of turned-on current	dI/dt			15	50	A/μs

TRIGGERING				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum peak gate power	P_{GM}		8.0	w
Maximum average gate power	P _{G(AV)}		2.0] "
Maximum peak positive gate current	+ I _{GM}		1.5	Α
Maximum peak negative gate voltage	- V _{GM}		10	V
	I _{GT}	Anode supply = 6 V, resistive load, T _J = - 10 °C	90	mA
Maximum required DC gate current to trigger		Anode supply = 6 V, resistive load, T _J = 25 °C	60	
		Anode supply = 6 V, resistive load, T _J = 125 °C	35	
		Anode supply = 6 V, resistive load, T _J = - 10 °C	3.0	
Maximum required DC gate voltage to trigger	V _{GT}	Anode supply = 6 V, resistive load, T _J = 25 °C	2.0	
voltage to mager		Anode supply = 6 V, resistive load, T _J = 125 °C	1.0]
Maximum DC gate voltage not to trigger	V_{GD}	T = 105 °C V = Poted value	0.25	
Maximum DC gate current not to trigger	I _{GD}	T _J = 125 °C, V _{DRM} = Rated value		mA

SWITCHING				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Typical turn-on time	t _{gt}	T _J = 25 °C	0.9	
Typical reverse recovery time	t _{rr}	T _J = 125 °C	4	μs
Typical turn-off time	tq	1] = 125 0	110	

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THERMAL AND MECHANICAL SPECIFICATIONS					
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range		T_J , T_{Stg}		- 40 to 125	°C
Maximum thermal resistance, junction to case		R_{thJC}	DC operation	2.5	
Maximum thermal resistance, junction to ambient		R _{thJA}		62	°C/W
Typical thermal resistance, case to heatsink		R_{thCS}	Mounting surface, smooth and greased	0.5	
Approximate weight				2	g
Approximate weight				0.07	OZ.
Mounting torque minimum maximum	minimum			6 (5)	kgf · cm
			12 (10)	(lbf · in)	
Marking device		0	16TTS08FP		
			Case style TO-220AB FULL-PAK (94/V0)	16TTS12FP	

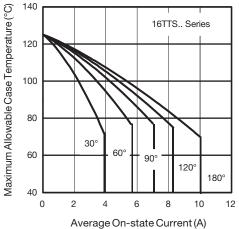


Fig. 1 - Current Rating Characteristics

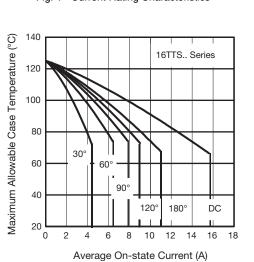


Fig. 2 - Current Rating Characteristics

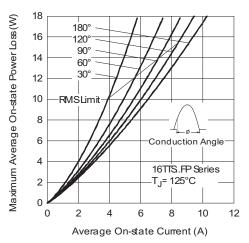


Fig. 3 - On-State Power Loss Characteristics

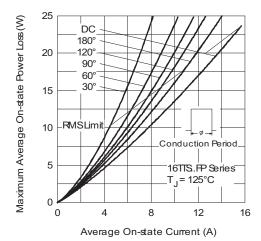


Fig. 4 - On-State Power Loss Characteristics

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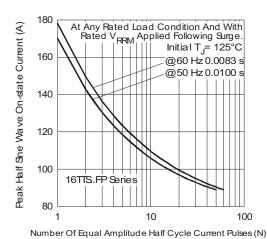


Fig. 5 - Maximum Non-Repetitive Surge Current

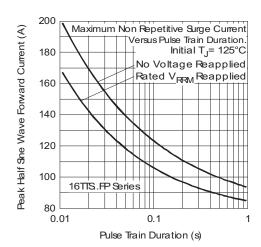
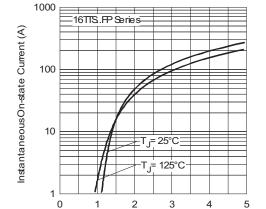
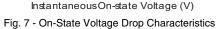


Fig. 6 - Maximum Non-Repetitive Surge Current





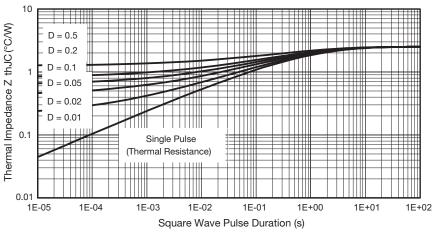


Fig. 8 - Thermal Impedance Z_{thJC} Characteristics

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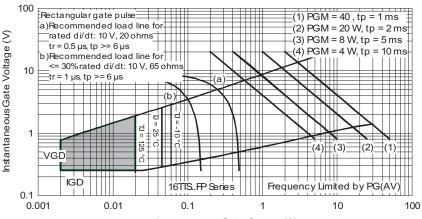
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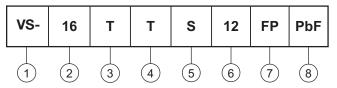
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Instantaneous Gate Current (A)
Fig. 9 - Gate Characteristics

ORDERING INFORMATION TABLE

Device code



1 - Vishay Semiconductors product

2 - Current rating, RMS value

Circuit configuration:

T = Single thyristor

4 - Package:

T = TO-220AB

5 - Type of silicon:

S = Converter grade

6 - Voltage code x 100 = V_{RRM} - 08 = 800 V 12 = 1200 V

7 - FULL-PAK

B - Environmental digit:

PbF = Lead (Pb)-free and RoHS compliant

-M3 = Halogen-free, RoHS compliant, and terminations lead (Pb)-free

ORDERING INFORMATION (Example)					
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION		
VS-16TTS08FPPbF	50	1000	Antistatic plastic tubes		
VS-16TTS08FP-M3	50	1000	Antistatic plastic tubes		
VS-16TTS12FPPbF	50	1000	Antistatic plastic tubes		
VS-16TTS12FP-M3	50	1000	Antistatic plastic tubes		

LINKS TO RELATED DOCUMENTS			
Dimensions		www.vishay.com/doc?95072	
Dort marking information	TO-220FP PbF	www.vishay.com/doc?95069	
Part marking information	TO-220FP -M3	www.vishay.com/doc?95456	

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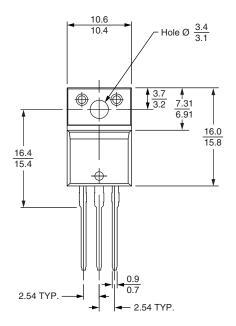
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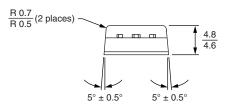


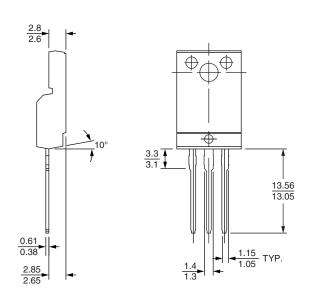
Outline Dimensions

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DIMENSIONS in millimeters







Lead assignments

Diodes

- 1. Anode/open
- 2. Cathode
- 3. Anode

Conforms to JEDEC outline TO-220 FULL-PAK



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