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Vishay Semiconductor/Diodes Division VSSB7L45-M3/52T

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VSSB7L45-M3 Vishay General Semiconductor

Surface Mount Trench MOS Barrier Schottky Rectifier

TMBS®

DO-214AA (SMB)

PRIMARY CHARACTERISTICS				
I _{F(AV)}	7.0 A			
V _{RRM}	45 V			
I _{FSM}	120 A			
V_F at I_F = 7.0 A (T_A = 125 °C)	0.40 V			
T _J max.	150 °C			
Package	DO-214AA (SMB)			
Diode variations	Single die			

FEATURES

- Low profile package
- Ideal for automated placement
- Trench MOS Schottky technology
- Low power losses, high efficiency
- Low forward voltage drop
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Material categorization: For definitions of compliance please see <u>www.vishay.com/doc?99912</u>

TYPICAL APPLICATIONS

For use in low voltage, high frequency inverters, freewheeling, DC/DC converters, and polarity protection applications.

MECHANICAL DATA

Case: DO-214AA (SMB)

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant, and commercial grade

Terminals: Matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

M3 suffix meets JESD 201 class 2 whisker test

Polarity: Color band denotes the cathode end

MAXIMUM RATINGS (T _A = 25 °C unless otherwise noted)				
PARAMETER	ETER SYMBOL		UNIT	
Device marking code		7L45		
Maximum repetitive peak reverse voltage	V _{RRM}	45	V	
Maximum DC forward current	Ι _Ε ⁽¹⁾	7.0	— A	
	I _F ⁽²⁾	3.8		
Peak forward surge current 10 ms single half sine-wave superimposed on rated load	I _{FSM}	I _{FSM} 120		
Operating junction and storage temperature range	T _J , T _{STG}	-40 to +150	°C	

Notes

⁽¹⁾ Mounted on 3 cm x 3 cm pad areas, 2 oz. PCB

⁽²⁾ Free air, mounted on recommended copper pad area

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RoHS

COMPLIANT

HALOGEN

FREE



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VSSB7L45-M3

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ELECTRICAL CHARACTERISTICS ($T_A = 25$ °C unless otherwise noted)						
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT
Instantaneous forward voltage	I _F = 3.5 A	T ₄ = 25 °C	V _F (1)	0.43	-	v
	$I_{F} = 7.0 \text{ A}$			0.49	0.57	
	I _F = 3.5 A	T _A = 125 °C		0.32	-	
	I _F = 7.0 A			0.40	0.48	
Reverse current	V _R = 45 V	T _A = 25 °C	$T_A = 25 \degree C$ $T_A = 125 \degree C$ $I_R^{(2)}$	-	1.6	mA
	$V_{R} = 43 V$ $T_{A} = 125 °C$	IR (-/	10	30	IIIA	
Typical junction capacitance	4.0 V, 1 MHz		CJ	1068	_	pF

Notes

⁽¹⁾ Pulse test: 300 µs pulse width, 1 % duty cycle

⁽²⁾ Pulse test: Pulse width \leq 5 ms

THERMAL CHARACTERISTICS ($T_A = 25 \text{ °C}$ unless otherwise specified)				
PARAMETER	SYMBOL VSSB7L45		UNIT	
Typical thermal resistance	R _{0JA} ⁽¹⁾	90	°C/W	
	R _{0JM} ⁽²⁾	10	0/10	

Notes

 $^{(1)}$ Free air, mounted on recommended PCB, 2 oz. pad area; thermal resistance $R_{\theta JA}$ - junction to ambient

 $^{(2)}$ Units mounted on 3 cm x 3 cm Aluminum, 2 oz. pad area; thermal resistance $R_{\theta JM}$ - junction to mount

ORDERING INFORMATION (Example)						
PREFERRED P/N UNIT WEIGHT (g) PREFERRED PACKAGE CODE		BASE QUANTITY	DELIVERY MODE			
VSSB7L45-M3/52T	0.096	52T	750	7" diameter plastic tape and reel		
VSSB7L45-M3/5BT	0.096	5BT	3200	13" diameter plastic tape and reel		

RATINGS AND CHARACTERISTICS CURVES (T_A = 25 °C unless otherwise noted)

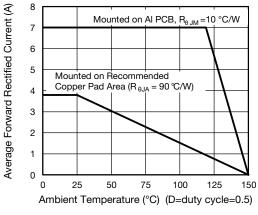


Fig. 1 - Maximum Forward Current Derating Curve

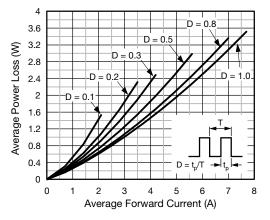


Fig. 2 - Forward Power Loss Characteristics

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10 000

Junction Capacitance (pF)

1000

100

100

10

1

0.01

Transient Thermal Impedance (°C/W)

0.1

1

Junction to Ambient

0.1

t-



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VSSB7L45-M3

T_I = 25 °C f = 1.0 MHz

 $V_{sig} = 50 \text{ mV}_{p}$

100

Vishay General Semiconductor

10

10

100

Reverse Voltage (V)

Fig. 5 - Typical Junction Capacitance

1

Pulse Duration (s)

Fig. 6 - Typical Transient Thermal Impedance

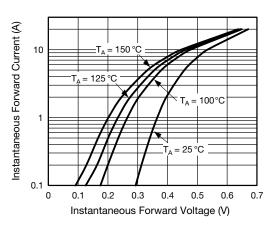


Fig. 3 - Typical Instantaneous Forward Characteristics

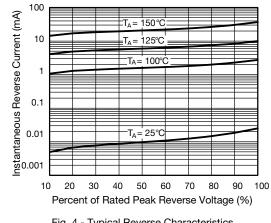
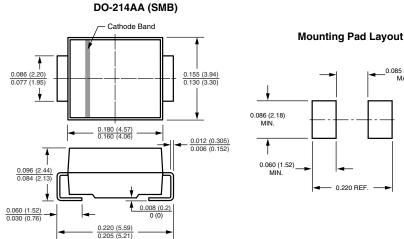


Fig. 4 - Typical Reverse Characteristics

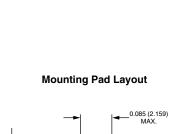


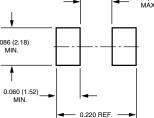


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