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[Diodes Incorporated](#)

[DDC144TH-7-F](#)

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DDC144TH

NPN PRE-BIASED SMALL SIGNAL DUAL SURFACE MOUNT TRANSISTOR

Features

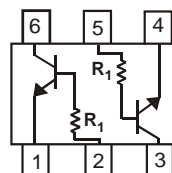
- Epitaxial Planar Die Construction
- Nominal 47kΩ Built-In Biasing Resistor
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free "Green" Device (Note 3)**

Mechanical Data

- Case: SOT563
- Case Material: Molded Plastic, "Green" Molding Compound; UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish – Matte Tin Plated Leads, Solderable per MIL-STD-202, Method 208 Ⓔ③
- Weight: 0.015 grams (Approximate)



Top View



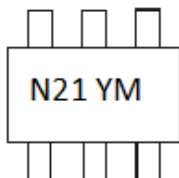
Device Schematic

Ordering Information (Notes 3 & 4)

Product	Compliance	Marking	Reel size (inches)	Tape width (mm)	Quantity per reel
DDC144TH-7-F	Standard	N21	7	8	3,000

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 4. For packaging details, go to our website at <http://www.diodes.com/products/packages.html>.

Marking Information



N21 = Product Type Marking Code
 YM = Date Code Marking
 Y = Year (ex: B = 2014)
 M = Month (ex: 9 = September)

Date Code Key

Year	2014	2015	2016	2017	2018	2019	2020	2021
Code	B	C	D	E	F	G	H	I

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

Absolute Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Supply Voltage	V _{CC}	50	V
Input Voltage	V _{IN}	-10 to +40	V
Output Current	I _{C(MAX)}	100	mA

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Power Dissipation (Notes 5 & 6)	P _D	150	mW
Thermal Resistance, Junction to Ambient Air (Note 5)	R _{θJA}	833	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
Collector-Base Breakdown Voltage	BV _{CB0}	50	—	—	V	I _C = 50μA
Collector-Emitter Breakdown Voltage	BV _{CEO}	50	—	—	V	I _C = 1mA
Emitter-Base Breakdown Voltage	BV _{EBO}	5	—	—	V	I _E = 50μA
Collector Cutoff Current	I _{CBO}	—	—	0.5	μA	V _{CB} = 50V
Emitter Cutoff Current	I _{EBO}	—	—	0.5	μA	V _{EB} = 4V
Collector-Emitter Saturation Voltage	V _{CE(sat)}	—	—	0.3	V	I _C /I _B = 1mA / 0.1mA
DC Current Transfer Ratio	h _{FE}	100	250	600	—	I _C = 1mA, V _{CE} = 5V
Input Resistor (R1)	R ₁	32.9	47Ω	61.1	kΩ	—
Gain-Bandwidth Product (Note 7)	f _T	—	250	—	MHZ	V _{CE} = 10V, I _E = -5mA, f = 100MHz

- Notes:
5. Mounted on FR4 PC Board with minimum recommended pad layout.
 6. 200mW per element must not be exceeded.
 7. Transistor - For Reference Only

Typical Curves (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

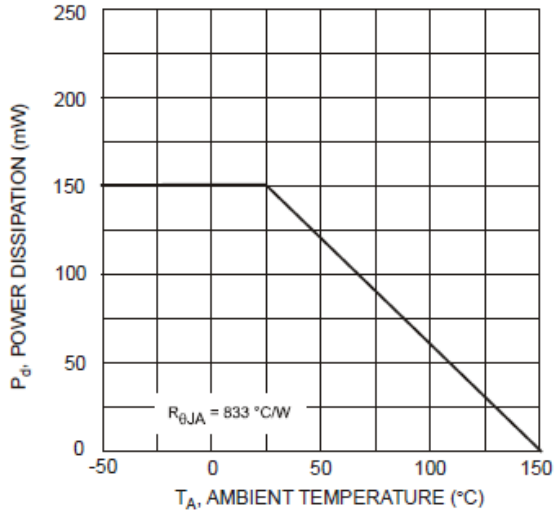


Fig. 1 Derating Curve

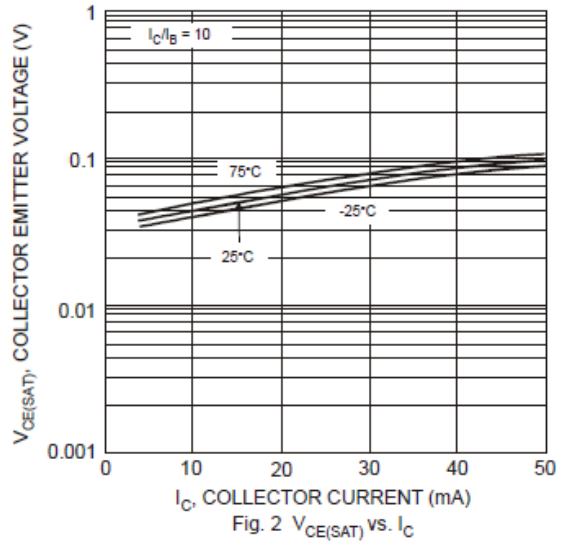


Fig. 2 $V_{CE(SAT)}$ vs. I_C

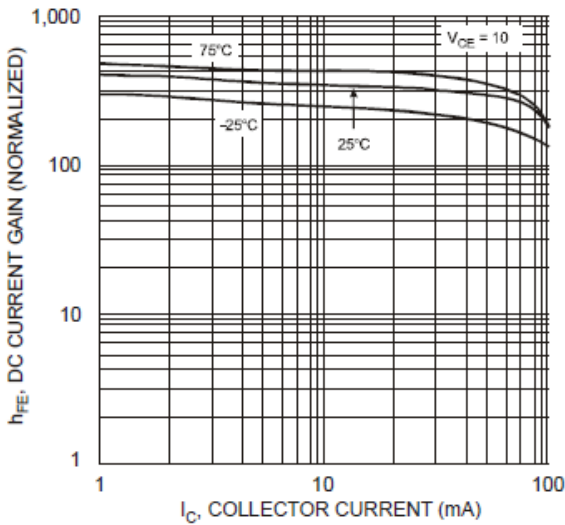


Fig. 3 DC Current Gain

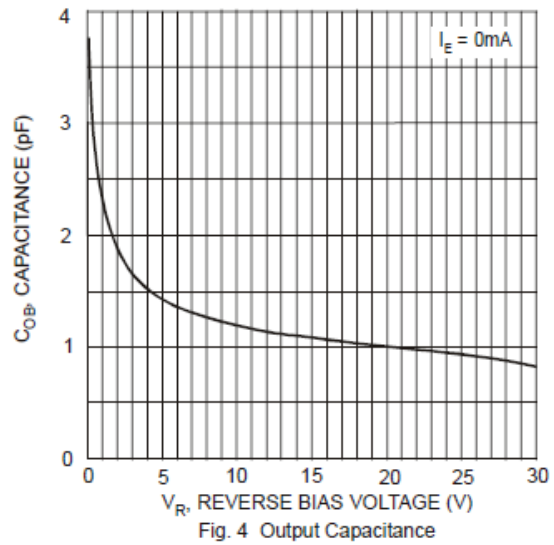


Fig. 4 Output Capacitance

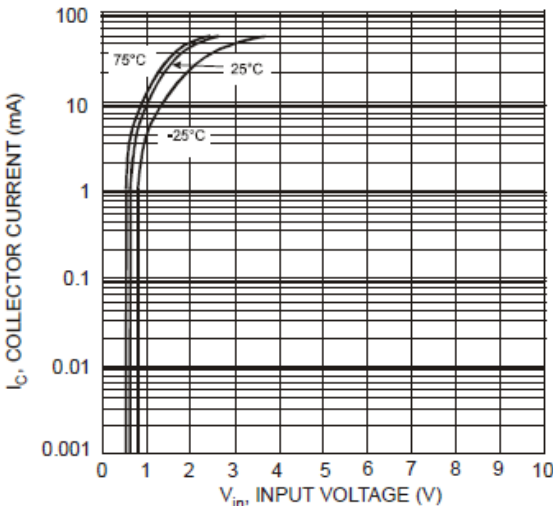


Fig. 5 Collector Current vs. Input Voltage

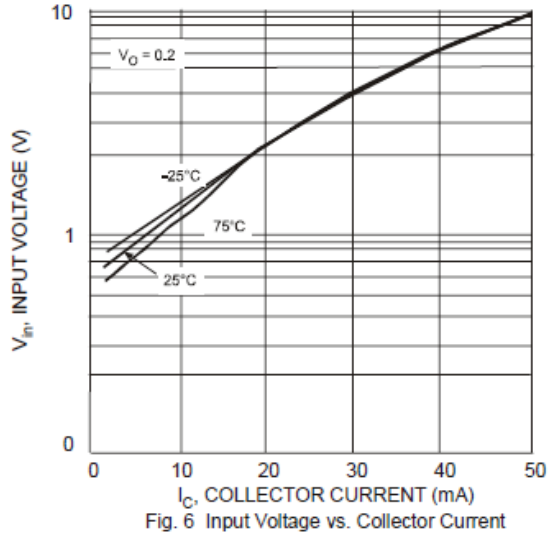
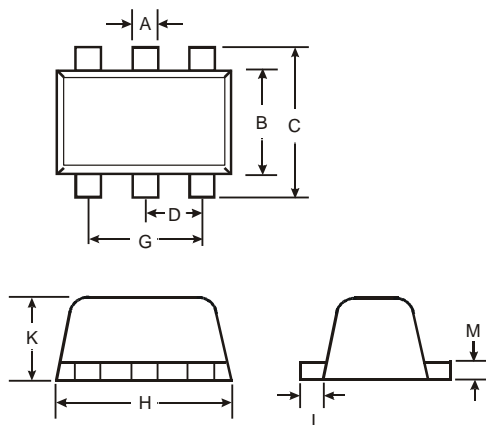


Fig. 6 Input Voltage vs. Collector Current

Package Outline Dimensions

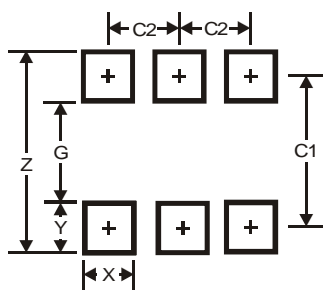
Please see AP02002 at <http://www.diodes.com/datasheets/ap02002.pdf> for the latest version.



SOT563			
Dim	Min	Max	Typ
A	0.15	0.30	0.20
B	1.10	1.25	1.20
C	1.55	1.70	1.60
D	-	-	0.50
G	0.90	1.10	1.00
H	1.50	1.70	1.60
K	0.55	0.60	0.60
L	0.10	0.30	0.20
M	0.10	0.18	0.11
All Dimensions in mm			

Suggested Pad Layout

Please see AP02001 at <http://www.diodes.com/datasheets/ap02001.pdf> for the latest version.



Dimensions	Value (in mm)
Z	2.2
G	1.2
X	0.375
Y	0.5
C1	1.7
C2	0.5

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