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[CP210-2N4416-CT20](#)

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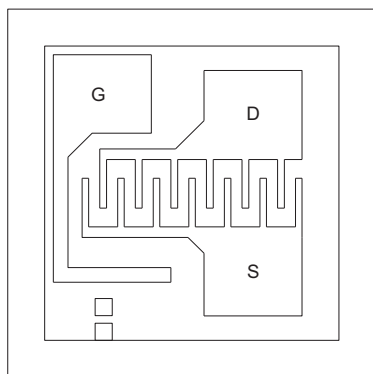
sales@integrated-circuit.com



www.centrasemi.com

CP210-2N4416 N-Channel JFET Die

The CP210-2N4416 is a silicon N-Channel JFET designed for VHF amplifier and mixer applications.



BACKSIDE GATE R1

MECHANICAL SPECIFICATIONS:

Die Size	15 x 15 MILS
Die Thickness	8.0 MILS
Drain Bonding Pad Size	3.2 x 4.0 MILS
Source Bonding Pad Size	3.2 x 4.0 MILS
Gate Bonding Pad Size	3.2 x 4.0 MILS
Top Side Metalization	Al – 30,000Å
Back Side Metalization	Au – 6,000Å
Scribe Alley Width	3.0 MILS
Wafer Diameter	5 INCHES
Gross Die Per Wafer	72,000

MAXIMUM RATINGS: (T_A=25°C)

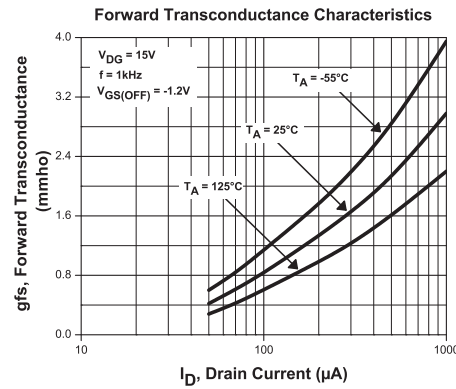
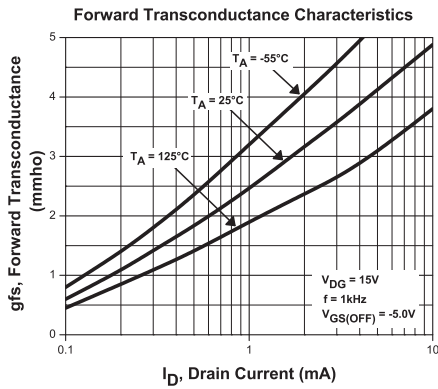
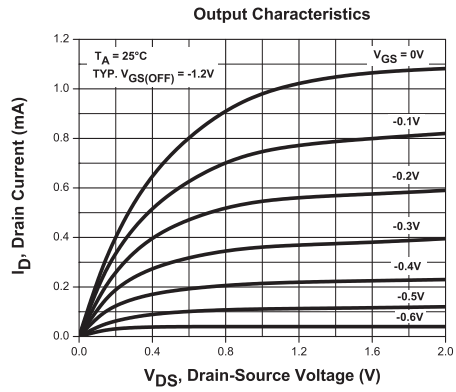
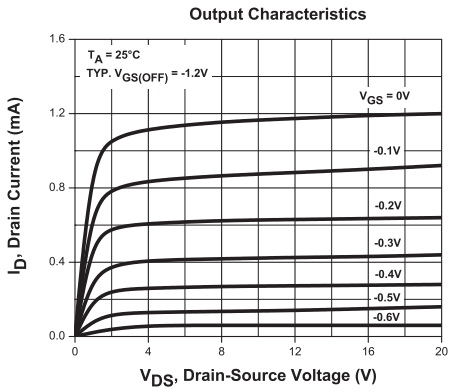
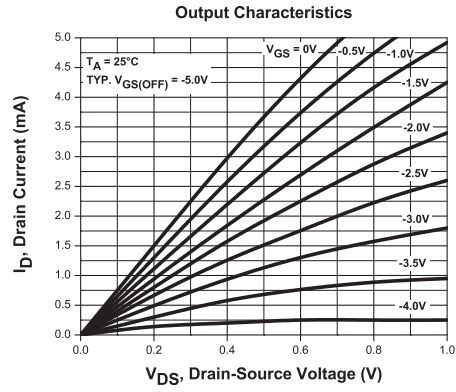
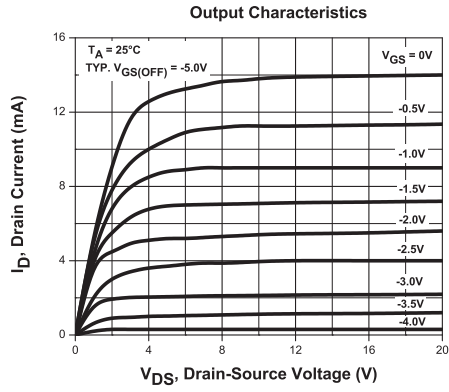
	SYMBOL		UNITS
Gate-Drain Voltage	V _{GD}	30	V
Gate-Source Voltage	V _{GS}	30	V
Drain-Source Voltage	V _{DS}	30	V
Gate Current	I _G	10	mA
Operating and Storage Junction Temperature	T _J , T _{stg}	-65 to +150	°C

ELECTRICAL CHARACTERISTICS: (T_A=25°C)

SYMBOL	TEST CONDITIONS	MIN	MAX	UNITS
I _{GSS}	V _{GS} =20V, V _{DS} =0		0.1	nA
I _{DSS}	V _{DS} =20V, V _{GS} =0	5.0	15	mA
BV _{GSS}	I _G =1.0μA	30		V
V _{GS(OFF)}	V _{DS} =15V, I _D =1.0nA		6.0	V
g _{FS}	V _{DS} =15V, V _{GS} =0, f=1.0kHz	4.5	7.5	mS
g _{OS}	V _{DS} =15V, V _{GS} =0, f=1.0kHz		50	μS
C _{rss}	V _{DS} =15V, V _{GS} =0, f=1.0MHz		1.0	pF
C _{iss}	V _{DS} =15V, V _{GS} =0, f=1.0MHz		4.0	pF
C _{oss}	V _{DS} =15V, V _{GS} =0, f=1.0MHz		2.0	pF
NF	V _{DS} =15V, I _D =5.0mA, R _G =1.0kΩ, f=100MHz		2.0	dB

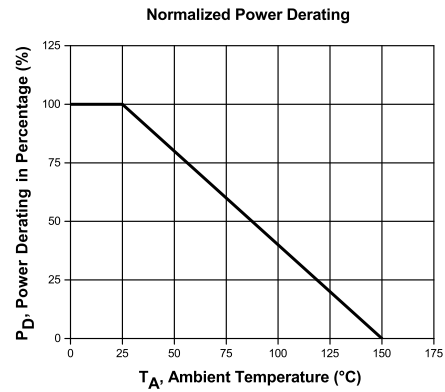
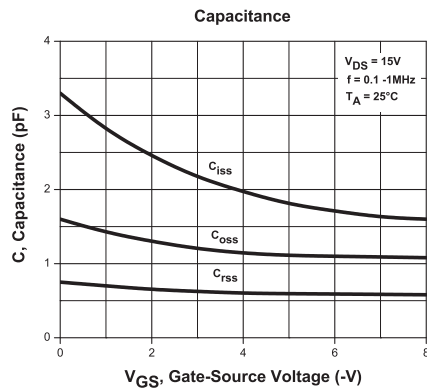
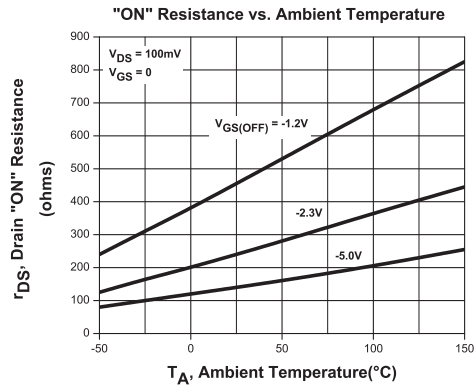
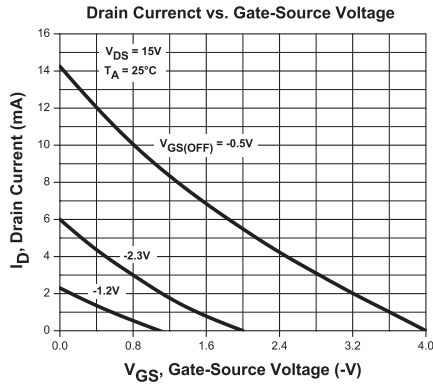
CP210-2N4416

Typical Electrical Characteristics



CP210-2N4416

Typical Electrical Characteristics



R1 (9-February 2016)

OUTSTANDING SUPPORT AND SUPERIOR SERVICES



PRODUCT SUPPORT

Central's operations team provides the highest level of support to insure product is delivered on-time.

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- PbSn plating options
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- Application notes
- Application and design sample kits
- Custom product and package development

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