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<u>Vishay Semiconductor/Diodes Division</u> <u>VS-30MQ040HM3/5AT</u>

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Datasheet of VS-30MQ040HM3/5AT - DIODE SCHOTTKY 30A SMA

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VS-30MQ040HM3

RoHS

COMPLIANT

HALOGEN

FREE

Vishay Semiconductors

High Performance Schottky Rectifier, 3 A



RODUCT SUMMARY	
Package	DO-214AC (

PRODUCT SUMMARY				
Package	DO-214AC (SMA)			
I _{F(AV)}	3 A			
V _R	40 V			
V _F at I _F	0.46 V			
I _{RM}	20 mA at 125 °C			
T _J max.	150 °C			
Diode variation	Single die			
E _{AS}	6.0 mJ			

FEATURES

- Low forward voltage drop
- · Guard ring for enhanced ruggedness and long term reliability
- · Small footprint, surface mountable
- High frequency operation
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Meets JESD 201 class 2 whisker test
- AEC-Q101 qualified
- · Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

APPLICATIONS

- Switching power supplies
- Meter protection
- · Reverse protection for power input to PC board circuits
- Battery isolation and charging
- · Low threshold voltage diode
- Freewheeling or by-pass diode
- · Low voltage clamp

DESCRIPTION

The VS-30MQ040HM3 Schottky rectifier is designed to be used for low power applications where a reverse voltage of 40 V is encountered and surface mountable is required.

MAJOR RATINGS AND CHARACTERISTICS				
SYMBOL	CHARACTERISTICS	VALUES	UNITS	
I _{F(AV)}	Rectangular waveform	3	A	
V_{RRM}		40	V	
I _{FSM}	t _p = 5 μs sine	330	A	
V _F	2 A _{pk} , T _J = 125 °C	0.43	V	
TJ	Range	-40 to +150	°C	

VOLTAGE RATINGS					
PARAMETER	SYMBOL	VS-30MQ040HM3	UNITS		
Maximum DC reverse voltage	V _R	40	V		
Maximum working peak reverse voltage	V_{RWM}	40	V		

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current See fig. 4	I _{F(AV)}	50 % duty cycle at T_L = 89 °C, re On PC board 9 mm ² island (0.013 mm thick copper pad area	· ·	3	А
Maximum peak one cycle non-repetitive surge current		5 μs sine or 3 μs rect. pulse	Following any rated load condition and with	330	Α
See fig. 6	IFSM	10 ms sine or 6 ms rect. pulse	rated V _{RRM} applied	140	A
Non-repetitive avalanche energy	E _{AS}	$T_{J} = 25 ^{\circ}\text{C}, I_{AS} = 1 \text{A}, L = 12 \text{mH}$		6.0	mJ
Repetitive avalanche current	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T_J maximum $V_A = 1.5 \times V_B$ typical		1.0	Α

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VS-30MQ040HM3

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ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum forward voltage drop		1 A	T _J = 25 °C	0.42	V
	V _{FM} ⁽¹⁾	3 A		0.51	
See fig. 1	V _{FM} (')	1 A	T _{.1} = 125 °C	0.34	V
		3 A	IJ = 125 °C	0.46	
Maximum reverse leakage current		T _J = 25 °C	V _R = Rated V _R	0.5	mA
See fig. 2	IRM	T _J = 125 °C		20	
Threshold voltage	V _{F(TO)}	T _{.I} = T _{.I} maximum		0.26	V
Forward slope resistance	r _t			64.6	mΩ
Typical junction capacitance	C _T	$V_R = 10 V_{DC}$, $T_J = 25 °C$, test signal = 1 MHz		134	pF
Typical series inductance	L _S	Measured lead to lead 5 mm from package body 2.0		nH	
Maximum voltage rate of change	dV/dt	Rated V _R 10 000 V/		V/µs	

Note

⁽¹⁾ Pulse width = 300 μ s, duty cycle = 2 %

THERMAL - MECHANICAL SPECIFICATIONS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range	T _J ⁽¹⁾ , T _{Stg}		-40 to +150	°C
Maximum thermal resistance, junction to ambient	R _{thJA}	DC operation	80	°C/W
Approximate weight			0.07	g
Approximate weight			0.002	OZ.
Marking device		Case style SMA (similar D-64)	3	F

Note

$$^{(1)} \quad \frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}} \quad \text{thermal runaway condition for a diode on its own heatsink}$$

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VS-30MQ040HM3

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I_F - Instantaneous Forward Current (A)

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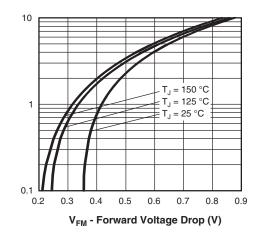


Fig. 1 - Maximum Forward Voltage Drop Characteristics

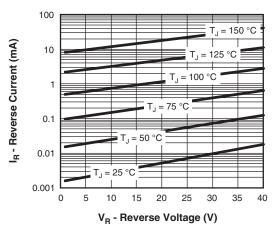


Fig. 2 - Typical Peak Reverse Current vs. Reverse Voltage

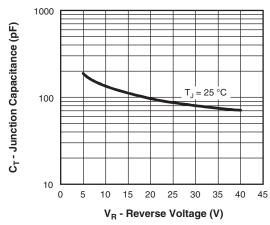


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

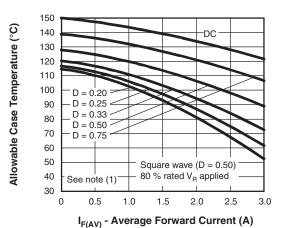


Fig. 4 - Maximum Average Forward Current vs. Allowable Lead Temperature

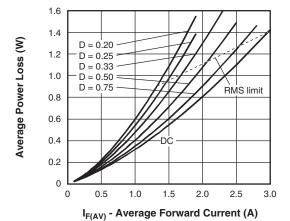


Fig. 5 - Maximum Average Forward Dissipation vs. Average Forward Current

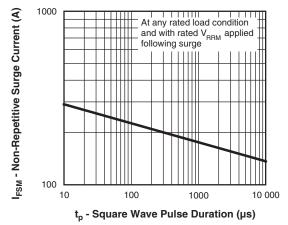


Fig. 6 - Maximum Peak Surge Forward Current vs. Pulse Duration

Note

Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{th,JC}$; $Pd = Forward power loss = I_{F(AV)} \times V_{FM}$ at $(I_{F(AV)}/D)$ (see fig. 6); $Pd_{REV} = Inverse power loss = V_{R1} \times I_R$ (1 - D); I_R at $V_{R1} = 80$ % rated V_R

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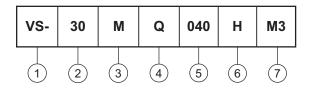


VS-30MQ040HM3

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ORDERING INFORMATION TABLE

Device code



- Vishay Semiconductors product
- 2 Current rating
- M = SMA
- 4 Q = Schottky "Q" series
- 5 Voltage rating (040 = 40 V)
- 6 H = AEC-Q101 qualified
- 7 Environmental digit:

M3 = Halogen-free, RoHS-compliant and terminations lead (Pb)-free

ORDERING INFORMATION (Example)				
PREFERRED P/N PREFERRED PACKAGE CODE MINIMUM ORDER QUANTITY PACKAGING DESCRIPTION				
VS-30MQ040HM3/5AT	5AT	7500	13" diameter plastic tape and reel	

LINKS TO RELATED DOCUMENTS		
Dimensions <u>www.vishay.com/doc?95400</u>		
Part marking information	www.vishay.com/doc?95403	
Packaging information	www.vishay.com/doc?95404	

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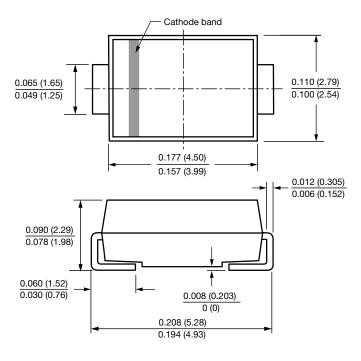
Outline Dimensions

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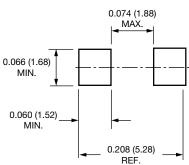
SMA

DIMENSIONS in inches (millimeters)

DO-214AC (SMA)



Mounting Pad Layout





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