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<u>Vishay Semiconductor/Diodes Division</u> <u>V20D202C-M3/I</u>

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Distributor of Vishay Semiconductor/Diodes Division: Excellent Integrated System Limite

Datasheet of V20D202C-M3/I - DIODE ARRAY SCHOTTKY 200V SMPD

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V20D202C

RoHS

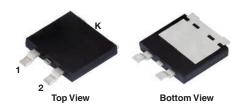
HALOGEN

FREE

Vishay General Semiconductor

Dual High-Voltage Trench MOS Barrier Schottky Rectifier

TMBS® eSMP® Series TO-263AC (SMPD)



V20D202C



PRIMARY CHARACTERISTICS				
I _{F(AV)}	2 x 10.0 A			
V_{RRM}	200 V			
I _{FSM}	150 A			
V _F at I _F = 10.0 A (T _A = 125 °C)	0.68 V			
T _J max.	175 °C			
Package	TO-263AC (SMPD)			
Diode variations	Dual common cathode			

FEATURES

- Trench MOS Schottky technology generation 2
- Very low profile typical height of 1.7 mm
- · Ideal for automated placement
- Low forward voltage drop, low power losses
- High efficiency operation
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified available:
 - Automotive ordering code: base P/NHM3
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

TYPICAL APPLICATIONS

For use in high frequency DC/DC converters, switching power supplies, freewheeling diodes, OR-ing diode, and reverse battery protection.

MECHANICAL DATA

Case: TO-263AC (SMPD)

Molding compound meets UL 94 V-0 flammability rating

Base P/N-M3 - halogen-free, RoHS-compliant

Base P/NHM3 - halogen-free, RoHS-compliant, and

AEC-Q101 qualified

Terminals: Matte tin plated leads, solderable per

J-STD-002 and JESD 22-B102

M3 and HM3 suffix meets JESD 201 class 2 whisker test

Polarity: As marked

MAXIMUM RATINGS (T _A = 25 °C unless otherwise noted)				
PARAMETER	SYMBOL	V20D202C	UNIT	
Maximum repetitive peak reverse voltage	V _{RRM}	200	V	
Maximum average forward rectified current per device	I _{F(AV)}	20	Α	
(fig. 1) per diode		10		
Maximum DC reverse voltage	V _{DC}	160	V	
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load	I _{FSM}	150	А	
Voltage rate of change (rated V _R)	dV/dt	10 000	V/µs	
Operating junction and storage temperature range	T _J , T _{STG}	-40 to +175	°C	

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ELECTRICAL CHARACTERISTICS (T _A = 25 °C unless otherwise noted)						
PARAMETER	TEST CONDITIONS SY		SYMBOL	TYP.	MAX.	UNIT
Instantaneous forward voltage per diode	I _F = 5 A	T _A = 25 °C	V _F ⁽¹⁾	0.75	-	. V
	I _F = 10 A			0.83	0.9	
	I _F = 5 A	T _A = 125 °C		0.6	-	
	I _F = 10 A			0.68	0.76	
Reverse current at rated V _R per diode	V _R = 160 V	T _A = 25 °C	I _R ⁽²⁾	0.8	-	μΑ
		T _A = 125 °C		1	-	mA
	V 200 V	T _A = 25 °C		-	150	μΑ
	V _R = 200 V	T _A = 125 °C		2.5	10	mA

Notes

(1) Pulse test: 300 µs pulse width, 1 % duty cycle

(2) Pulse test: Pulse width $\leq 5 \text{ ms}$

THERMAL CHARACTERISTICS (T _A = 25 °C unless otherwise noted)				
PARAMETER		SYMBOL	V20D202C	UNIT
Typical thermal resistance	per diode	- R _{θJC} - R _{θJA} (1)(2)	2.8	°C/W
	per device		1.5	
	per device		58	

Notes

(1) The heat generated must be less than the thermal conductivity from junction-to-ambient: dP_D/dT_J < 1/R_{θJA} - junction-to -mount

⁽²⁾ Free air, without heatsink

ORDERING INFORMATION (Example)					
PACKAGE	PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE
TO-263AC (SMPD)	V20D202C-M3/I	0.55	I	2000/reel	13" diameter plastic tape and reel
TO-263AC (SMPD)	V20D202CHM3/I (1)	0.55	I	2000/reel	13" diameter plastic tape and reel

Note

(1) AEC-Q101 qualified

RATINGS AND CHARACTERISTICS CURVES (T_A = 25 °C unless otherwise noted)

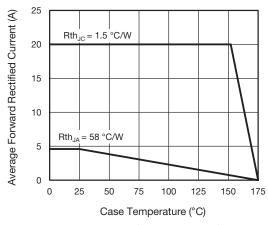


Fig. 1 - Forward Current Derating Curve

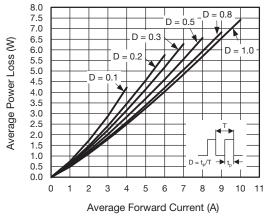


Fig. 2 - Forward Power Loss Characteristics

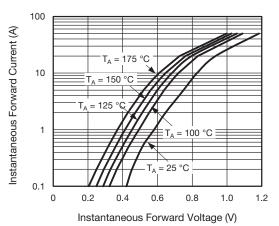
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Fig. 3 - Typical Instantaneous Forward Characteristics

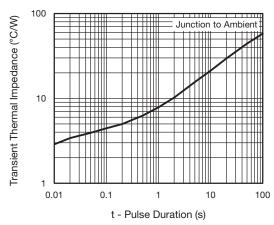


Fig. 6 - Typical Transient Thermal Impedance

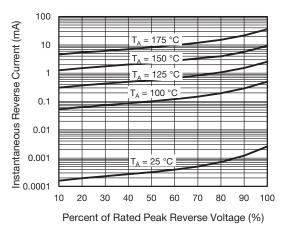


Fig. 4 - Typical Reverse Characteristics

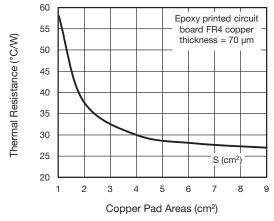


Fig. 7 - Thermal Resistance Junction-to-Ambient vs. Copper Pad Areas

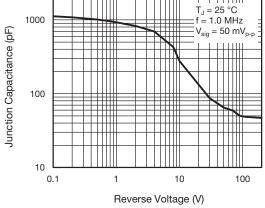


Fig. 5 - Typical Junction Capacitance

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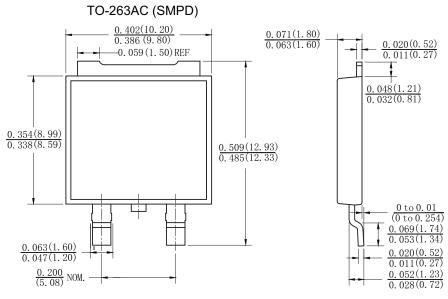
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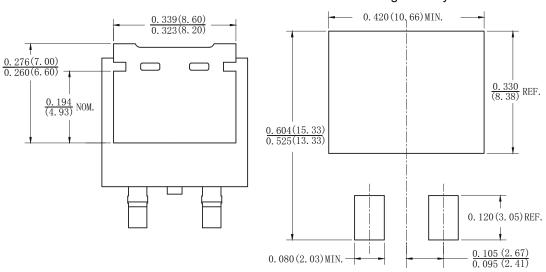
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PACKAGE OUTLINE DIMENSIONS in inches (millimeters)



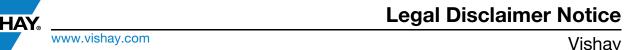
Mounting Pad Layout





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