

Excellent Integrated System Limited

Stocking Distributor

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[Cardinal Components](#)

[CV622E9](#)

For any questions, you can email us directly:

sales@integrated-circuit.com

CARDINAL COMPONENTS

Telecom OC48 VCXO

- 622.08 MHz Osc
- Low Phase Noise
- PECL Output

Applications

- SDH / SONET
- Digital Switching
- Test Equipment
- Cellular Telephony
- Land Mobile Radio

Series

CV622E

Specifications:	Min	Typ	Max	Unit
Frequency Range:		622.08		MHz
Available Stability Options:	-30		30	ppm
Supply Voltage:	3.135	3.3	3.465	V
Operating Temperature Range Options:	-10		85	°C
Storage Temperature:	-55		125	°C
Duty Cycle:	45		55	%
Start-Up Time:		3	10	mS
Aging (PPM/1st Year): Ta=25C, Vdd=3.3V			±5	
Static Discharge Voltage Mil-Std 883, method 3015	2000			V
Supply Current			100	mA
Short Circuit Current		± 50		mA
Jitter				
Period, RMS		4		pS
Period, Peak to Peak		25		pS
Accumulated, RMS		7		pS
Peak to Peak		45		pS
Phase Noise @1KHz Offset		-125		dBc/Hz
Output Voltage				
Voh	Vdd - 1.025			V
Vol			Vdd - 1.62	V
RI = 50Ω – (Vdd - 2V)				
Rise / Fall Time				
Clock Rise (tr)		0.6	1.5	nS
Clock Fall (tf)		0.5	1.5	nS
Vcon Modulation BW	25	0V ≤ Vcon ≤ 3.3V -3dB		KHz
Pull Range	-190		190	ppm
Linearity	5		10	%
Frequency Tuning Range		115		ppm/V

Output Level: PECL

Packaging: Tape & Reel

Notes: Extended exposure exceeding maximum ratings may cause permanent damage and affect reliability. Operation outside of specifications is not supported.

Cardinal Components, Inc., 155 Rt. 46 W, Wayne, NJ. 07470 TEL: (973)785-1333 FAX: (973)785-0053

http://www.cardinalxtal.com

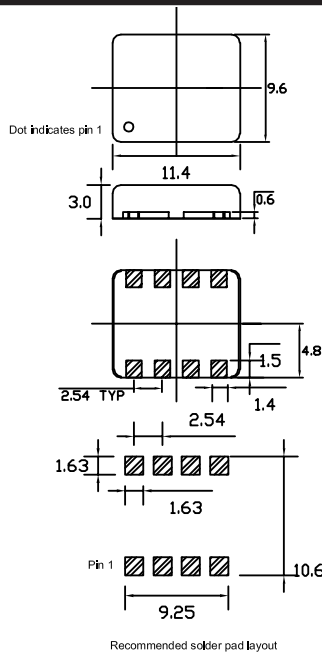
E-Mail: cardinal@cardinalxtal.com

Specifications subject to change without notice. Check website for latest updates



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CV622E



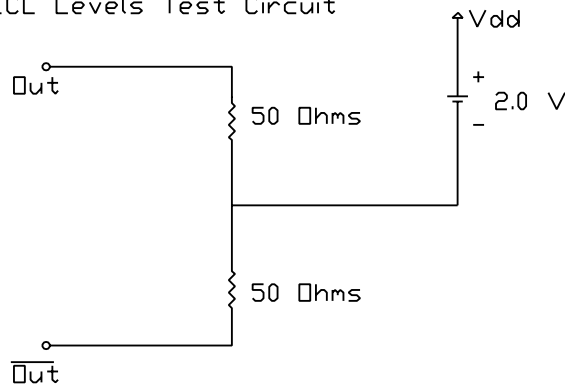
PIN	FUNCTION
1	OE
2	V _{CONTROL}
3	N/C
4	GND
5	PECL +
6	PECL -
7	N/C
8	V _{dd}

DIMENSIONS ARE IN MM

Recommended solder pad layout

LEVELS TEST CIRCUIT

PECL Levels Test Circuit



OUTPUT SKEW

PECL Output Skew

