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ZXMN6A09G

60V N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

$V_{(BR)DSS}$	$R_{DS(ON)}$	I_D $T_A = +25^\circ C$
60V	0.04Ω @ $V_{GS} = 10V$	7.5A
	0.06Ω @ $V_{GS} = 4.5V$	6.2A

Description

This new generation trench MOSFET features a unique structure combining the benefits of low on-resistance and fast switching, making it ideal for high efficiency power management applications.

Applications

- DC-DC Converters
- Power Management Functions
- Disconnect Switches
- Motor Control

Features and Benefits

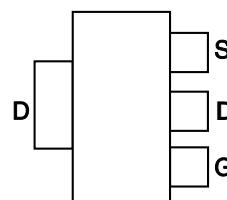
- High Voltage
- Low On-resistance
- Fast Switching Speed
- Low Gate Drive
- Low Threshold
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

Mechanical Data

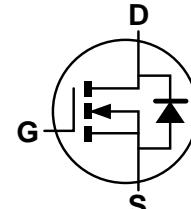
- Case: SOT223
- Case Material: Molded Plastic, "Green" Molding Compound; UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Matte Tin Finish (e3)
- Weight: 0.112 grams (Approximate)



Top View



Pin Out - Top



Equivalent Circuit

Ordering Information (Note 4)

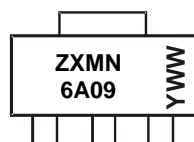
Part Number	Marking	Reel size (inches)	Tape width (mm)	Quantity per reel
ZXMN6A09GTA	ZXMN6A09	7	12	1,000

Notes:

- EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. All applicable RoHS exemptions applied.
- See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- For packaging details, go to our website at <http://www.diodes.com/products/packages.html>.

Marking Information

SOT223



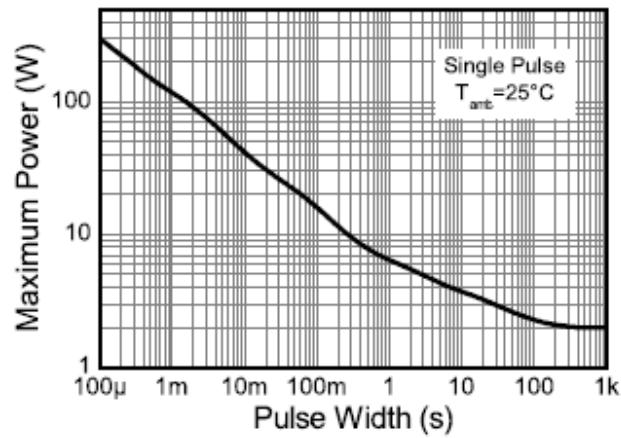
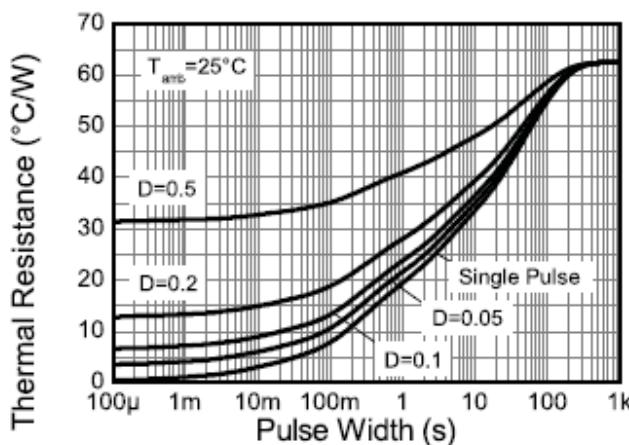
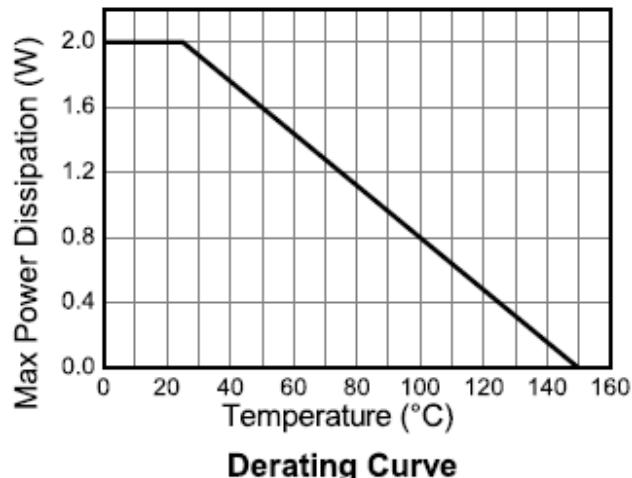
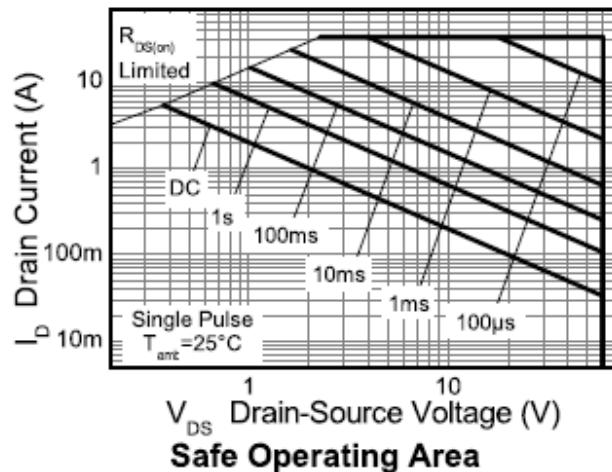
ZXMN6A09 = Product Type Marking Code
 YWW = Date Code Marking
 Y or Y = Last Digit of Year (ex: 5 = 2015)
 WW or WW = Week Code (01~53)

Maximum Ratings (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Drain-Source Voltage	V_{DSS}	60	V
Gate-Source Voltage	V_{GSS}	± 20	V
Continuous Drain Current @ $V_{GS} = 10\text{V}$; $T_A = +25^\circ\text{C}$ (Note 6) @ $V_{GS} = 10\text{V}$; $T_A = +70^\circ\text{C}$ (Note 6) @ $V_{GS} = 10\text{V}$; $T_A = +25^\circ\text{C}$ (Note 5)	I_D	7.5 6 5.4	A
Pulsed Drain Current (Note 7)	I_{DM}	33	A
Continuous Source Current (Body Diode) (Note 6)	I_S	3.5	A
Pulsed Source Current (Body Diode) (Note 7)	I_{SM}	33	A

Thermal Characteristics (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Power Dissipation at $T_A = +25^\circ\text{C}$ (Note 5)	P_D	2.0	W
Linear Derating Factor		16	$\text{mW}/^\circ\text{C}$
Power Dissipation at $T_A = +25^\circ\text{C}$ (Note 6)	P_D	3.9	W
Linear Derating Factor		31	$\text{mW}/^\circ\text{C}$
Thermal Resistance, Junction to Ambient (Note 5)	R_{JJA}	62.5	$^\circ\text{C}/\text{W}$
Thermal Resistance, Junction to Ambient (Note 6)	R_{JJA}	32.2	$^\circ\text{C}/\text{W}$
Operating and Storage Temperature Range	T_J, T_{STG}	-55 to +150	$^\circ\text{C}$





ZXMN6A09G

Electrical Characteristics (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV_{DSS}	60	-	-	V	$\text{V}_{\text{GS}} = 0\text{V}$, $\text{I}_D = 250\mu\text{A}$
Zero Gate Voltage Drain Current	I_{DSS}	-	-	1	μA	$\text{V}_{\text{DS}} = 60\text{V}$, $\text{V}_{\text{GS}} = 0\text{V}$
Gate-Source Leakage	I_{GSS}	-	-	100	nA	$\text{V}_{\text{GS}} = \pm 20\text{V}$, $\text{V}_{\text{DS}} = 0\text{V}$
ON CHARACTERISTICS						
Gate Threshold Voltage	$\text{V}_{\text{GS(TH)}}$	1	-	3	V	$\text{V}_{\text{DS}} = \text{V}_{\text{GS}}$, $\text{I}_D = 250\mu\text{A}$
Static Drain-Source On-Resistance (Note 8)	$\text{R}_{\text{DS(ON)}}$	-	-	0.04	Ω	$\text{V}_{\text{GS}} = 10\text{V}$, $\text{I}_D = 8.2\text{A}$
		-	-	0.06	Ω	$\text{V}_{\text{GS}} = 10\text{V}$, $\text{I}_D = 7.4\text{A}$
Forward Transconductance (Notes 8 & 10)	g_{fs}	-	15	-	mS	$\text{V}_{\text{DS}} = 15\text{V}$, $\text{I}_D = 8.2\text{A}$
Diode Forward Voltage (Note 8)	V_{SD}	-	0.85	0.95	V	$\text{I}_S = 6.6\text{A}$, $\text{V}_{\text{GS}} = 0\text{V}$, $\text{T}_J = +25^\circ\text{C}$
DYNAMIC CHARACTERISTICS						
Input Capacitance (Note 10)	C_{iss}	-	1407	-	pF	$\text{V}_{\text{DS}} = 40\text{V}$, $\text{V}_{\text{GS}} = 0\text{V}$, $f = 1.0\text{MHz}$
Output Capacitance (Note 10)	C_{oss}	-	121	-	pF	
Reverse Transfer Capacitance (Note 10)	C_{rss}	-	59	-	pF	$\text{V}_{\text{DS}} = 15\text{V}$ $\text{I}_D = 3.5\text{A}$
Total Gate Charge (Notes 9 & 10) $\text{V}_{\text{GS}} = 5\text{V}$	Q_g	-	12.4	-	nC	
Total Gate Charge (Notes 9 & 10) $\text{V}_{\text{GS}} = 10\text{V}$	Q_g	-	24.2	-	nC	$\text{V}_{\text{DD}} = 15\text{V}$, $\text{I}_D = 3.5\text{A}$, $\text{V}_{\text{GS}} = 5\text{V}$
Gate-Source Charge (Notes 9 & 10)	Q_{gs}	-	5.2	-	nC	
Gate-Drain Charge (Notes 9 & 10)	Q_{gd}	-	3.5	-	nC	$\text{I}_F = 3.5\text{A}$, $\text{di}/\text{dt} = 100\text{A}/\mu\text{s}$, $\text{T}_J = +25^\circ\text{C}$
Turn-On Delay Time (Notes 9 & 10)	$\text{t}_{\text{D(ON)}}$	-	4.9	-	ns	
Turn-On Rise Time (Note 9 & 10)	t_R	-	5.0	-	ns	$\text{V}_{\text{DD}} = 15\text{V}$, $\text{I}_D = 3.5\text{A}$, $\text{V}_{\text{GS}} = 5\text{V}$
Turn-Off Delay Time (Notes 9 & 10)	$\text{t}_{\text{D(OFF)}}$	-	25.3	-	ns	
Turn-Off Fall Time (Notes 9 & 10)	t_F	-	4.6	-	ns	$\text{I}_F = 3.5\text{A}$, $\text{di}/\text{dt} = 100\text{A}/\mu\text{s}$, $\text{T}_J = +25^\circ\text{C}$
Reverse Recovery Time (Note 10)	t_{RR}	-	26.3	-	ns	
Reverse Recovery Charge (Note 10)	Q_{rr}	-	26.6	-	nC	$\text{I}_F = 3.5\text{A}$, $\text{di}/\text{dt} = 100\text{A}/\mu\text{s}$, $\text{T}_J = +25^\circ\text{C}$

Notes: 5. For a device surface mounted on 25mm x 25mm FR4 PCB with high coverage of single sided 1oz copper, in still air conditions.

6. For a device surface mounted on FR4 PCB measured at $t \leq 10$ secs.

7. Repetitive rating 25mm x 25mm FR4 PCB, $D=0.02$ pulse width=300 μs - pulse width limited by maximum junction temperature.

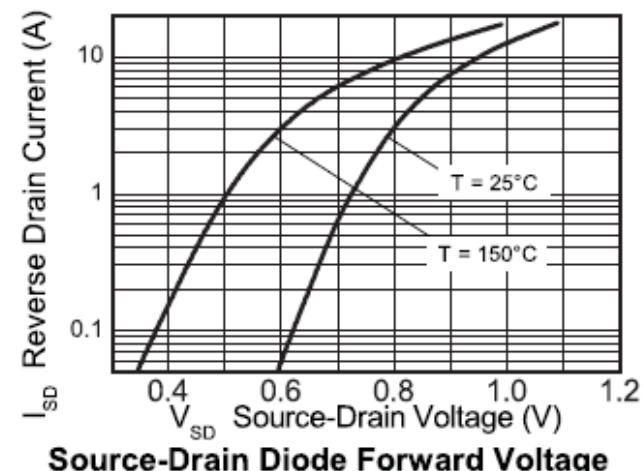
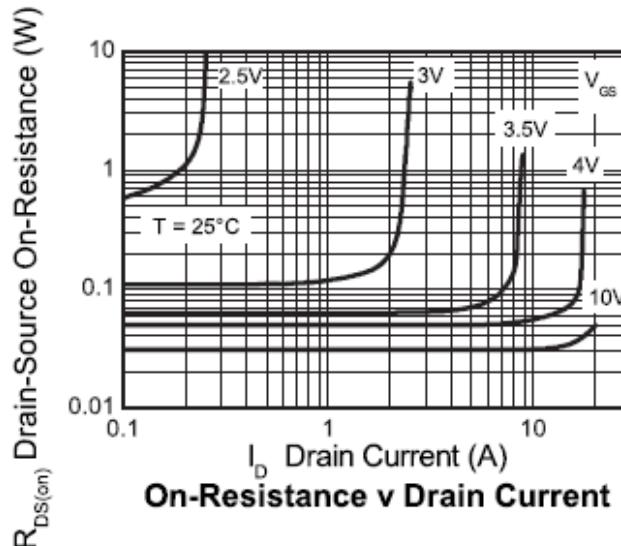
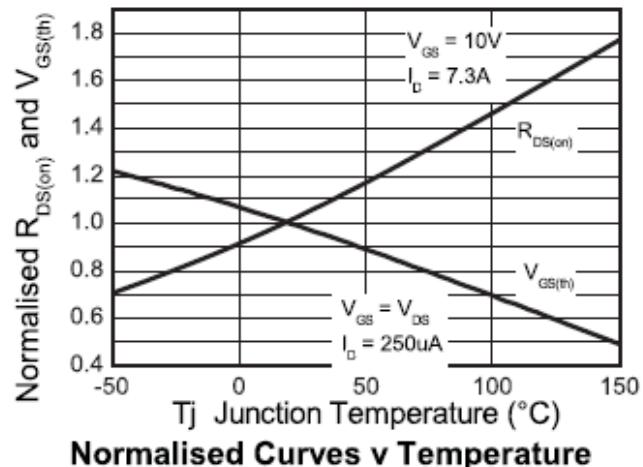
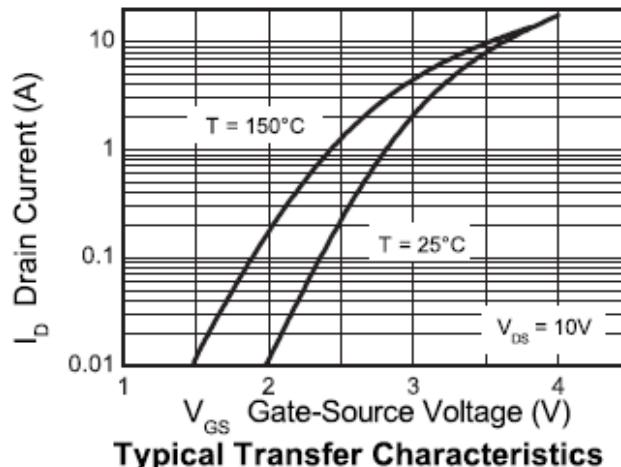
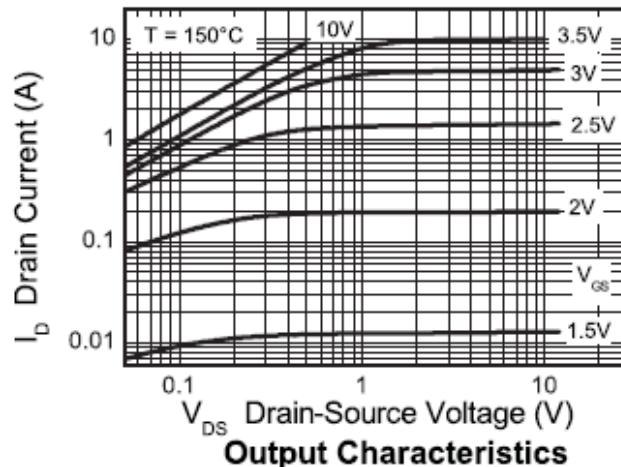
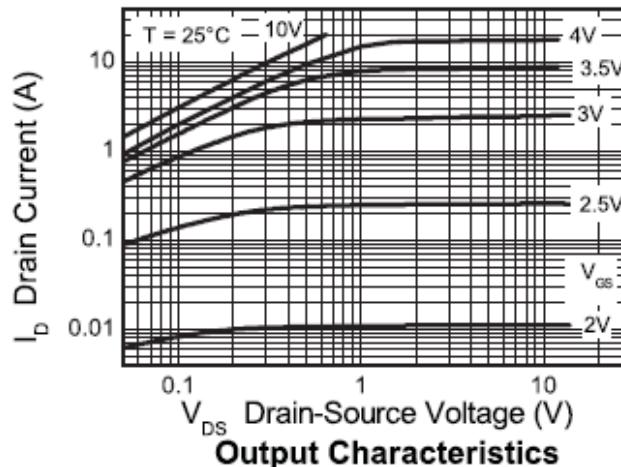
8. Measured under pulsed conditions. Pulse width $\leq 300\mu\text{s}$; duty cycle $\leq 2\%$.

9. Switching characteristics are independent of operating junction temperature.

10. For design aid only, not subject to production testing.

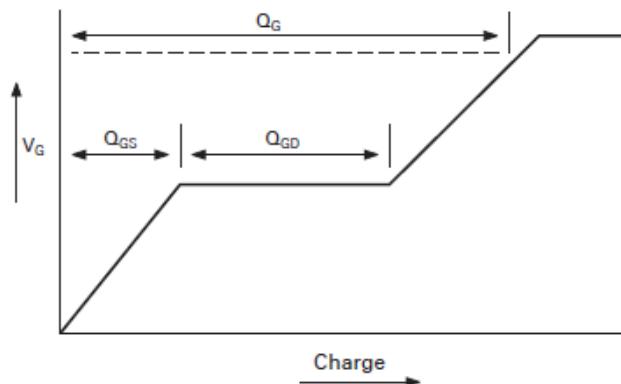
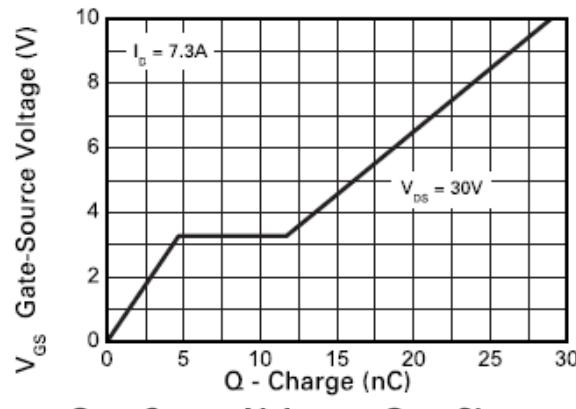
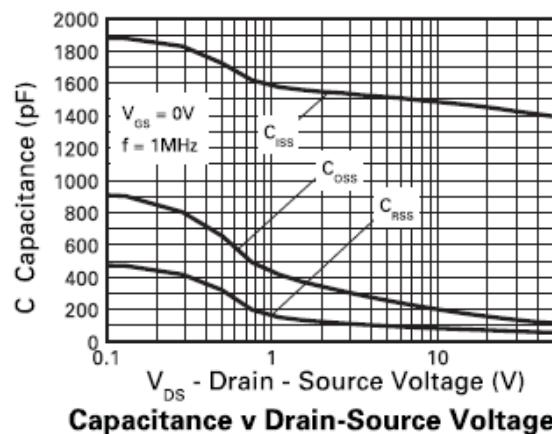


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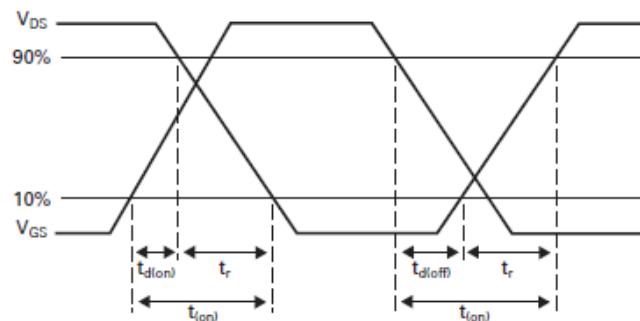
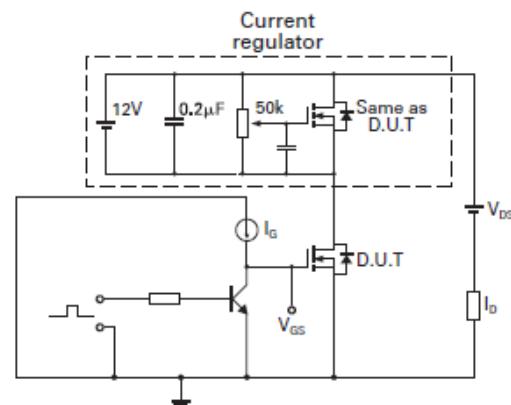




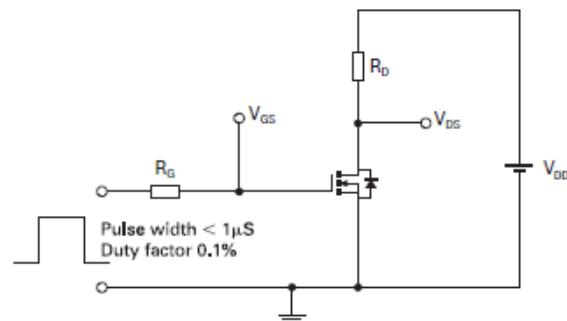
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Basic gate charge waveform



Switching time waveforms

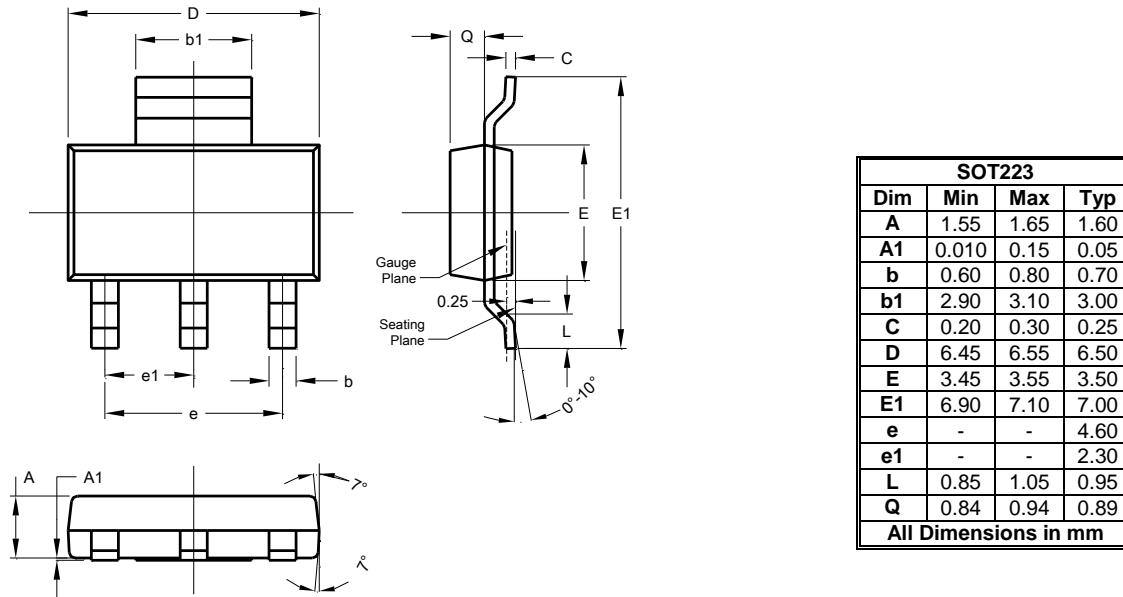




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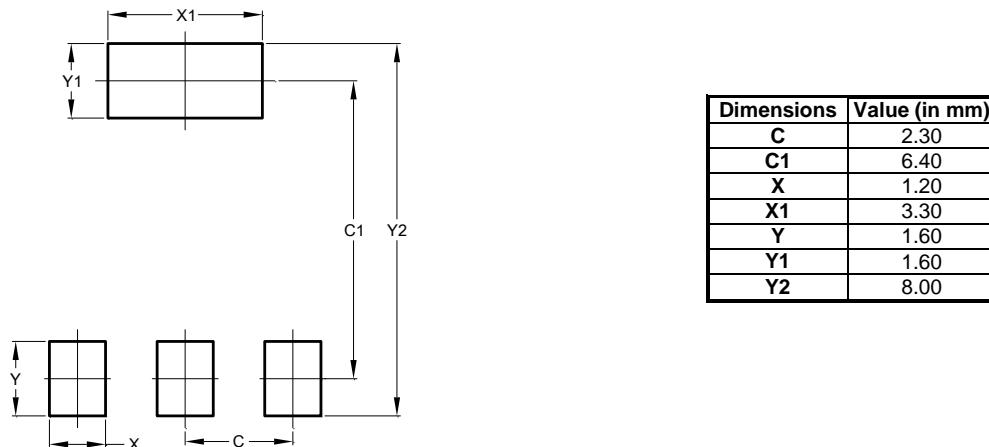
Package Outline Dimensions

Please see AP02002 at <http://www.diodes.com/datasheets/ap02002.pdf> for the latest version.



Suggested Pad Layout

Please see AP02001 at <http://www.diodes.com/datasheets/ap02001.pdf> for the latest version.



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