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sales@integrated-circuit.com

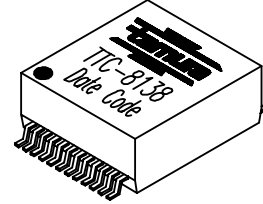
REV. Status

PRELIMINARY
11/06/00 BT
 PRELIMINARY 1
 DELETED DCR
 ON PG. 1, &
 ADDED PAD
 LAYOUT ON
 PG. 2.
 11/27/00 LL



**Gigabit Ethernet, 1000 Base-T
24 pin SOIC**

- Supports 4 pairs of Category UTP5 cable
- Complies with IEEE 802.3ab Standard
- Surface Mount Device withstands IR reflow profile process
- Operating temperature: 0°C to 70°C
- Enhanced Common Mode Attenuation

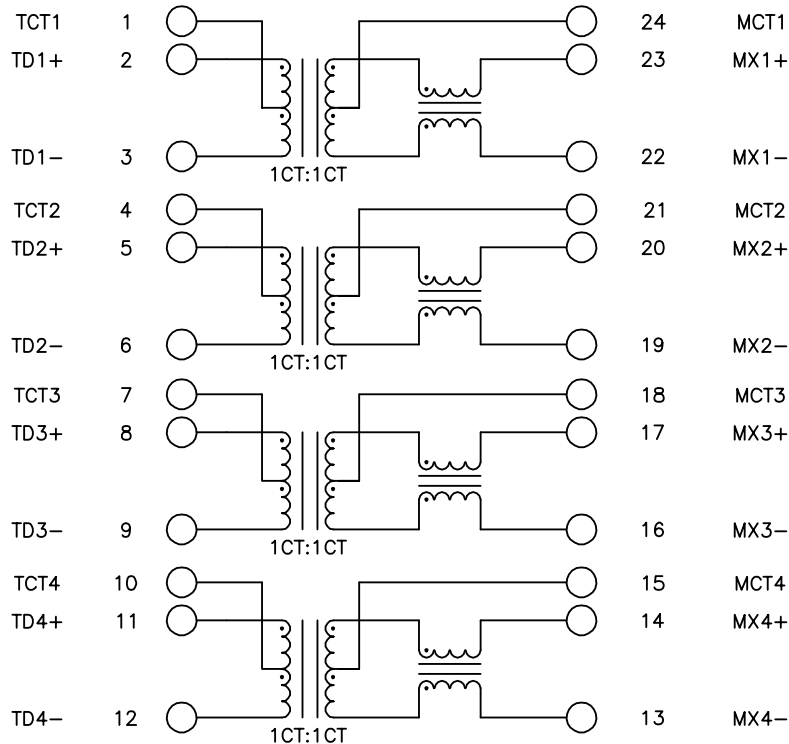


MODEL NUMBER
TTC-8138

PRELIMINARY

Electrical Specification @ 25°C

Insertion Loss (dB Max)	Return Loss (dB Min)			Differential Mode Rejection Ratio (dB Min)	Common Mode Rejection Ratio (dB Min)	Leakage Inductance (μH Max)	Crosstalk (dB Min)	OCL (μH Min)	Hipot (Vrms)
	0.1-30 MHz	60-80 MHz	100 MHz						
1-100 MHz	0.1-30 MHz	60-80 MHz	100 MHz	30-100 MHz	30-100 MHz		1-100 MHz	8mA DC Bias	
-1	-18	-12	-10	-33	-30	0.5	-35	350	1500



PREPARED BY:

D. Rund

ENGINEER:

L. Le

QUALITY CONTROL:

D. Kelley

APPROVED:

J. Coleman

DWG CONTROL NO.
P-A1-12655
ACAD\TTC\A1126551.DWG

REV
PR1

GIGABIT ETHERNET, 1000 BASE-T
24 PIN SOIC

TTC-8138

MODEL SPECIFICATION

CONTENTS OF THIS DRAWING ARE
SUBJECT TO CHANGE WITHOUT
PRIOR NOTICE

TAMURA CORPORATION OF AMERICA
43352 BUSINESS PARK DRIVE, TEMECULA, CA. 92590-6624
(909) 699-1270 FAX 9096769482

DIM: mm(In) SCL: NONE SH: 1 OF 2

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REV. Status

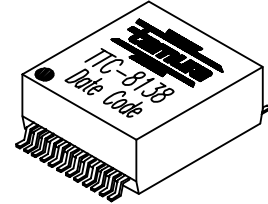
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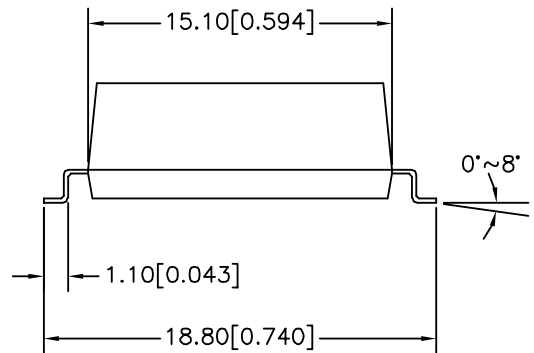
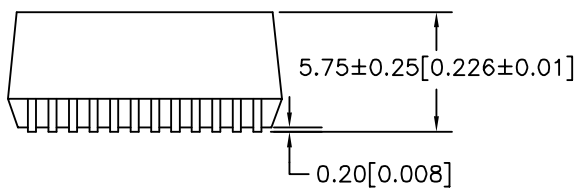
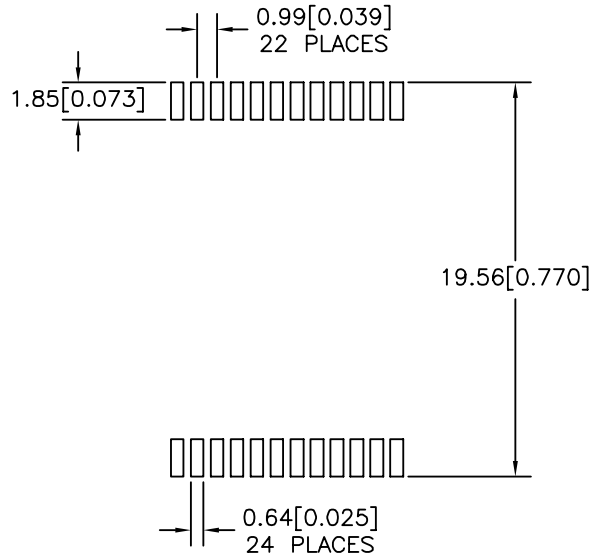
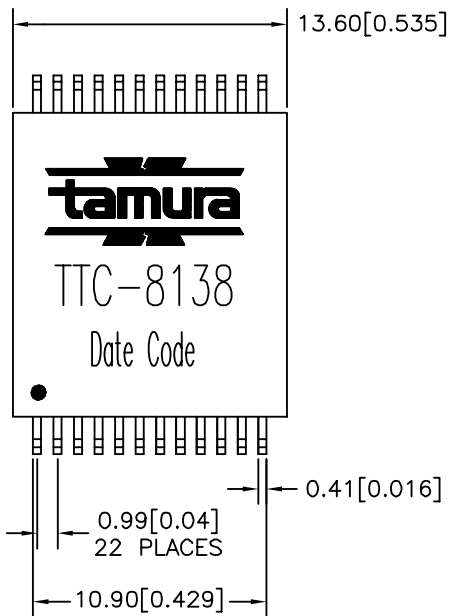


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24 pin SOIC

MODEL NUMBER
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PRELIMINARY



PREPARED BY:

D. Rund

NOTE: Unless otherwise specified, all dimensions are mm[in] with tolerance of ±0.25[0.010]

ENGINEER:

L. Le

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ACAD\TTC\A1126552.DWG

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QUALITY CONTROL:

D. Kelley

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MODEL SPECIFICATION
DIM: mm(in) SCL: 3/1 SH: 2 OF 2

APPROVED:

J. Coleman

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