# **SC-74 Quad Monolithic Common Anode**

## **Transient Voltage Suppressors for ESD Protection**

This quad monolithic silicon voltage suppressor is designed for applications requiring transient overvoltage protection capability. It is intended for use in voltage and ESD sensitive equipment such as computers, printers, business machines, communication systems, medical equipment, and other applications. Its quad junction common anode design protects four separate lines using only one package. These devices are ideal for situations where board space is at a premium.

#### **Features**

- SC-74 Package Allows Four Separate Unidirectional Configurations
- Peak Power Min. 24 W @ 1.0 ms (Unidirectional), per Figure 5 Waveform
- Peak Power Min. 150 W @ 20 µs (Unidirectional), per Figure 6 Waveform
- Maximum Clamping Voltage @ Peak Pulse Current
- Low Leakage < 2.0 μA
- ESD Rating of Class 3B (exceeding 16 kV) per the Human Body
- SZ Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and **PPAP** Capable
- These Devices are Pb-Free and are RoHS Compliant\*



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## SC-74 QUAD TRANSIENT **VOLTAGE SUPPRESSOR** 24 WATTS PEAK POWER 5.6 - 33 VOLTS



SC-74 **CASE 318F** STYLE 1

#### **PIN ASSIGNMENT**



- PIN 1. CATHODE
  - 2. ANODE
  - 3. CATHODE 4. CATHODE

  - 5. ANODE 6. CATHODE

#### MARKING DIAGRAM



= Specific Device Code XXX

= Date Cade = Pb-Free Package

(Note: Microdot may be in either location)

#### **DEVICE MARKING & ORDERING INFORMATION**

See specific marking and ordering information in the device marking and ordering information table on page 6 of this data

<sup>\*</sup>For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

### THERMAL CHARACTERISTICS (T<sub>A</sub> = 25°C Unless Otherwise Noted)

Characteristic	Symbol	Value	Unit
Peak Power Dissipation @ 1.0 ms (Note 1) @ T <sub>A</sub> ≤ 25°C	P <sub>pk</sub>	24	W
Peak Power Dissipation @ 20 μs (Note 2) @ T <sub>A</sub> ≤ 25°C	P <sub>pk</sub>	150	W
Total Power Dissipation on FR-5 Board (Note 3) @ T <sub>A</sub> = 25°C	P <sub>D</sub>	225 1.8	MW mW/°C
Thermal Resistance from Junction-to-Ambient	$R_{ heta JA}$	556	°C/W
Total Power Dissipation on Alumina Substrate (Note 4)  @ T <sub>A</sub> = 25°C  Derate above 25°C	P <sub>D</sub>	300 2.4	MW mW/°C
Thermal Resistance from Junction-to-Ambient	$R_{ heta JA}$	417	°C/W
Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	-55 to +150	°C
Lead Solder Temperature – Maximum (10 Second Duration)	TL	260	°C

- 1. Non-repetitive current pulse per Figure 5 and derate above  $T_A = 25^{\circ}C$  per Figure 4. 2. Non-repetitive current pulse per Figure 6 and derate above  $T_A = 25^{\circ}C$  per Figure 4.
- 3.  $FR-5 = 1.0 \times 0.75 \times 0.62$  in.
- 4. Alumina = 0.4 x 0.3 x 0.024 in., 99.5% alumina

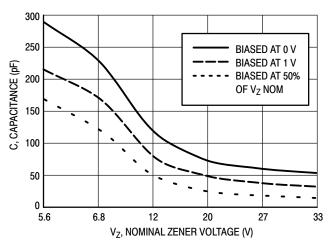
#### ELECTRICAL CHARACTERISTICS (T<sub>A</sub> = 25°C Unless Otherwise Noted) UNIDIRECTIONAL

(Circuit tied to pins 1, 2, and 5; Pins 2, 3, and 5; Pins 2, 4, and 5; or Pins 2, 5, and 6) ( $V_F = 0.9 \text{ V Max} @ I_F = 10 \text{ mA}$ )

	Breakdown Voltage				Max Reverse Leakage Current			Max	Max Reverse Voltage @ I <sub>RSM</sub>	Maximum	Capacitance @ 0 Volt Bias, 1 MHz	
		VzT (Note 6) (V)		@ I <sub>ZT</sub>	I <sub>R</sub>	V <sub>R</sub>	Max Zener Impedance (Note 7)	Reverse Surge Current	(Note 8) (Clamping Voltage)	Temperature Coefficient of V <sub>Z</sub>	(pF)	
<b>Device</b> (Note 5)	Min	Nom	Max	(mA)	(nA)	(V)	$Z$ ZT @ $I$ ZT $(\Omega)$ $(mA)$	IRSM (A)	VRSM (V)	(mV/°C)	Min	Max
MMQA5V6T1G	5.32	5.6	5.88	1.0	2000	3.0	400	3.0	8.0	1.26	_	-
MMQA6V2T1G/T3G	5.89	6.2	6.51	1.0	700	4.0	300	2.66	9.0	10.6	_	-
MMQA6V8T1G	6.46	6.8	7.14	1.0	500	4.3	300	2.45	9.8	10.9	100	250
MMQA12VT1G	11.4	12	12.6	1.0	75	9.1	80	1.39	17.3	14	_	-
MMQA13VT1G	12.4	13	13.7	1.0	75	9.8	80	1.29	18.6	15	_	-
MMQA15VT1G	14.3	15	15.8	1.0	75	11	80	1.1	21.7	16	-	-
MMQA18VT1G	17.1	18	18.9	1.0	75	14	80	0.923	26	19	-	-
MMQA20VT1G/T3G	19	20	21	1.0	75	15	80	0.84	28.6	20.1	-	-
MMQA22VT1G	20.9	22	23.1	1.0	75	17	80	0.758	31.7	22	-	-
MMQA24VT1G	22.8	24	25.2	1.0	75	18	100	0.694	34.6	25	_	-
MMQA27VT1G	25.7	27	28.4	1.0	75	21	125	0.615	39	28	_	-
MMQA33VT1G	31.4	33	34.7	1.0	75	25	200	0.504	48.6	37	-	-

- 5. Includes SZ-prefix devices where applicable.
- 6. V<sub>Z</sub> measured at pulse test current I<sub>T</sub> at an ambient temperature of 25°C.
- $Z_{ZT}^{-}$  is measured by dividing the AC voltage drop across the device by the AC current supplied. The specified limits are  $I_{Z(AC)} = 0.1 I_{Z(DC)}$ , with AC frequency = 1 kHz.
- 8. Surge current waveform per Figure 5 and derate per Figure 4.

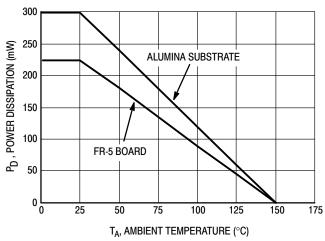
### **TYPICAL CHARACTERISTICS**



10,000 1,000 1,000 +150°C +150°C +25°C +25°C -40°C 5.6 6.8 20 27 33 V<sub>Z</sub>, NOMINAL ZENER VOLTAGE (V)

Figure 1. Typical Capacitance

Figure 2. Typical Leakage Current





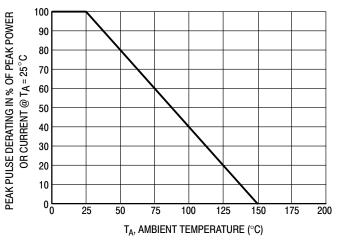


Figure 4. Pulse Derating Curve

#### **TYPICAL CHARACTERISTICS**

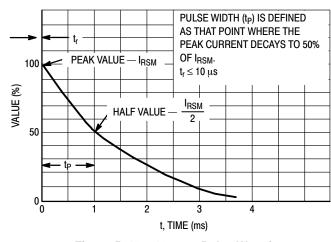


Figure 5. 10  $\times$  1000  $\mu s$  Pulse Waveform

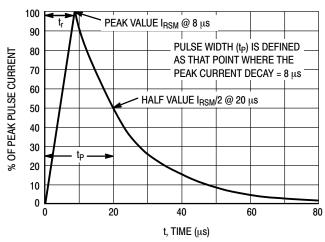


Figure 6.  $8 \times 20~\mu s$  Pulse Waveform

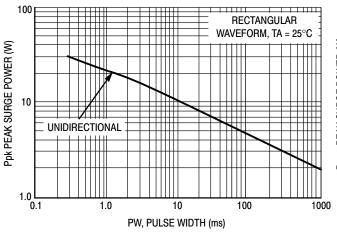


Figure 7. Maximum Non-Repetitive Surge Power, P<sub>pk</sub> versus PW

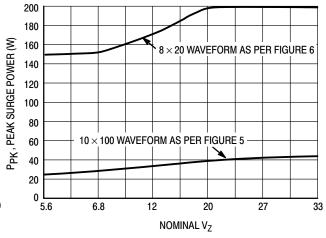


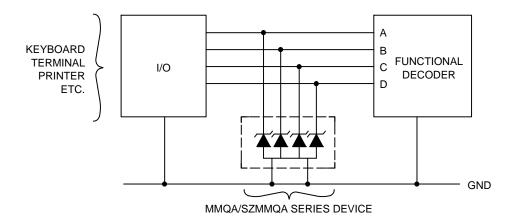
Figure 8. Typical Maximum Non-Repetitive Surge Power,  $P_{pk}$  versus  $V_Z$ 

Power is defined as  $V_{RSM} \, x \, I_Z(pk)$  where  $V_{RSM}$  is the clamping voltage at  $I_Z(pk)$ .

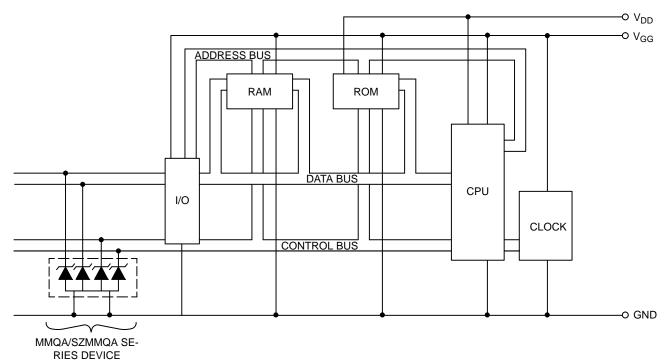
### **TYPICAL COMMON ANODE APPLICATIONS**

A quad junction common anode design in a SC-74 package protects four separate lines using only one package. This adds flexibility and creativity to PCB design especially

when board space is at a premium. A simplified example of MMQA/SZMMQA Series Device applications is illustrated below.



**Figure 9. Computer Interface Protection** 



**Figure 10. Microprocessor Protection** 

### **DEVICE MARKING AND ORDERING INFORMATION**

Device*	Device Marking	Package	Shipping
MMQA5V6T1G	5A6		3,000/Tape & Reel
MMQA6V2T1G	6A2		3,000/Tape & Reel
MMQA6V2T3G	6A2		10,000/Tape & Reel
MMQA6V8T1G	6A8		3,000/Tape & Reel
MMQA12VT1G	12A		3,000/Tape & Reel
MMQA13VT1G	13A		3,000/Tape & Reel
MMQA15VT1G	15A		3,000/Tape & Reel
MMQA18VT1G	18A	SC-74 (Pb-Free)	3,000/Tape & Reel
MMQA20VT1G	20A		3,000/Tape & Reel
MMQA20VT3G	20A		10,000/Tape & Reel
MMQA22VT1G	22A		3,000/Tape & Reel
MMQA24VT1G	24A		3,000/Tape & Reel
MMQA27VT1G	27A		3,000/Tape & Reel
MMQA27VT3G	27A		10,000/Tape & Reel
MMQA33VT1G	33A		3,000/Tape & Reel

<sup>†</sup>For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, BRD8011/D.

#### **Mechanical Characteristics:**

**CASE:** Void–free, Transfer–molded, Thermosetting Plastic Case.

FINISH: Corrosion resistant finish, easily solderable.

Package designed for optimal automated board assembly.

Small package size for high density applications.

Available in 8 mm Tape and Reel.

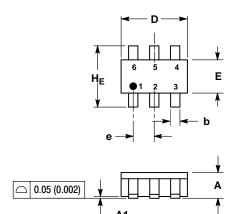
Use the Device Number to order the 7 inch/3,000 unit reel.

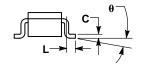
Replace the "T1" with "T3" in the Device Number to order the 13 inch/10,000 unit reel.

<sup>\*</sup>IncludeS SZ-prefix devices where applicable.

#### PACKAGE DIMENSIONS

#### SC-74 CASE 318F-05 ISSUE N





#### NOTES:

- 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
- CONTROLLING DIMENSION: INCH.
  MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH THICKNESS. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL.
- 318F-01, -02, -03, -04 OBSOLETE. NEW STANDARD 318F-05.

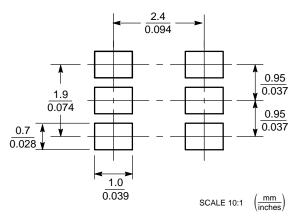
	М	ILLIMETE	RS	INCHES			
DIM	MIN	NOM	MAX	MIN	NOM	MAX	
Α	0.90	1.00	1.10	0.035	0.039	0.043	
A1	0.01	0.06	0.10	0.001	0.002	0.004	
b	0.25	0.37	0.50	0.010	0.015	0.020	
С	0.10	0.18	0.26	0.004	0.007	0.010	
D	2.90	3.00	3.10	0.114	0.118	0.122	
E	1.30	1.50	1.70	0.051	0.059	0.067	
е	0.85	0.95	1.05	0.034	0.037	0.041	
L	0.20	0.40	0.60	0.008	0.016	0.024	
HE	2.50	2.75	3.00	0.099	0.108	0.118	
θ	0°	_	10°	0°	_	10°	

## STYLE 1: PIN 1. CATHODE

- - 2. ANODE 3. CATHODE
  - CATHODE

  - ANODE CATHODE

### **SOLDERING FOOTPRINT\***



\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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