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STMicroelectronics TIP34C

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TIP33C TIP34C

Complementary power transistors

Features

- Low collector-emitter saturation voltage
- Complementary NPN PNP transistors

Applications

General purpose

Description

The devices are manufactured in epitaxial-base planar technology and are suitable for power linear and switching applications.

obsolete Product(s)

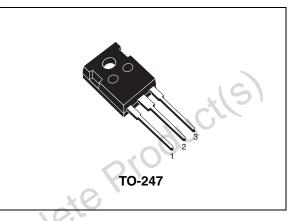


Figure 1. Internal schematic diagrams

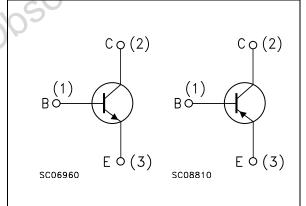


Table 1.	Device summary
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Order code	Marking	Package	Packaging
TIP33C	TIP33C	TO-247	Tube
TIP34C	TIP34C	10-247	Tube



Electrical ratings

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1 Electrical ratings

Symbol	Parameter		Value	Unit	
	-	NPN	TIP33C		
		PNP	TIP34C		
V _{CBO}	Collector-base voltage (I _E = 0)		140	V	
V _{CES}	Collector-emitter voltage (V _{BE} = 0)		140	v	
V _{CEO}	Collector-emitter voltage (I _B = 0) 100			V	
V _{EBO}	Emitter-base voltage ($I_{C} = 0$)	5	V		
۱ _C	Collector current	10	А		
I _{CM}	Collector peak current (t _P < 5 ms)			А	
Ι _Β	Base current 3		3	А	
P _{TOT}	Total dissipation at T _{case} = 25 °C 80		80	W	
T _{stg}	Storage temperature -65 to -		-65 to 150	°C	
ТJ	Max. operating junction temperature 150		°C		

Table 2. Absolute maximum ratings

For PNP type voltage and current values are negative.

Table 3.	Thermal data	1

S	Symbol	Parameter		Value	Unit
F	R _{thj-case}	Thermal resistance junction-case	max	1.56	°C/W
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Electrical characteristics 2

(T_{case} = 25 °C; unless otherwise specified)

Table 4.	Electrical characterist	ristics					
Symbol	Parameter Test conditions		Min.	Тур.	Max.	Unit	
I _{CES}	Collector cut-off current $(V_{BE} = 0)$	V _{CE} = 140 V				0.4	mA
I _{CEO}	Collector cut-off current (I _B = 0)	V _{CE} = 60 V				0.7	mA
I _{EBO}	Emitter cut-off current (I _C = 0)	V _{EB} = 5 V					mA
V _{CEO(sus)} ⁽¹⁾	Collector-emitter sustaining voltage (I _B = 0)	I _C = 30 mA	Q	100	90		V
V _{CE(sat)} ⁽¹⁾	Collector-emitter saturation voltage	I _C = 3 A I _C = 10 A	$I_{B} = 0.3 A$ $I_{B} = 2.5 A$			1 4	V V
V _{BE(on)} ⁽¹⁾	Base-emitter voltage	$I_{\rm C} = 3$ A $I_{\rm C} = 10$ A	$V_{CE} = 4 V$ $V_{CE} = 4 V$			1.6 3	V V
h _{FE} ⁽¹⁾	DC current gain	$I_{\rm C} = 1 \text{ A}$ $I_{\rm C} = 3 \text{ A}$	$V_{CE} = 4 V$ $V_{CE} = 4 V$	40 20		100	
h _{fe}	Small signal current gain	l _C = 0.5 A f = 1 kHz	V _{CE} = 10 V	3			
fT	Transition frequency	l _C = 0.5 A f = 1 MHz	V _{CE} = 10 V	3			MHz
t _{on} t _s t _f	Resistive load Turn-on time Storage time Fall time	$V_{CC} = 30 V$ $I_{B1} = -I_{B2} = 0.6 A$ $tp = 20 \ \mu s$	-		0.6 0.4 1		μs μs μs

Table 4 Electrical characteristics





Package mechanical data

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3 Package mechanical data

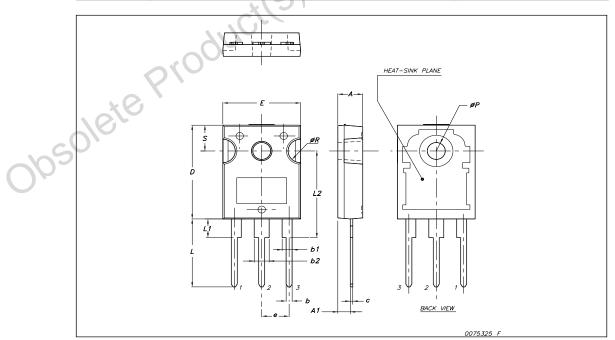
In order to meet environmental requirements, ST offers these devices in ECOPACK® packages. These packages have a Lead-free second level interconnect. The category of second level interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available at: Obsolete Product(s). Obsolete Product(s) www.st.com



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Package mechanical data

	TO-247 Mechanical data				
Dim.		mm.			
2	Min.	Тур	Max.		
A	4.85		5.15		
A1	2.20		2.60		
b	1.0		1.40		
b1	2.0		2.40		
b2	3.0		3.40		
с	0.40		0.80		
D	19.85		20.15		
E	15.45		15.75		
е		5.45			
L	14.20	101	14.80		
L1	3.70		4.30		
L2		18.50			
øP	3.55	<u>O</u> Y	3.65		
øR	4.50		5.50		
S	IC	5.50			



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Revision history

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4 Revision history

Table 5.	Document revision history

	Date	Revision	Changes
	01-Oct-1999	2	
	20-Feb-2008	3	Package change from TO-218 to TO-247.
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