

Excellent Integrated System Limited

Stocking Distributor

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ON Semiconductor MBR340

For any questions, you can email us directly: <u>sales@integrated-circuit.com</u>



Distributor of ON Semiconductor: Excellent Integrated System Limited Datasheet of MBR340 - DIODE SCHOTTKY 40V 3A DO201AD Contact us: sales@integrated-circuit.com Website: www.integrated-circuit.com

MBR340

Preferred Device

Axial Lead Rectifier

This device employs the Schottky Barrier principle in a large area metal-to-silicon power diode. State-of-the-art geometry features epitaxial construction with oxide passivation and metal overlap contact. Ideally suited for use as rectifiers in low-voltage, high-frequency inverters, free wheeling diodes, and polarity protection diodes.

Features

- Extremely Low V_F
- Low Power Loss/High Efficiency
- Highly Stable Oxide Passivated Junction
- Low Stored Charge, Majority Carrier Conduction
- Pb–Free Packages are Available*

Mechanical Characteristics:

- Case: Epoxy, Molded
- Weight: 1.1 Gram (Approximately)
- Finish: All External Surfaces Corrosion Resistant and Terminal Leads are Readily Solderable
- Lead Temperature for Soldering Purposes: 260°C Max. for 10 Seconds
- Polarity: Cathode indicated by Polarity Band

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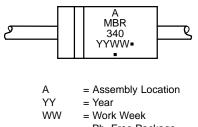
ON Semiconductor®

http://onsemi.com

SCHOTTKY BARRIER RECTIFIER 3.0 AMPERES, 40 VOLTS



MARKING DIAGRAM



= Pb-Free Package

(Note: Microdot may be in either location)

ORDERING INFORMATION

Device	Package	Shipping [†]
MBR340	Axial Lead	500 Units / Bag
MBR340G	Axial Lead (Pb–Free)	500 Units / Bag
MBR340RL	Axial Lead	1500/Tape & Reel
MBR340RLG	Axial Lead (Pb–Free)	1500/Tape & Reel

+For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

Preferred devices are recommended choices for future use and best overall value.

MAXIMUM RATINGS

Rating	Symbol	Max	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V _{RRM} V _{RWM} V _R	40	V
Average Rectified Forward Current $T_A = 65^{\circ}C$ ($R_{\theta JA} = 28^{\circ}C/W$, P.C. Board Mounting)	Ι _Ο	3.0	A
Non–Repetitive Peak Surge Current (Note 1) (Surge Applied at Rated Load Conditions Halfwave, Single Phase, 60 Hz, $T_L = 75^{\circ}C$)	I _{FSM}	80	A
Operating and Storage Junction Temperature Range (Reverse Voltage Applied) (Note 2)	T _J , T _{stg}	–65 to +175	°C

THERMAL CHARACTERISTICS

Rating	Symbol	Max	Unit	1
Thermal Resistance, Junction-to-Ambient (see Note 5, Mounting Method 3)	$R_{\theta J A}$	28	°C/W	

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

1. Lead Temperature reference is cathode lead 1/32 in from case.

2. The heat generated must be less than the thermal conductivity from Junction-to-Ambient: $dP_D/dT_J < 1/R_{\theta JA}.$

*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.



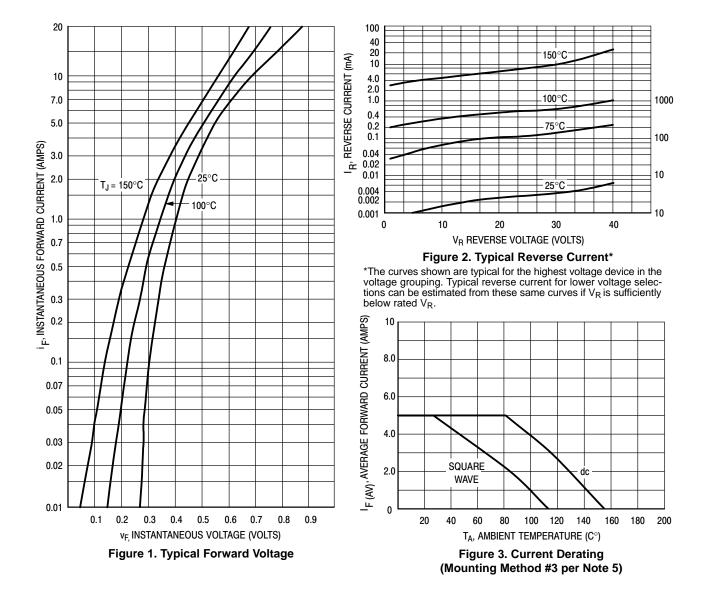
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ELECTRICAL CHARACTERISTICS (T_L = 25°C unless otherwise noted) (Note 3)

Characteristic	Symbol	Max	Unit
Maximum Instantaneous Forward Voltage (Note 4) ($i_F = 1.0 \text{ Amp}$) ($i_F = 3.0 \text{ Amp}$) ($i_F = 9.4 \text{ Amp}$)	VF	0.500 0.600 0.850	V
Maximum Instantaneous Reverse Current @ Rated dc Voltage (Note 4) $T_L = 25^{\circ}C$ $T_L = 100^{\circ}C$	İR	0.60 20	mA

3. Lead Temperature reference is cathode lead 1/32in from case.

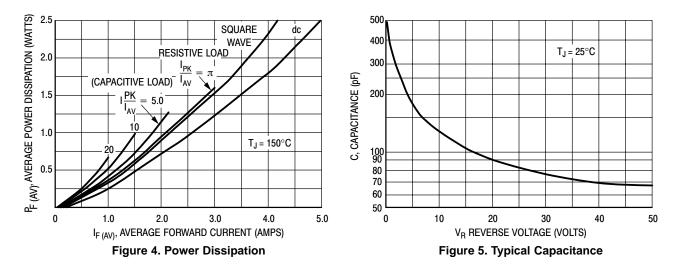
4. Pulse Test: Pulse Width = $300 \ \mu$ s, Duty Cycle = 2.0%.





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NOTE 5 — MOUNTING DATA

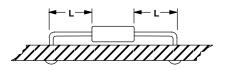
Data shown for thermal resistance junction–to–ambient $(R_{\theta JA})$ for the mountings shown is to be used as typical guideline values for preliminary engineering, or in case the tie point temperature cannot be measured.

TYPICAL VALUES FOR $R_{\theta JA}$ IN STILL AIR

Mounting Lead Length, L (in)					
Method	1/8	1/4	1/2	3/4	$R_{\theta JA}$
1	50	51	53	55	°C/W
2	58	59	61	63	°C/W
3	28				°C/W

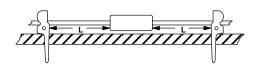
Mounting Method 1

P.C. Board where available copper surface is small.



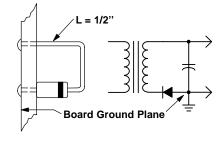
Mounting Method 2

Vector Push–In Terminals T–28



Mounting Method 3

P.C. Board with 2–1/2" X 2–1/2" copper surface.

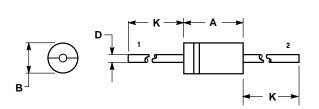




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PACKAGE DIMENSIONS

AXIAL LEAD CASE 267-05 ISSUE G



NOTES 1. DIMENSIONS AND TOLERANCING PER ANSI Y14.5M, 1982. CONTROLLING DIMENSION: INCH.

3. 267-04 OBSOLETE, NEW STANDARD 267-05.

	INCHES		MILLIMETERS	
DIM	MIN	MAX	MIN	MAX
Α	0.287	0.374	7.30	9.50
В	0.189	0.209	4.80	5.30
D	0.047	0.051	1.20	1.30
K	1.000		25.40	

STYLE 1: PIN 1. CATHODE (POLARITY BAND) 2. ANODE

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