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Rohm Semiconductor DTB143ESTP

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Transistors

DTB143EK / DTB143EC / DTB143ES

-500mA / -50V Digital transistors (with built-in resistors)

DTB143EK / DTB143EC / DTB143ES

Applications

Inverter, Interface, Driver

Features

- Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see equivalent circuit).
- 2) The bias resistors consist of thin-film resistors with complete isolation to allow positive biasing of the input. They also have the advantage of almost completely eliminating parasitic effects.
- Only the on / off conditions need to be set for operation, making the device design easy.

Structure

PNP epitaxial planar silicon transistor (Resistor built-in type)

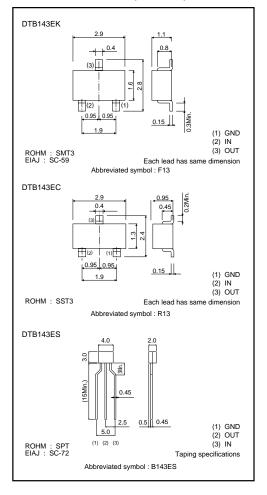
Packaging specifications

	Package	SMT3	SST3	SPT	
	Package type	Taping	Taping	Taping	
	Code	T146	T116	TP	
Part No.	Basic ordering unit (pieces)	3000	3000	5000	
DTB143EK		0	-	-	
DTB143EC		-	0	-	
DTB143ES		_	-	0	

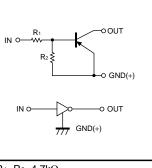
Absolute maximum ratings (Ta=25°C)

Parameter	Symbol		Unit		
	Symbol	DTB143EK DTB143EC DTB143ES			
Supply voltage	Vcc	-50			V
Input voltage	Vin	-30 to +10			V
Output current	lc	-500		mA	
Power dissipation	Pd	200 3		300	mW
Junction temperature	Tj	150		°C	
Storage temperature	Tstg	-55 to +150		°C	

•External dimensions (Unit : mm)



•Equivalent circuit



R1=R2=4.7kΩ



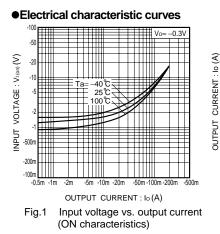
DTB143EK / DTB143EC / DTB143ES

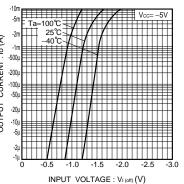
Transistors

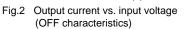
Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions	
1 7 17	VI(off)	-	-	-0.5		Vcc=-5V, Io=-100µA	
Input voltage	VI(on)	-3	-	-	V	Vo=-0.3V, Io=-20mA	
Output voltage	VO(on)	_	-0.1	-0.3	V	lo/l= -50mA/-2.5mA	
Input current	h	-	-	-1.8	mA	Vi=−5V	
Output current	IO(off)	-	-	-0.5	μA	Vcc= −50V, Vi=0V	
DC current gain	Gi	47	-	-	-	Vo= -5V, Io= -50mA	
Input resistance	R1	3.29	4.7	6.11	kΩ	_	
Resistance ratio	R2/R1	0.8	1	1.2	-	-	
Transition frequency	f⊤ *	-	200	-	MHz	Vce= -10V, Ie=50mA, f=100MHz	

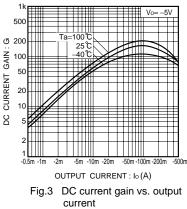
•Electrical characteristics (Ta=25°C)

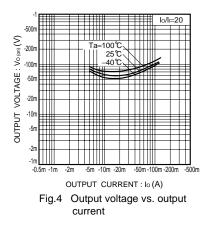
* Characteristics of built-in transsistor











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Appendix

Notes

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