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ON Semiconductor NUP4301MR6T1

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NUP4301MR6T1

Low Capacitance Diode Array for ESD Protection in Four Data Lines

NUP4301MR6T1 is a MicroIntegration[™] device designed to provide protection for sensitive components from possible harmful electrical transients; for example, ESD (electrostatic discharge).

Features

- Low Capacitance (1.5 pf Maximum Between I/O Lines)
- Single Package Integration Design
- Provides ESD Protection for JEDEC Standards JESD22 Machine Model = Class C Human Body Model = Class 3B
- Protection for IEC61000-4-2 (Level 4) 8.0 kV (Contact) 15 kV (Air)
- Ensures Data Line Speed and Integrity
- Fewer Components and Less Board Space
- Direct the Transient to Either Positive Side or to the Ground

Applications

- USB 1.1 and 2.0 Data Line Protection
- T1/E1 Secondary IC Protection
- T3/E3 Secondary IC Protection
- HDSL, IDSL Secondary IC Protection
- Video Line Protection
- Microcontroller Input Protection
- Base Stations
- I²C Bus Protection

MAXIMUM RATINGS (Each Diode) (T_{.1} = 25°C unless otherwise noted)

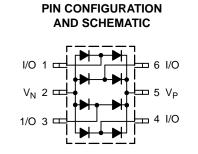
Rating	Symbol	Value	Unit	
Reverse Voltage	V _R	70	Vdc	
Forward Current	١ _F	200	mAdc	
Peak Forward Surge Current	I _{FM(surge)}	500	mAdc	
Repetitive Peak Reverse Voltage	V _{RRM}	70	V	
Average Rectified Forward Current (Note 1) (averaged over any 20 ms period)	I _{F(AV)}	715	mA	
Repetitive Peak Forward Current	I _{FRM}	450	mA	
Non-Repetitive Peak Forward Current t = 1.0 μ s t = 1.0 ms t = 1.0 S	I _{FSM}	2.0 1.0 0.5	A	

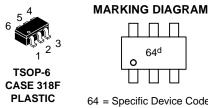
1. FR-5 = $1.0 \times 0.75 \times 0.062$ in.



ON Semiconductor®

http://onsemi.com





64 = Specific Device Code d = Date Code

ORDERING INFORMATION

Device	Package	Shipping
NUP4301MR6T1	TSOP-6	3000/Tape & Reel



NUP4301MR6T1

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance Junction-to-Ambient	$R_{ hetaJA}$	556	°C/W
Lead Solder Temperature Maximum 10 Seconds Duration	TL	260	C°
Junction Temperature	TJ	-40 to +85	°C
Storage Temperature	T _{stg}	-55 to +150	°C

ELECTRICAL CHARACTERISTICS ($T_J = 25^{\circ}C$ unless otherwise noted) (Each Diode)

Characteristic	Symbol	Min	Тур	Max	Unit	
OFF CHARACTERISTICS						
Reverse Breakdown Voltage ($I_{(BR)} = 100 \ \mu A$)	V _(BR)	70	-	-	Vdc	
$\begin{tabular}{lllllllllllllllllllllllllllllllllll$	i v		- -	2.5 30 50	μAdc	
Capacitance (between I/O pins) ($V_R = 0 V$, f = 1.0 MHz)	CD	-	0.8	1.5	pF	
Capacitance (between I/O pin and ground) $(V_R = 0 V, f = 1.0 MHz)$	CD	-	1.6	3	pF	
Forward Voltage $\begin{array}{l} (I_{F}=1.0\mbox{ mAdc})\\ (I_{F}=10\mbox{ mAdc})\\ (I_{F}=50\mbox{ mAdc})\\ (I_{F}=150\mbox{ mAdc})\\ \end{array}$		- - - -	- - - -	715 855 1000 1250	mV _{dc}	

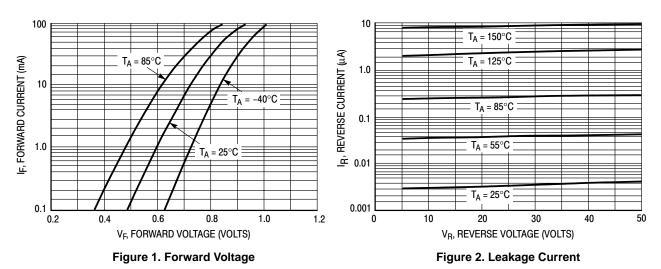
1. FR-5 = $1.0 \times 0.75 \times 0.062$ in. 2. Alumina = $0.4 \times 0.3 \times 0.024$ in. 99.5% alumina.

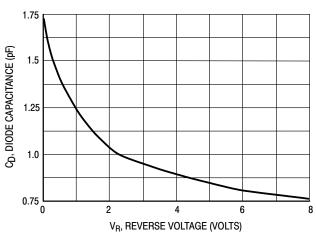


Distributor of ON Semiconductor: Excellent Integrated System Limited Datasheet of NUP4301MR6T1 - TVS DIODE SC74 Contact us: sales@integrated-circuit.com Website: www.integrated-circuit.com

NUP4301MR6T1

Curves Applicable to Each Cathode





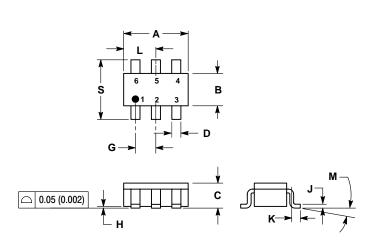




NUP4301MR6T1

PACKAGE DIMENSIONS

TSOP-6 CASE 318F-04 ISSUE J



NOTES

- NOTES: 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982. 2. CONTROLLING DIMENSION: INCH. 3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH THICKNESS. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL. 4. 246E 04. 020.020 OPEOLETE NEW.
- 318F-01, -02, -03 OBSOLETE. NEW STANDARD 318F-04.

	INC	HES	MILLIMETERS		
DIM	MIN	MAX	MIN	MAX	
Α	0.1142	0.1220	2.90	3.10	
В	0.0512	0.0669	1.30	1.70	
C	0.0354	0.0433	0.90	1.10	
D	0.0098	0.0197	0.25	0.50	
G	0.0335	0.0413	0.85	1.05	
Н	0.0005	0.0040	0.013	0.100	
J	0.0040	0.0102	0.10	0.26	
K	0.0079	0.0236	0.20	0.60	
L	0.0493	0.0649	1.25	1.65	
M	0 °	10 °	0 °	10 °	
S	0.0985	0.1181	2.50	3.00	

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NUP4301MR6T1/D