

**Excellent Integrated System Limited** 

Stocking Distributor

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EPCOS (TDK) B49410A2205Q000

For any questions, you can email us directly: sales@integrated-circuit.com



## Distributor of EPCOS (TDK): Excellent Integrated System Limited

Datasheet of B49410A2205Q000 - CAP 200F -10% +30% 2.5V T/H Contact us: sales@integrated-circuit.com Website: www.integrated-circuit.com



# $UltraCap^{ ext{$(ar{B})$}}$

Single cell 200 F/ 2.5 V

Series/Type:

Ordering code: B49410A2205Q000

Date: March 2005

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Datasheet of B49410A2205Q000 - CAP 200F -10% +30% 2.5V T/H



UltraCap<sup>®</sup> B49410A2205Q000

**EPCOS** 

## Single cell, 200 F/ 2.5 V

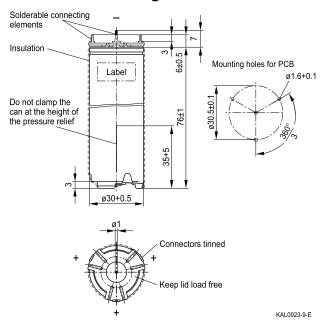
#### **Features**

- Solder pin/4
- Power type
- Insulated with polyurethane
- Short-circuit-proof

#### Note

- Do not put into fire!
- Do not open the capacitor!
- To avoid health and fire hazards, do not operate the capacitor beyond the voltage or temperature limits given in the data sheet. Any excess may also result in a reduction of lifetime.
- Please pay also attention to the transport and waste disposal instructions in chapter "Cautions".

## **Dimensional drawing**



Dimensions in mm

## **Electrical specifications**

Rated capacitance	(T <sub>A</sub> = 25 °C; DCC) <sup>1)</sup>	C <sub>B</sub>	200	F
Tolerance of C <sub>R</sub>	,		-10/+30	%
Rated voltage	(T <sub>A</sub> = 25 °C)	$V_R$	2.5	V
Capacity			140	mAh
Specific power	(IEC 62391-2)		3.3	kW/kg
Specific power	(IEC 62391-2)		3.9	kW/I
Stored energy	$(V = V_R)$	E	625	J
Specific energy	$(V = V_R)$		2.7	Wh/kg
Specific energy	$(V = V_R)$		3.1	Wh/I
Surge voltage		$V_{\text{surge}}$	2.8	V
Maximum series resistance	$(T_A = 25  {}^{\circ}C;  1  kHz)$	ESR	1.5	mΩ
Maximum series resistance	$(T_A = 25  ^{\circ}C; 50  \text{mHz})$	ESR <sub>DC</sub>	3.5	mΩ
Weight			65	g
Volume			0.056	1
Operating temperature range		T <sub>op</sub>	-30/+70	°C
Storage temperature	(V = 0 V)	$T_{st}$	-40/+70	°C
Lifetime (hours) 2)	$(T_A = 25  {}^{\circ}C;  V = V_R)$		90000	h
Lifetime (cycles) 3)	$(T_A = 25  {}^{\circ}C; I = 8  A)$		500000	cycles

<sup>1)</sup> DCC: discharging with constant current.

<sup>2)</sup> Requirements:  $|\Delta C/C_R| \le 30\%$ , ESR  $\le 2$  times of specified limit,  $I_{leak} \le 2$  times of initial value.

<sup>3)</sup> Requirements:  $|\Delta C/C_R| \le 30\%$ , ESR  $\le 2$  times of specified limit,  $I_{leak} \le 2$  times of initial value (1 cycle: charging to  $V_R$ , 30 s rest, discharging to  $V_R/2$ , 30 s rest).