

## Excellent Integrated System Limited

Stocking Distributor

Click to view price, real time Inventory, Delivery & Lifecycle Information:

[Vishay Semiconductor/Diodes Division](#)  
[VS-40L15CT-1PBF](#)

For any questions, you can email us directly:

[sales@integrated-circuit.com](mailto:sales@integrated-circuit.com)



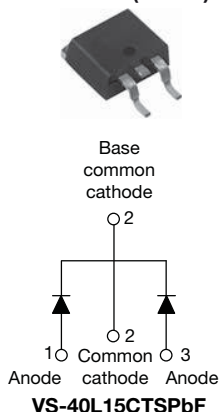
www.vishay.com

## VS-40L15CTSPbF, VS-40L15CT-1PbF

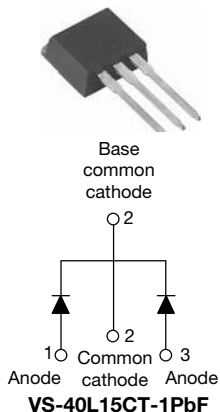
Vishay Semiconductors

### High Performance Schottky Rectifier, 2 x 20 A

TO-263AB (D<sup>2</sup>PAK)



TO-262AA



#### FEATURES

- 125 °C T<sub>J</sub> operation (V<sub>R</sub> < 5 V)
- Center tap module
- Optimized for OR-ing applications
- Ultralow forward voltage drop
- High frequency operation
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- Guard ring for enhanced ruggedness and long term reliability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified
- Material categorization: for definitions of compliance please see [www.vishay.com/doc?99912](http://www.vishay.com/doc?99912)



**RoHS**  
COMPLIANT  
HALOGEN  
**FREE**

PRODUCT SUMMARY	
Package	TO-263AB (D <sup>2</sup> PAK), TO-262AA
I <sub>F(AV)</sub>	2 x 20 A
V <sub>R</sub>	15 V
V <sub>F</sub> at I <sub>F</sub>	see datasheet
I <sub>RM</sub> max.	600 mA at 100 °C
T <sub>J</sub> max.	125 °C
E <sub>AS</sub>	10 mJ
Diode variation	Common cathode

#### DESCRIPTION

The center tap Schottky rectifier module has been optimized for ultralow forward voltage drop specifically for the OR-ing of parallel power supplies. The proprietary barrier technology allows for reliable operation up to 125 °C junction temperature. Typical applications are in parallel switching power supplies, converters, reverse battery protection, and redundant power subsystems.

MAJOR RATINGS AND CHARACTERISTICS			
SYMBOL	CHARACTERISTICS	VALUES	UNITS
I <sub>F(AV)</sub>	Rectangular waveform	40	A
V <sub>RRM</sub>		15	V
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	700	A
V <sub>F</sub>	19 A <sub>pk</sub> , T <sub>J</sub> = 125 °C (per leg, typical)	0.25	V
T <sub>J</sub>		-55 to +125	°C

VOLTAGE RATINGS				
PARAMETER	SYMBOL	TEST CONDITIONS	VS-40L15CTSPbF VS-40L15CT-1PbF	UNITS
Maximum DC reverse voltage	V <sub>R</sub>	T <sub>J</sub> = 100 °C	15	V
Maximum working peak reverse voltage	V <sub>RWM</sub>			

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current, see fig. 5	I <sub>F(AV)</sub>	50 % duty cycle at T <sub>C</sub> = 85 °C, rectangular waveform		20	A
				40	
Maximum peak one cycle non-repetitive surge current per leg, see fig. 7	I <sub>FSM</sub>	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated V <sub>RRM</sub> applied	700	
		10 ms sine or 6 ms rect. pulse		330	
Non-repetitive avalanche energy per leg	E <sub>AS</sub>	T <sub>J</sub> = 25 °C, I <sub>AS</sub> = 2 A, L = 6 mH		10	mJ
Repetitive avalanche current per leg	I <sub>AR</sub>	Current decaying linearly to zero in 1 μs Frequency limited by T <sub>J</sub> maximum V <sub>A</sub> = 1.5 x V <sub>R</sub> typical		2	A



www.vishay.com

## VS-40L15CTSPbF, VS-40L15CT-1PbF

Vishay Semiconductors

ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS		TYP.	MAX.	UNITS
Maximum forward voltage drop per leg See fig. 1	$V_{FM}^{(1)}$	19 A	$T_J = 25\text{ }^\circ\text{C}$	-	0.41	V
		40 A		-	0.52	
		19 A	$T_J = 125\text{ }^\circ\text{C}$	0.25	0.33	
		40 A		0.37	0.50	
Reverse leakage current per leg See fig. 2	$I_{RM}^{(1)}$	$T_J = 25\text{ }^\circ\text{C}$	$V_R = \text{Rated } V_R$	-	10	mA
		$T_J = 100\text{ }^\circ\text{C}$		-	600	
Threshold voltage	$V_{F(TO)}$	$T_J = T_J \text{ maximum}$		0.182		V
Forward slope resistance	$r_f$			7.6		m $\Omega$
Maximum junction capacitance per leg	$C_T$	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz), 25 $^\circ\text{C}$		-	2000	pF
Typical series inductance per leg	$L_S$	Measured lead to lead 5 mm from package body		8	-	nH
Maximum voltage rate of change	dV/dt	Rated $V_R$		10 000		V/ $\mu\text{s}$

### Note

(1) Pulse width < 300  $\mu\text{s}$ , duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction temperature range	$T_J$		-55 to +125	$^\circ\text{C}$
Maximum storage temperature range	$T_{Stg}$		-55 to +150	
Maximum thermal resistance, junction to case per leg	$R_{thJC}$	DC operation See fig. 4	1.5	$^\circ\text{C/W}$
Typical thermal resistance, case to heatsink	$R_{thCS}$	Mounting surface, smooth and greased	0.50	
Maximum thermal resistance, junction to ambient	$R_{thJA}$	DC operation	40	
Approximate weight			2	g
			0.07	oz.
Mounting torque	minimum	Non-lubricated threads	6 (5)	kgf · cm (lbf · in)
	maximum		12 (10)	
Marking device		Case style TO-263AB (D <sup>2</sup> PAK)	40L15CTS	
		Case style TO-262AA	40L15CT-1	



www.vishay.com

# VS-40L15CTSPbF, VS-40L15CT-1PbF

Vishay Semiconductors

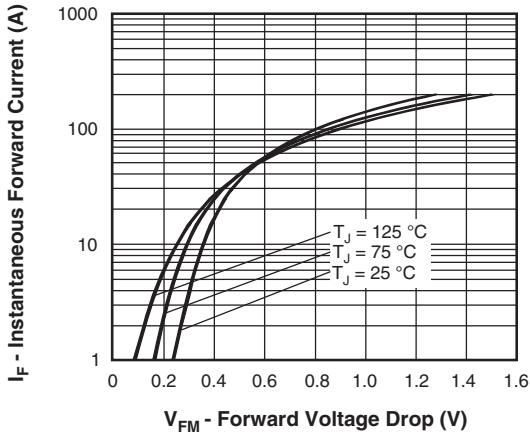


Fig. 1 - Maximum Forward Voltage Drop Characteristics

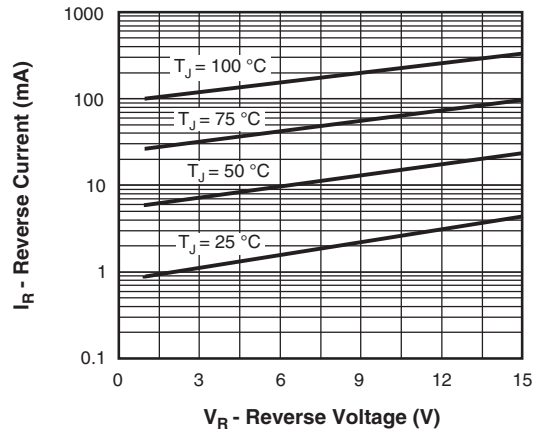


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

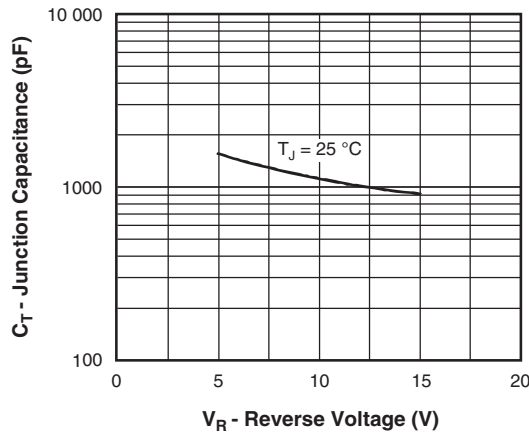


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

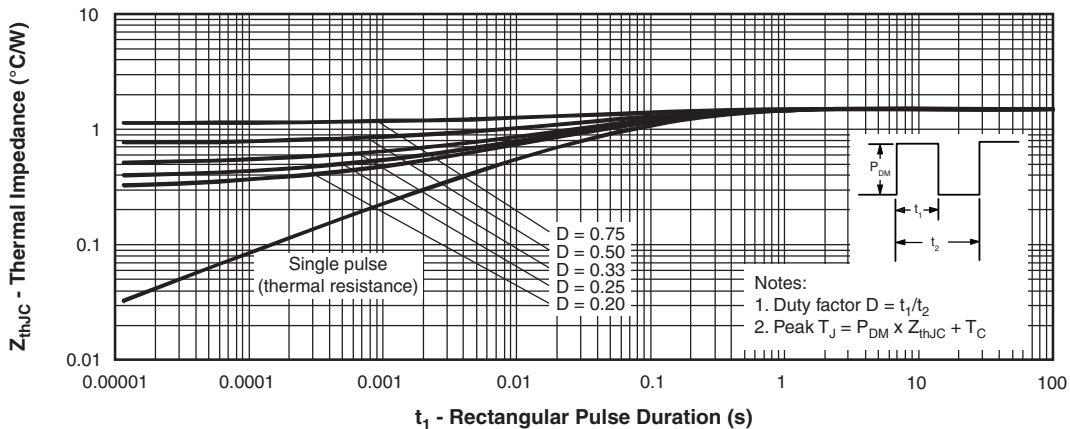


Fig. 4 - Maximum Thermal Impedance  $Z_{thJC}$  Characteristics



www.vishay.com

**VS-40L15CTSPbF, VS-40L15CT-1PbF**

Vishay Semiconductors

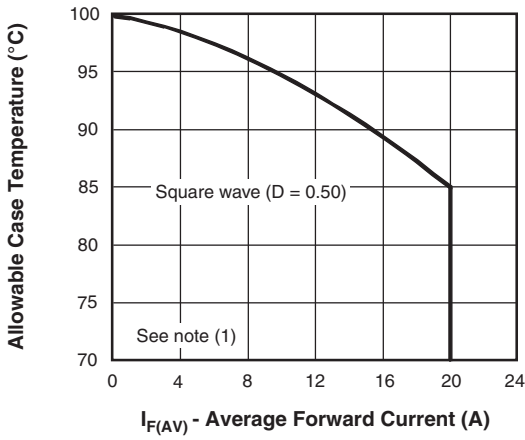


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

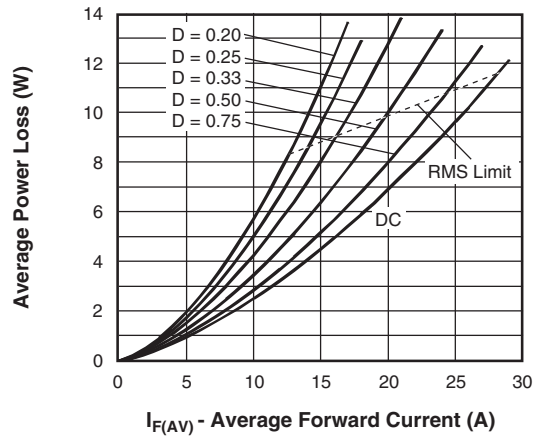


Fig. 6 - Forward Power Loss Characteristics

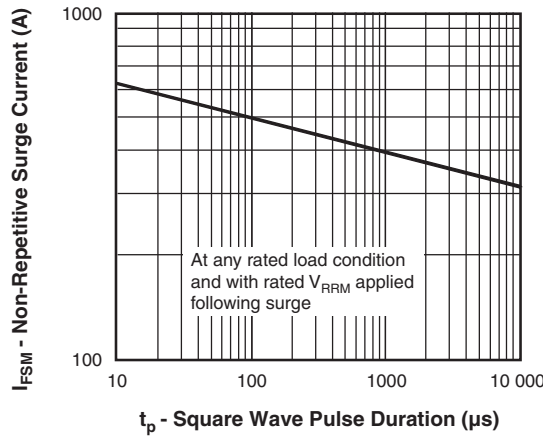


Fig. 7 - Maximum Non-Repetitive Surge Current

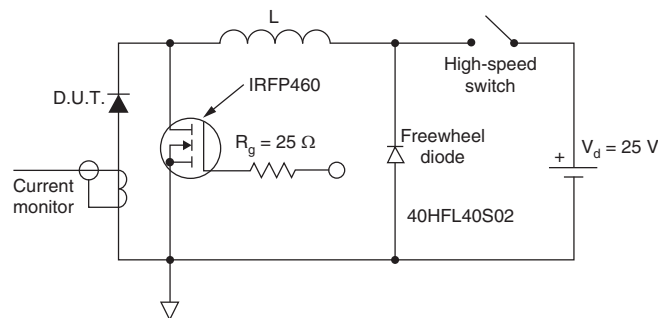


Fig. 8 - Unclamped Inductive Test Circuit

**Note**

- (1) Formula used:  $T_C = T_J - (P_d + P_{dREV}) \times R_{thJC}$ ;
- $P_d$  = Forward power loss =  $I_{F(AV)} \times V_{FM}$  at  $(I_{F(AV)}/D)$  (see fig. 6);
- $P_{dREV}$  = Inverse power loss =  $V_{R1} \times I_R (1 - D)$ ;  $I_R$  at  $V_{R1} = 80\%$  rated  $V_R$



www.vishay.com

## VS-40L15CTSPbF, VS-40L15CT-1PbF

Vishay Semiconductors

### ORDERING INFORMATION TABLE

Device code	<b>VS-</b>	<b>40</b>	<b>L</b>	<b>15</b>	<b>C</b>	<b>T</b>	<b>S</b>	<b>TRL</b>	<b>PbF</b>
	①	②	③	④	⑤	⑥	⑦	⑧	⑨

- 1** - Vishay Semiconductors product
- 2** - Current rating (40 A)
- 3** - L = Schottky "L" series
- 4** - Voltage rating (15 V)
- 5** - C = common cathode
- 6** - T = TO-220
- 7** -
  - S = D<sup>2</sup>PAK
  - -1 = TO-262
- 8** -
  - None = tube
  - TRL = tape and reel (left oriented - for D<sup>2</sup>PAK only)
  - TRR = tape and reel (right oriented - for D<sup>2</sup>PAK only)
- 9** - PbF = lead (Pb)-free

ORDERING INFORMATION (Example)			
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION
VS-40L15CTSPbF	50	1000	Antistatic plastic tubes
VS-40L15CTSTRRPbF	800	800	13" diameter plastic tape and reel
VS-40L15CTSTRLPbF	800	800	13" diameter plastic tape and reel
VS-40L15CT-1PbF	50	1000	Antistatic plastic tubes

LINKS TO RELATED DOCUMENTS	
Dimensions	TO-263AB (D <sup>2</sup> PAK) <a href="http://www.vishay.com/doc?95046">www.vishay.com/doc?95046</a>
	TO-262AA <a href="http://www.vishay.com/doc?95419">www.vishay.com/doc?95419</a>
Part marking information	<a href="http://www.vishay.com/doc?95008">www.vishay.com/doc?95008</a>
Packaging information	<a href="http://www.vishay.com/doc?95032">www.vishay.com/doc?95032</a>



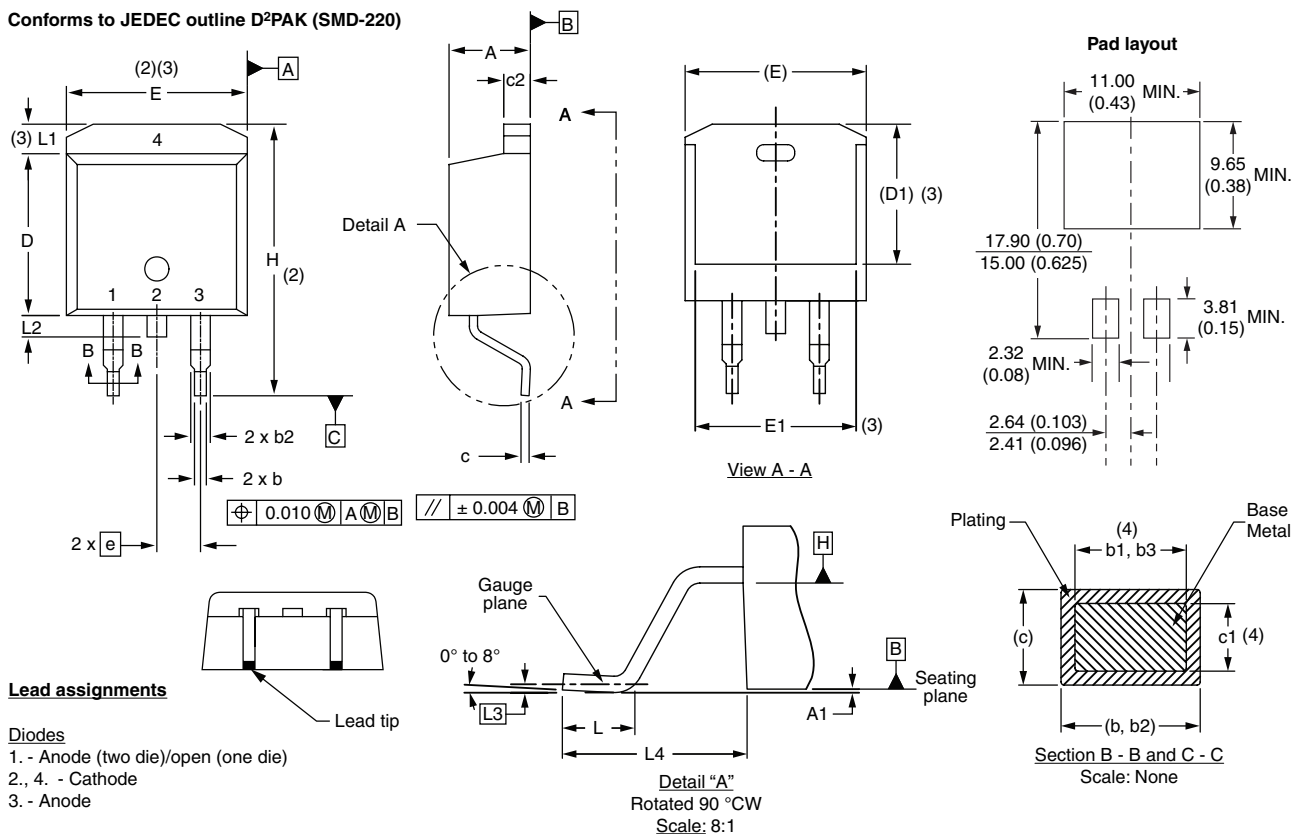
# Outline Dimensions

## Vishay High Power Products

### D<sup>2</sup>PAK, TO-262

#### DIMENSIONS FOR D<sup>2</sup>PAK in millimeters and inches

Conforms to JEDEC outline D<sup>2</sup>PAK (SMD-220)



SYMBOL	MILLIMETERS		INCHES		NOTES
	MIN.	MAX.	MIN.	MAX.	
A	4.06	4.83	0.160	0.190	
A1	0.00	0.254	0.000	0.010	
b	0.51	0.99	0.020	0.039	
b1	0.51	0.89	0.020	0.035	4
b2	1.14	1.78	0.045	0.070	
b3	1.14	1.73	0.045	0.068	4
c	0.38	0.74	0.015	0.029	
c1	0.38	0.58	0.015	0.023	4
c2	1.14	1.65	0.045	0.065	
D	8.51	9.65	0.335	0.380	2
D1	6.86	8.00	0.270	0.315	3
E	9.65	10.67	0.380	0.420	2, 3
E1	7.90	8.80	0.311	0.346	3
e	2.54 BSC		0.100 BSC		
H	14.61	15.88	0.575	0.625	
L	1.78	2.79	0.070	0.110	
L1	-	1.65	-	0.066	3
L2	1.27	1.78	0.050	0.070	
L3	0.25 BSC		0.010 BSC		
L4	4.78	5.28	0.188	0.208	

#### Notes

- (1) Dimensioning and tolerancing per ASME Y14.5 M-1994
- (2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body
- (3) Thermal pad contour optional within dimension E, L1, D1 and E1
- (4) Dimension b1 and c1 apply to base metal only
- (5) Datum A and B to be determined at datum plane H
- (6) Controlling dimension: inch
- (7) Outline conforms to JEDEC outline TO-263AB

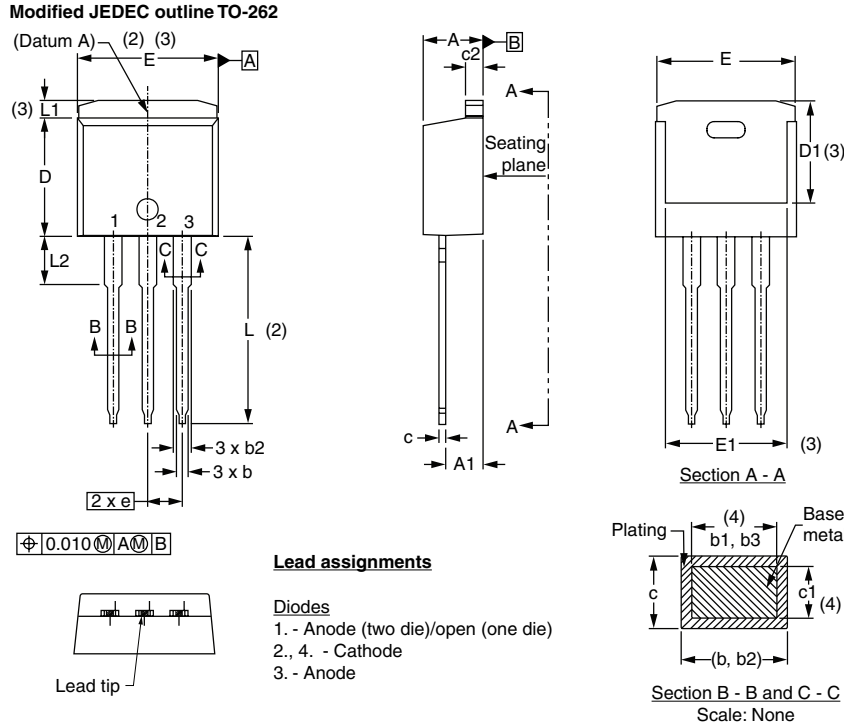
# Outline Dimensions

Vishay High Power Products

D<sup>2</sup>PAK, TO-262



## DIMENSIONS FOR TO-262 in millimeters and inches



SYMBOL	MILLIMETERS		INCHES		NOTES
	MIN.	MAX.	MIN.	MAX.	
A	4.06	4.83	0.160	0.190	
A1	2.03	3.02	0.080	0.119	
b	0.51	0.99	0.020	0.039	4
b1	0.51	0.89	0.020	0.035	4
b2	1.14	1.78	0.045	0.070	
b3	1.14	1.73	0.045	0.068	4
c	0.38	0.74	0.015	0.029	
c1	0.38	0.58	0.015	0.023	4
c2	1.14	1.65	0.045	0.065	
D	8.51	9.65	0.335	0.380	2
D1	6.86	8.00	0.270	0.315	3
E	9.65	10.67	0.380	0.420	2, 3
E1	7.90	8.80	0.311	0.346	3
e	2.54 BSC		0.100 BSC		
L	13.46	14.10	0.530	0.555	
L1	-	1.65	-	0.065	3
L2	3.56	3.71	0.140	0.146	

**Notes**

- (1) Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body
- (3) Thermal pad contour optional within dimension E, L1, D1 and E1
- (4) Dimension b1 and c1 apply to base metal only
- (5) Controlling dimension: inches
- (6) Outline conform to JEDEC TO-262 except A1 (maximum), b (minimum) and D1 (minimum) where dimensions derived the actual package outline





## Disclaimer

ALL PRODUCT, PRODUCT SPECIFICATIONS AND DATA ARE SUBJECT TO CHANGE WITHOUT NOTICE TO IMPROVE RELIABILITY, FUNCTION OR DESIGN OR OTHERWISE.

Vishay Intertechnology, Inc., its affiliates, agents, and employees, and all persons acting on its or their behalf (collectively, "Vishay"), disclaim any and all liability for any errors, inaccuracies or incompleteness contained in any datasheet or in any other disclosure relating to any product.

Vishay makes no warranty, representation or guarantee regarding the suitability of the products for any particular purpose or the continuing production of any product. To the maximum extent permitted by applicable law, Vishay disclaims (i) any and all liability arising out of the application or use of any product, (ii) any and all liability, including without limitation special, consequential or incidental damages, and (iii) any and all implied warranties, including warranties of fitness for particular purpose, non-infringement and merchantability.

Statements regarding the suitability of products for certain types of applications are based on Vishay's knowledge of typical requirements that are often placed on Vishay products in generic applications. Such statements are not binding statements about the suitability of products for a particular application. It is the customer's responsibility to validate that a particular product with the properties described in the product specification is suitable for use in a particular application. Parameters provided in datasheets and / or specifications may vary in different applications and performance may vary over time. All operating parameters, including typical parameters, must be validated for each customer application by the customer's technical experts. Product specifications do not expand or otherwise modify Vishay's terms and conditions of purchase, including but not limited to the warranty expressed therein.

Except as expressly indicated in writing, Vishay products are not designed for use in medical, life-saving, or life-sustaining applications or for any other application in which the failure of the Vishay product could result in personal injury or death. Customers using or selling Vishay products not expressly indicated for use in such applications do so at their own risk. Please contact authorized Vishay personnel to obtain written terms and conditions regarding products designed for such applications.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document or by any conduct of Vishay. Product names and markings noted herein may be trademarks of their respective owners.