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<u>Vishay Semiconductor/Diodes Division</u> <u>MBR20100CTPBF</u>

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Distributor of Vishay Semiconductor/Diodes Division: Excellent Integrated System Limite

Datasheet of MBR20100CTPBF - DIODE ARRAY SCHOTTKY 100V TO220

Contact us: sales@integrated-circuit.com Website: www.integrated-circuit.com



VS-MBRB20...CTPbF, VS-MBR20...CT-1PbF Series

Vishay High Power Products

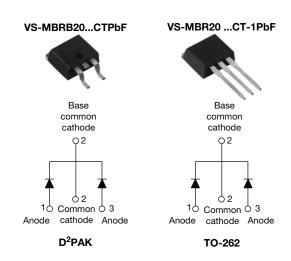
RoHS

COMPLIANT

HALOGEN

FREE

Schottky Rectifier, 2 x 10 A



PRODUCT SUMMARY					
I _{F(AV)}	2 x 10 A				
V_{R}	80 V to 100 V				

FEATURES

- 150 °C T_J operation
- · Low forward voltage drop
- High frequency operation
- Center tap D²PAK and TO-262 packages
- High purity, high temperature encapsulation for enhanced mechanical strength and moisture resistance
- Guard ring for enhanced ruggedness and long term reliability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Halogen-free according to IEC 61249-2-21 definition
- Compliant to RoHS directive 2002/95/EC
- AEC-Q101 qualified

DESCRIPTION

This center tap Schottky rectifier has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS							
SYMBOL	CHARACTERISTICS	VALUES	UNITS				
I _{F(AV)}	Rectangular waveform (per device)	20	^				
I _{FRM}	T _C = 133 °C (per leg)	20	Α				
V _{RRM}		80 to 100	V				
I _{FSM}	t _p = 5 μs sine	850	А				
V _F	10 Apk, T _J = 125 °C	0.70	V				
T _J	Range	- 65 to 150	°C				

VOLTAGE RATINGS								
PARAMETER	SYMBOL			VS-MBRB20100CTPbF VS-MBR20100CT-1PbF	UNITS			
Maximum DC reverse voltage V _R		80	90	100	W			
Maximum working peak reverse voltage	V_{RWM}	60	90	100	V			

ABSOLUTE MAXIMUM RATINGS							
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS			
Maximum average per leg	1	T _C = 133 °C, rated V _B	10				
forward current per device	I _{F(AV)}	$I_C = 133$ C, rated V_R	20				
Peak repetitive forward current per leg	I _{FRM}	Rated V _R , square wave, 20 kHz, T _C = 133 °C	20				
Non-repetitive peak surge current		5 μs sine or Following any rated load ondition 3 μs rect. pulse and with rated V _{RRM} applied	850	А			
Non-repetitive peak surge current	I _{FSM}	Surge applied at rated load conditions halfwave, single phase, 60 Hz	150				
Peak repetitive reverse surge current	I _{RRM}	2.0 μs, 1.0 kHz	0.5				
Non-repetitive avalanche energy per leg	E _{AS}	$T_J = 25 ^{\circ}\text{C}, I_{AS} = 2 \text{A}, L = 12 \text{mH}$	24	mJ			

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For technical questions, contact: diodestech@vishay.com

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ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CO	VALUES	UNITS		
		10 A	T. = 25 °C	0.80	V	
Maximum forward voltage drop	V _{FM} ⁽¹⁾	20 A	1J=25 C	0.95		
Maximum forward voltage drop	VFM (1)	10 A	T _{.1} = 125 °C	0.70		
		20 A	- IJ = 125 C	0.85		
Maximum instantaneous	I _{RM} ⁽¹⁾	T _J = 25 °C	Rated DC voltage	0.10	- mA	
reverse current		T _J = 125 °C	hated DC voltage	6		
Threshold voltage	V _{F(TO)}	$T_{.1} = T_{.1}$ maximum		0.433	V	
Forward slope resistance	r _t	ı ij = ij maximum	15.8	mΩ		
Maximum junction capacitance	C _T	V _R = 5 V _{DC} (test signal ran	ge 100 kHz to 1 MHz), 25 °C	400	pF	
Typical series inductance	L _S	Measured from top of terr	8.0	nH		
Maximum voltage rate of change	dV/dt	Rated V _R 10 000				

Note

 $^{^{(1)}\,}$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS			
Maximum junction temperature range	TJ		- 65 to 150	°C			
Maximum storage temperature range	T _{Stg}		- 65 to 175)			
Maximum thermal resistance, junction to case per leg	R _{thJC}	DC operation	2.0				
Typical thermal resistance, case to heatsink	R _{thCS}	Mounting surface, smooth and greased	0.50	°C/W			
Maximum thermal resistance, junction to ambient	R _{thJA}	DC operation	50				
Annua vimata wa inht			2	g			
Approximate weight			0.07	oz.			
Mounting torque minimum		Non-lubricated threads	6 (5)	kgf · cm			
Mounting torque maximum		Non-lubricated tiffeads	12 (10)	(lbf \cdot in)			
Modeling daying		Case style D ² PAK	MBRB2	0100CT			
Marking device		Case style TO-262	MBR201	100CT-1			

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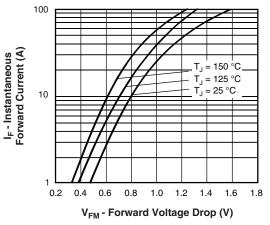


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

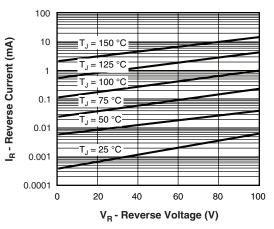


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

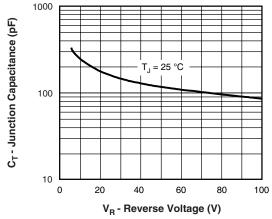


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

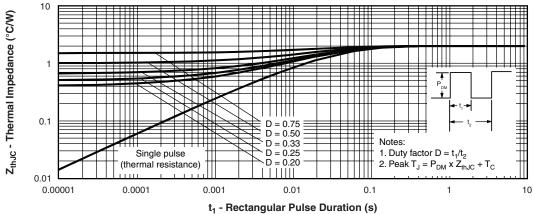


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

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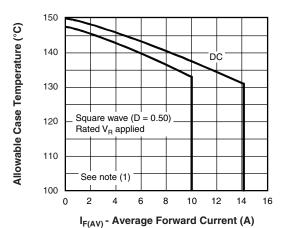


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

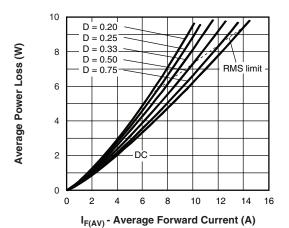


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

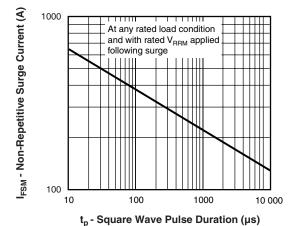


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

Note

 $^{(1)}$ Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC};$ $Pd = Forward power loss = I_{F(AV)} \times V_{FM}$ at (I_{F(AV)}/D) (see fig. 6); $Pd_{REV} = Inverse$ power loss = $V_{R1} \times I_R$ (1 - D); I_R at $V_{R1} = Rated \ V_R$

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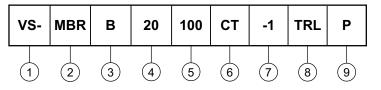


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Schottky Rectifier, 2 x 10 A Vishay High Power Products

ORDERING INFORMATION TABLE

Device code



1 - HPP product suffix

2 - Essential part number

B = D²PAK
 None
 None = TO-262
 | 7 | = -1

4 - Current rating (20 = 20 A) 80 = 80 V 5 - Voltage ratings 90 = 90 V

5 - Voltage ratings 90 = 90 V 100 = 100 V

7 - • None = D²PAK 3 = B • -1 = TO-262 3 None

8 - • None = Tube (50 pieces)

• TRL = Tape and reel (left oriented - for D²PAK only)

• TRR = Tape and reel (right oriented - for D²PAK only)

9 - • PbF = Lead (Pb)-free (for TO-262 and D²PAK tube)

• P = Lead (Pb)-free (for D²PAK TRR and TRL)

LINKS TO RELATED DOCUMENTS							
Dimensions	www.vishay.com/doc?95014						
Part marking information	www.vishay.com/doc?95008						
Packaging information	www.vishay.com/doc?95032						

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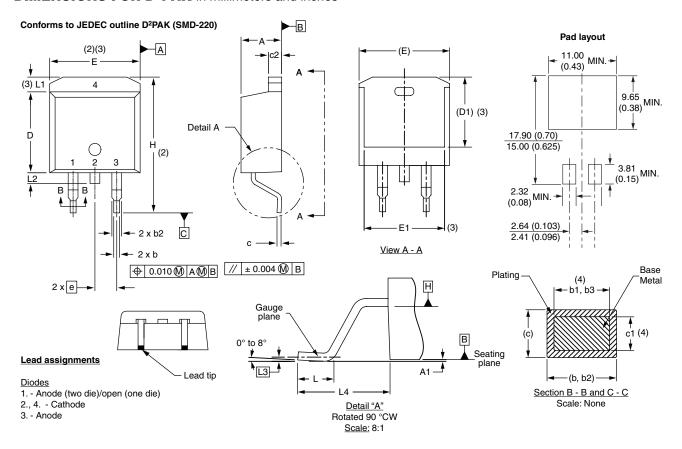


Outline Dimensions

Vishay High Power Products

D²PAK, **TO-262**

DIMENSIONS FOR D²PAK in millimeters and inches



SYMBOL	MILLIM	ETERS	INC	HES	IES NOTES		SYMBOL	MILLIMETERS		INCHES		NOTES
STIVIBUL	MIN.	MAX.	MIN.	MAX.	NOTES		STINIBUL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.06	4.83	0.160	0.190			D1	6.86	8.00	0.270	0.315	3
A1	0.00	0.254	0.000	0.010			Е	9.65	10.67	0.380	0.420	2, 3
b	0.51	0.99	0.020	0.039			E1	7.90	8.80	0.311	0.346	3
b1	0.51	0.89	0.020	0.035	4		е	2.54 BSC		0.100 BSC		
b2	1.14	1.78	0.045	0.070			Н	14.61	15.88	0.575	0.625	
b3	1.14	1.73	0.045	0.068	4		L	1.78	2.79	0.070	0.110	
С	0.38	0.74	0.015	0.029			L1	-	1.65	-	0.066	3
c1	0.38	0.58	0.015	0.023	4		L2	1.27	1.78	0.050	0.070	
c2	1.14	1.65	0.045	0.065			L3	0.25	BSC	0.010	BSC	
D	8.51	9.65	0.335	0.380	2		L4	4.78	5.28	0.188	0.208	

Notes

- ⁽¹⁾ Dimensioning and tolerancing per ASME Y14.5 M-1994
- (2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body
- $^{(3)}$ Thermal pad contour optional within dimension E, L1, D1 and E1
- (4) Dimension b1 and c1 apply to base metal only
- (5) Datum A and B to be determined at datum plane H
- (6) Controlling dimension: inch

(7) Outline conforms to JEDEC outline TO-263AB

Document Number: 95014 Revision: 31-Mar-09 For technical questions concerning discrete products, contact: diodes-tech@vishay.com
For technical questions concerning module products, contact: ind-modules@vishay.com

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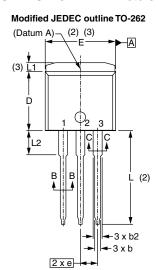
Outline Dimensions

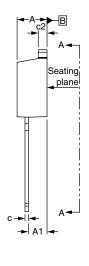
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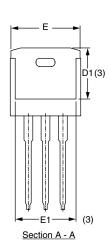
D²PAK, TO-262



DIMENSIONS FOR TO-262 in millimeters and inches







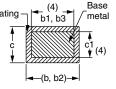
⊕ 0.010 A **M** B

Lead assignments



<u>Diodes</u>
1. - Anode (two die)/open (one die)
2., 4. - Cathode

ode <u>Section B - B and C - C</u>



Scale: None

CVMDOL	MILLIN	METERS	INC	INCHES			
SYMBOL	MIN.	MAX.	MIN.	MAX.	NOTES		
Α	4.06	4.83	0.160	0.190			
A1	2.03	3.02	0.080	0.119			
b	0.51	0.99	0.020	0.039			
b1	0.51	0.89	0.020	0.035	4		
b2	1.14	1.78	0.045	0.070			
b3	1.14	1.73	0.045	0.068	4		
С	0.38	0.74	0.015	0.029			
c1	0.38	0.58	0.015	0.023	4		
c2	1.14	1.65	0.045	0.065			
D	8.51	9.65	0.335	0.380	2		
D1	6.86	8.00	0.270	0.315	3		
E	9.65	10.67	0.380	0.420	2, 3		
E1	7.90	8.80	0.311	0.346	3		
е	2.54 BSC		0.10	0 BSC			
L	13.46	14.10	0.530	0.555			
L1	-	1.65	-	0.065	3		
L2	3.56	3.71	0.140	0.146			

Notes

- (1) Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body
- (3) Thermal pad contour optional within dimension E, L1, D1 and E1
- (4) Dimension b1 and c1 apply to base metal only
- (5) Controlling dimension: inches

(6) Outline conform to JEDEC TO-262 except A1 (maximum), b (minimum) and D1 (minimum) where dimensions derived the actual package outline



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Please note that some Vishay documentation may still make reference to RoHS Directive 2002/95/EC. We confirm that all the products identified as being compliant to Directive 2002/95/EC conform to Directive 2011/65/EU.

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