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Fairchild Semiconductor FSAV331QSCX

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April 2004 Revised August 2005

## FSAV331

## **Dual Channel 4:1 Video Switch**

## **General Description**

The Fairchild video switch FSAV331 is a dual 4:1 high speed video switch which can be configured as either multiplexer or demultiplexer. Low On Resistance allows inputs to be connected to outputs without adding propagation delay or generating additional ground bounce noise.

When the OE Pin is LOW,  $\rm S_0$  and  $\rm S_1$  connect the A Port to the selected B Port output. When the OE Pin is HIGH, the switch is OPEN and a HIGH-Impedance state exists between the two ports.

#### **Features**

- Wide bandwidth: 300 MHz
- -73 dB non adjacent channel crosstalk at 10MHz
- -56 dB Off Isolation at 10MHz
- $3\Omega$  typical On Resistance (R<sub>ON</sub>)
- Low power consumption (3uA maximum)
- Control input: TTL compatible

### **Applications**

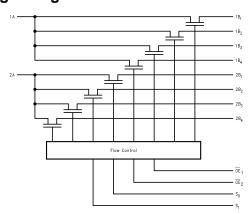
■ Y/C video or CVBS video switch in LCD, plasma, and projector displays

### **Ordering Code:**

Order Number	Package Number	Package Description
FSAV331QSC	MQA16A	16-Lead Quarter Size Small Outline Package (QSOP), JEDEC MO-137, 0.150" Wide
FSAV331MTC	MTC16	16-Lead Thin Shrink Small Outline Package (TSSOP), JEDEC MO-153, 4.4mm Wide

Device also available in Tape and Reel. Specify by appending suffix letter "X" to the ordering code.

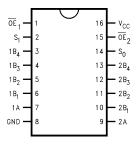
### **Logic Diagram**



### **Pin Descriptions**

Pin Name	Description
$\overline{OE}_1, \overline{OE}_2$	Bus Switch Enables
S <sub>0</sub> , S <sub>1</sub>	Select Inputs
А	Bus A
B <sub>1</sub> , B <sub>2</sub> , B <sub>3</sub> , B <sub>4</sub>	Bus B

### **Connection Diagram**



### **Truth Table**

S <sub>1</sub>	S <sub>0</sub>	OE <sub>1</sub>	OE <sub>2</sub>	Function
Х	Х	Н	Χ	Disconnect 1A
Х	Χ	Χ	Н	Disconnect 2A
L	L	L	L	$A = B_1$
L	Н	L	L	$A = B_2$
Н	L	L	L	$A = B_3$
Н	Н	L	L	$A = B_4$



### Absolute Maximum Ratings(Note 1)

Supply Voltage ( $V_{CC}$ ) -0.5V to +7.0V DC Switch Voltage (Note 2) -0.5V to  $V_{CC}$  + 0.5V DC Input Voltage ( $V_{IN}$ ) (Note 2) -0.5V to +7.0V DC Input Diode Current -50 mA DC Output Current 128 mA Storage Temperature Range ( $T_{STG}$ )  $-65^{\circ}$ C to  $+150^{\circ}$ C ESD (Human Body Model) 2000V

# **Recommended Operating Conditions** (Note 3)

Supply Voltage ( $V_{CC}$ ) 4.75V to 5.25V Control Input Voltage 0V to  $V_{CC}$  Switch Input Voltage 0V to  $V_{CC}$  Operating Temperature  $-40^{\circ}\text{C}$  to  $+85^{\circ}\text{C}$ 

Thermal Resistance

(TSSOP) 100°C/W (QSOP) 127°C/W

**Note 1:** The Absolute Maximum Ratings are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the Electrical Characteristics tables are not guaranteed at the absolute maximum rating. The Recommended Operating Conditions tables will define the conditions for actual device operation.

**Note 2:** The input and output negative voltage ratings may be exceeded if the input and output diode current ratings are observed.

Note 3: Unused control inputs must be held HIGH or LOW. They may not float.

## **DC Electrical Characteristics** All typical value are for V<sub>CC</sub> = 5V @ 25°C unless otherwise specified.

Symbol	Parameter	V <sub>CC</sub>	$T_A = -40 ^{\circ}\text{C}$ to +85 $^{\circ}\text{C}$			Units	Conditions
Symbol		(V)	Min	Тур	Max	Ullits	Conditions
V <sub>ANALOG</sub>	Analog Signal Range	4.75 - 5.25	0		2.0	V	
V <sub>IK</sub>	Clamp Diode Voltage	4.75			-1.2	V	I <sub>IN</sub> = -18 mA
V <sub>IH</sub>	Input Voltage HIGH	4.75 - 5.25	2.0			V	
V <sub>IL</sub>	Input Voltage LOW	4.75 - 5.25			0.8	V	
I <sub>IN</sub>	Control Input Leakage	5.25			±1.0	μА	V <sub>IN</sub> = 0V to V <sub>CC</sub>
I <sub>OZ</sub>	OFF-STATE Leakage Current	5.25			±1.0	μА	$0 \le A, B \le V_{CC}$
R <sub>ON</sub>	Switch On Resistance	4.75		3.3	7.0	Ω	$V_{IN}$ = 1V, $R_L$ = 75 $\Omega$ , $I_{ON}$ = 13 mA
	(Note 4)	4.75		5.0	10.0	Ω	V <sub>IN</sub> = 2V, R <sub>L</sub> = 75Ω, I <sub>ON</sub> = 26 mA
Icc	Quiescent Supply Current	5.25			3.0	μА	V <sub>IN</sub> = 0V or V <sub>CC</sub> , I <sub>OUT</sub> = 0V

**Note 4:** Measured by the voltage drop between A and B Pins at the indicated current through the switch. On Resistance is determined by the lower of the voltages on the two (A or B Ports).



## **AC Electrical Characteristics**

Symbol	Parameter	v <sub>cc</sub>	T <sub>A</sub> = -40 °C to +85 °C		Huita	Conditions	Figure	
Syllibol	Farameter	(V)	Min	Тур	Max	Units	Conditions	Number
t <sub>ON</sub>	Turn ON Time S-to-Bus B	4.75 to 5.25	1.0		5.3		V 7Vfort and V ODEN fort	Figures
	Output Enable Time OE-to-A or B	4.75 to 5.25	1.0		5.3	ns	$V_I = 7V$ for $t_{PZL}$ and $V_I = OPEN$ for $t_{PZH}$	1, 2
t <sub>OFF</sub>	Turn OFF Time S-to-Bus B	4.75 to 5.25	1.0		5.8	no	$V_1 = 7V$ for $t_{PLZ}$ and $V_1 = OPEN$ for $t_{PHZ}$	Figures
	Output Disable Time OE-to-A or B	4.75 to 5.25	1.0		5.5	ns		1, 2
t <sub>PLH</sub> ,	Propagation Delay (Note 5)	4.75 to 5.25			0.1	no	V <sub>I</sub> OPEN	Figures
t <sub>PHL</sub>	Select-to-Bus A Delay	4.75 to 5.25			5.0	ns	VIOPEN	1, 2
DG	Differential Gain	4.75 to 5.25		0.26		%	$R_L = 150\Omega$ , $f = 3.58MHz$	
DP	Differential Phase	4.75 to 5.25		0.23		Degree	$R_L = 150\Omega$ , $f = 3.58MHz$	
O <sub>IRR</sub>	Non Adjacent OFF-Isolation	4.75 to 5.25		-56.0		dB	$f = 10MHz, R_L = 150\Omega$	Figure 3
X <sub>TALK</sub>	Non Adjacent Channel Crosstalk	4.75 to 5.25		-73.0		dB	$R_L = 150\Omega$ , $f = 10MHz$	Figure 4
BW	-3dB Bandwidth	4.75 to 5.25		300		MHz	$R_L = 150\Omega$	Figure 5

Note 5: This specification is guaranteed by design.

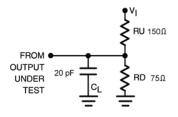
## Capacitance

Symbol	Parameter	T <sub>A</sub> = -40 °C to +85 °C	Units	Conditions
Symbol	Farameter	Тур	Ollits	
C <sub>IN</sub>	Control Pin Input Capacitance	3.0	pF	V <sub>CC</sub> = 5.0V
C <sub>ON</sub>	A/B On Capacitance	39.0	pF	V <sub>CC</sub> = 5.0, <del>OE</del> = 0V
C <sub>OFF</sub>	Port B OFF Capacitance	5.0	pF	$V_{CC}$ and $\overline{OE} = 5.0V$
	Port A OFF Capacitance	13.0	pF	VCC and OE = 5.0V

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# **AC Loading and Waveforms**

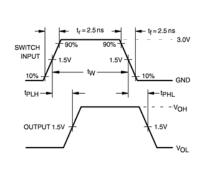


Note: Input driven by 50  $\Omega$  source terminated in 50  $\Omega$ 

Note: C<sub>L</sub> includes load and stray capacitance

Note: Input PRR = 1.0 MHz,  $t_W = 500 \text{ ns}$ 

FIGURE 1. AC Test Circuit



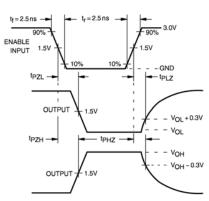


FIGURE 2. AC Waveforms

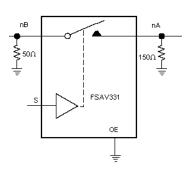


FIGURE 3. OFF Isolation Test

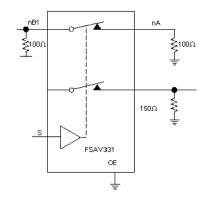


FIGURE 4. Crosstalk Test

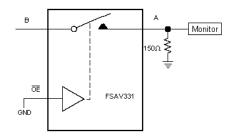
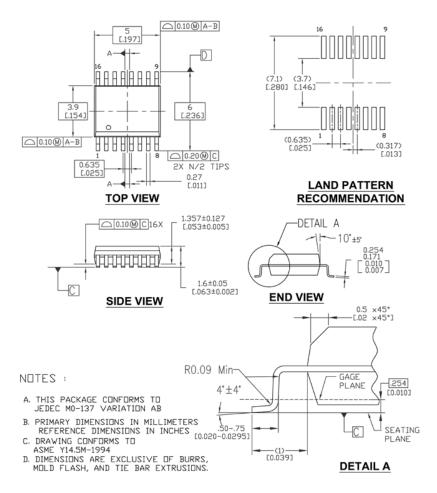


FIGURE 5. Bandwidth Test



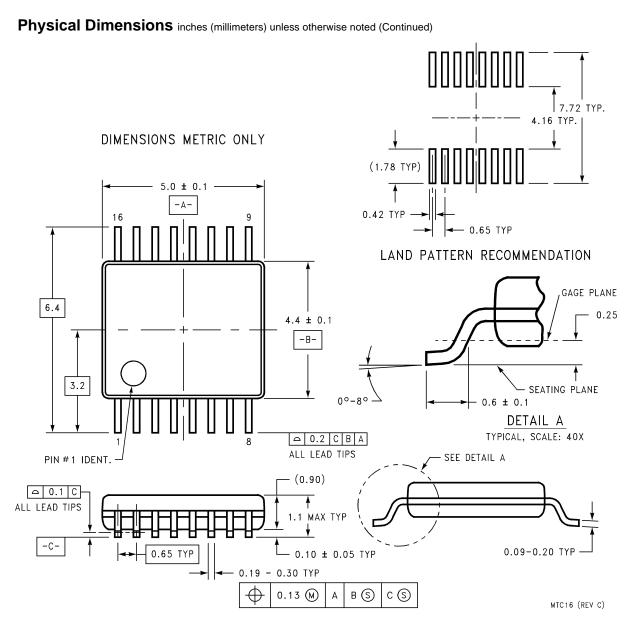
### Physical Dimensions inches (millimeters) unless otherwise noted



MQA16AREVB

16-Lead Quarter Size Small Outline Package (QSOP), JEDEC MO-137, 0.150" Wide Package Number MQA16A

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# 16-Lead Thin Shrink Small Outline Package (TSSOP), JEDEC MO-153, 4.4mm Wide Package Number MTC16

### **Technology Description**

The Fairchild Switch family derives from and embodies Fairchild's proven switch technology used for several years in its 74LVX3L384 (FST3384) bus switch product.



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Datasheet Identification	Product Status	Definition			
-		This datasheet contains the design specifications for product development. Specifications may change in any manner without notice.			
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