

## **Excellent Integrated System Limited**

Stocking Distributor

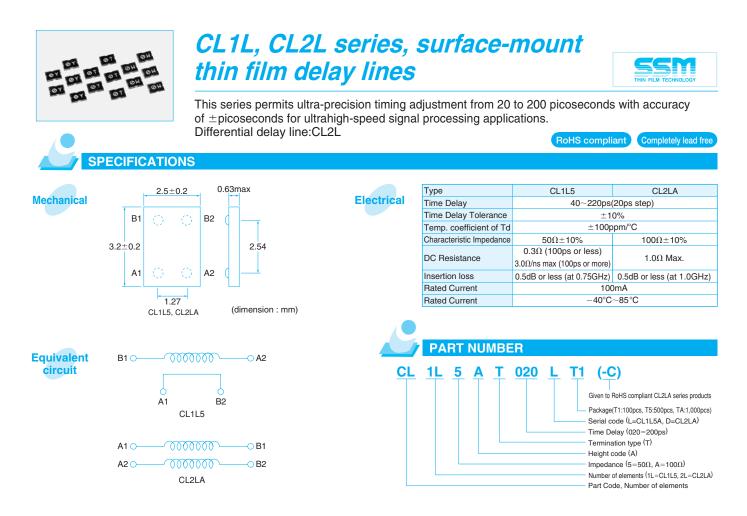
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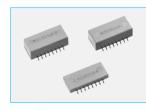
Susumu GL1L5LS010S-C

For any questions, you can email us directly: <u>sales@integrated-circuit.com</u>



**Distributor of Susumu: Excellent Integrated System Limited** Datasheet of GL1L5LS010S-C - IND DELAY LINE 100PS 1 OHM SMD Contact us: sales@integrated-circuit.com Website: www.integrated-circuit.com





## GL1L/GL2L series, SOP(small outline package) thin film differential delay lines

These delay lines offer the same excellent frequency performance as SIP delay lines but come in a gullwinged surface mount package. The differential SMT is useful for PECL application, and contains two identical transmission lines matched for the time delay. Featuring a stripline shielded construction, these parts offer very low EMI/RFI, and are ideal for high



	13.66max			Туре	GL1L	GL2L
Mechanical			Electrical	Time Delay	0.1~5.0ns(0.1ns step)	0.1~3.0ns(0.1ns step) 3.5~4.5ns(0.5ns step)
Equivalent	•MS250SθE			Time Delay Tolerance	±0.05ns	±0.05ns(0.1~2.9ns) −0.5/+0.1ns(3.0ns) ±0.1ns(3.5~4.5ns)
	1 -			Temp. coefficient of Td	0~150	ppm/°C
	1.27 ± 0.15			Characteristic Impedance	50:	±5Ω
	Time I	Delav H Cod		Rise/fall time	200	ps/ns
			e	Rated Currrent	100	DmA
		.0ns 2.37max L		Characteristic Impedance	-25-	~85°C
	20-000000-01	0		PART NUMBE	R	
circuit	20 1 3 9 11 16	0	GI	PART NUMBE	50 <u>S</u> -T* (-C)	Given to RoHS compliant produ Package(T1:100pcs, T5:500pc
	GL2L 20-000000000000000000000000000000000		G		50 <u>S</u> -T* (-C)	Package(T1:100pcs, T5:500pc Circuit : S(GL1L),D(GL2L) Time Delay : (250=2.50ns)
	1 3 9 11 16 GL2L		G		50 <u>S</u> -T∗ (-C)	Package(T1:100pcs, T5:500p Circuit : S(GL1L),D(GL2L)

frequency/tight tolerance timing and deskew applications.