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<u>Fairchild Semiconductor</u> <u>BU407</u>

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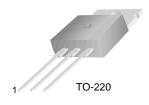




BU407/407H

High Voltage Switching

• Use In Horizontal Deflection Output Stage



1.Base 2.Collector 3.Emitter

NPN Epitaxial Silicon Transistor

Absolute Maximum Ratings T_C=25°C unless otherwise noted

| Symbol | Parameter | Value | Units |
|------------------|--|------------|-------|
| V _{CBO} | Collector-Base Voltage | 330 | V |
| V _{CEO} | Collector-Emitter Voltage | 150 | V |
| V _{EBO} | Emitter-Base Voltage | 6 | V |
| I _C | Collector Current (DC) | 7 | А |
| I _{CP} | Collector Current (Pulse) | 10 | А |
| I _B | Base Current | 4 | А |
| P _C | Collector Dissipation (T _C =25°C) | 60 | W |
| T _J | Junction Temperature | 150 | °C |
| T _{STG} | Storage Temperature | - 65 ~ 150 | °C |

Electrical Characteristics $T_C=25^{\circ}C$ unless otherwise noted

| Symbol | Parameter | Test Condition | Min. | Max. | Units |
|-----------------------|--------------------------------------|--|------|------|-------|
| I _{CES} | Collector Cut-off Current | $V_{CE} = 330V, V_{BE} = 0$ | | 5 | mA |
| | | $V_{CE} = 200V, V_{BE} = 0$ | | 100 | μΑ |
| | | $V_{CE} = 200V, V_{BE} = 0 @ T_{C} = 150^{\circ}C$ | | 1 | mA |
| I _{EBO} | Emitter Cut-off Current | $V_{BE} = 6V, I_{C} = 0$ | | 1 | mA |
| V _{CE} (sat) | Collector-Emitter Saturation Voltage | | | | |
| | : BU407 | $I_C = 5A, I_B = 0.5A$ | | 1 | V |
| | : BU407H | $I_C = 5A, I_B = 0.8A$ | | 1 | V |
| V _{BE} (sat) | Base-Emitter Saturation Voltage | | | | |
| | : BU407 | $I_C = 5A, I_B = 0.5A$ | | 1.2 | V |
| | : BU407H | $I_C = 5A, I_B = 0.8A$ | | 1.2 | V |
| f _T | Current Gain Bandwidth Product | V _{CE} = 10V, I _C = 0.5A | 10 | | MHz |
| t _{OFF} | Turn OFF Time | | | | |
| | : BU407 | $I_C = 5A, I_B = 0.5A$ | | 0.75 | μs |
| | : BU407H | $I_C = 5A, I_B = 0.8A$ | | 0.4 | μs |

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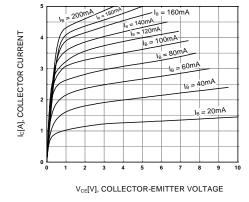


Figure 1. Static Characteristic

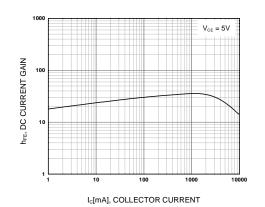


Figure 2. DC current Gain

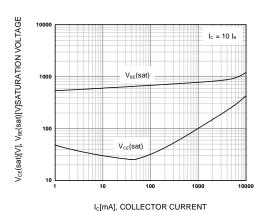


Figure 3. Base-Emitter Saturation Voltage Collector-Emitter Saturation Voltage

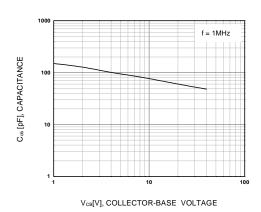


Figure 4. Collector Output Capacitance

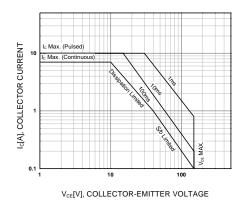


Figure 5. Safe Operating Area

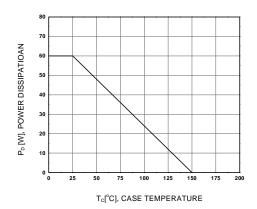
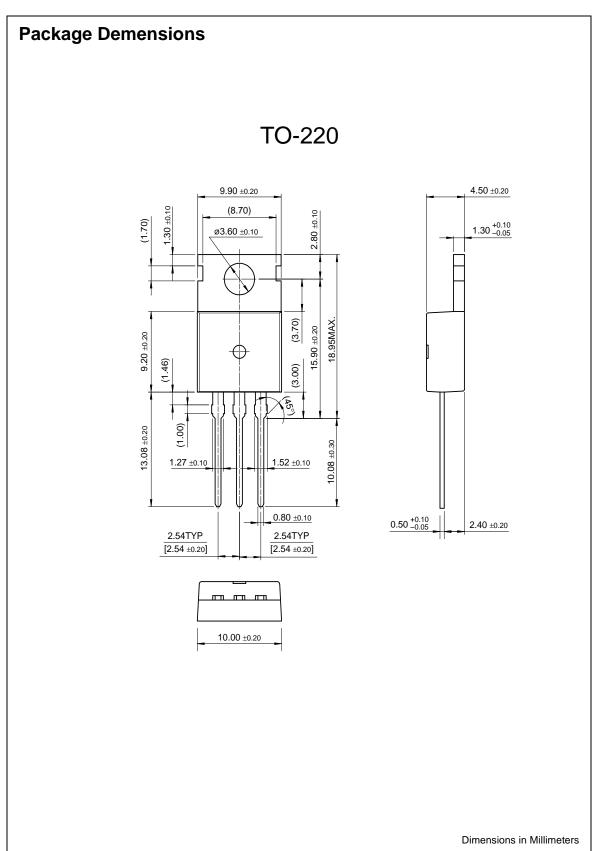


Figure 6. Power Derating

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