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STMicroelectronics T2550H-600TRG

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T2550H

High temperature 25 A Snubberless™ TRIAC

Features

- Medium current TRIAC
- 150 °C max. Tj turn-off commutation
- Low thermal resistance with clip bonding
- Very high 3 quadrant commutation capability
- RoHS (2002/95/EC) compliant package

Applications

Specifically designed for use in high temperature environment (found in hot appliances such as cookers, ovens, hobs, electric heaters, coffee machines).

Description

This 25 A T2550H TRIAC provides an enhanced performance in terms of reduced power loss and thermal dissipation. This allows for the optimization of the heatsinking cimensions, leading to space and cost effectiveness when compared to electro-n echanical solutions.

Based on ST shubberless technology, the T2550H offers high commutation switching capabilities and high noise immunity levels. Thanks to the clip assembly technique, it provides a superior performance in surge current handling.

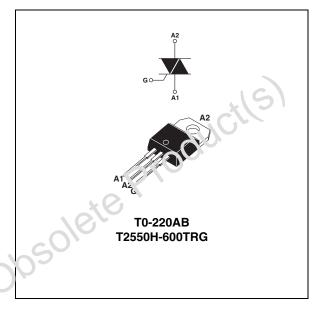


Table 1. Device summary

Symbol	Value	Unit
I _{T(RMS)}	25	А
V _{DRM} /V _{RRM}	600	V
I _{GT (Q1})	50	mA



Characteristics

T2550H

1 Characteristics

Table 2. Absolute maximum ratings

Symbol	Parameter			Value	Unit
I _{T(RMS)}	RMS on-state current (full sine wave)		T _c = 125 °C	25	А
1.	Non repetitive surge peak on-state current	F = 50 Hz	t = 20 ms	250	А
ITSM	(full cycle, T _j initial = 25° C)	F = 60 Hz	t = 16.7 ms	260	~
l ² t	I ² t Value for fusing	t _p = 10 ms		340	A ² s
dl/dt	Critical rate of rise of on-state current I_G = 2 x I_{GT} , $t_r \leq$ 100 ns	F = 120 Hz	T _j = 150 °C	50	A/µs
V _{DSM} /V _{RSM}	Non repetitive surge peak off-state voltage	t _p = 10 ms	T _j = 25 °C	700	51
I _{GM}	Peak gate current	t _p = 20 μs	T _j = 150 °C	4	Α
P _{G(AV)}	Average gate power dissipation		T _j = 150 °C	AU	W
T _{stg} T _j	Storage junction temperature range Operating junction temperature range		20	40 to + 150 40 to + 150	°C

Table 3.Electrical characteristics ($T_i = 25$ °C, unless otherwise specified)

Symbol	Test Conditions	Quartiant		Value	Unit
I _{GT} ⁽¹⁾	$V_{\rm D} = 12 \text{ V} \text{ R}_{\rm I} = 33 \Omega$	Sr-II	MAX.	50	mA
V _{GT}	$A_{D} = 15 A_{D} = 12 A_{D} = 22 25$	1 - 11 - 111	MAX.	1.3	V
V _{GD}	$V_D = V_{DRM}$ $R_L = 3.3 \text{ k}\Omega$ $T_j = 150^{\circ} \text{ C}$	- -	MIN.	0.15	V
I _H ⁽²⁾	I _T = 500 mA		MAX.	75	mA
١L	I _G = 1.2 I _{GT}	- -	MAX.	90	mA
dV/dt ⁽²⁾	$V_{D} = 67\% V_{DRM}$ gous open $T_{j} = 150^{\circ} C$		MIN.	500	V/µs
(dl/dt)c ⁽²⁾	Without snu's $T_j = 150^\circ C$		MIN.	11.1	A/ms

1. minimum I_{GT} is guaranted at 10% of I_{GT} max.

2. for both pole riting of A2 referenced to A1.

Table 4. Static Characteristics

Syrabol	Test Conditions			Value	Unit
V _T ⁽¹⁾	$I_{TM} = 35 \text{ A}$ $t_p = 380 \mu \text{s}$	T _j = 25 °C	MAX.	1.5	V
V _{to} ⁽¹⁾	Threshold voltage	T _j = 150 °C	MAX.	0.80	V
R _d ⁽¹⁾	Dynamic resistance	T _j = 150 °C	MAX.	19	mΩ
	<u> И</u> – И	T _j = 25 °C		5	μA
	$V_{\text{DRM}} = V_{\text{RRM}}$	T _j = 150 °C	MAX.	8.5	m۸
IRRM	$V_{\text{DRM}}/V_{\text{RRM}}$ = 400 V (at mains peak voltage)	T _j = 150 °C		5.5	mA

1. for both polarities of A2 referenced to A1.





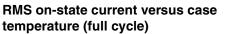
T2550H

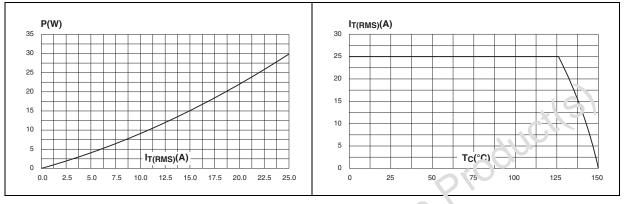
Characteristics

Table 5.Thermal resistance

Symbol	Parameter	Value	Unit
R _{th(j-c)}	Junction to case (AC)	0.8	°C/W

Figure 1. Maximum power dissipation versus Figure 2. RMS on-state current (full cycle)





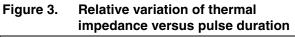


Figure 4. On-state characteristics (maximum values)

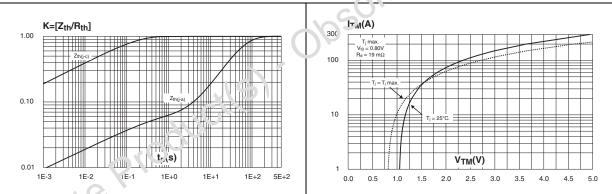
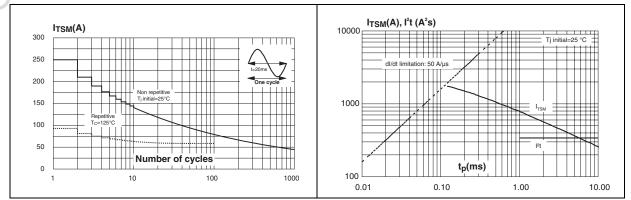


Figure 5.

Surge peak on-state current versus Figure 6. number of cycles

Non-repetitive surge peak on-state current for a sinusoidal pulse with width $t_p < 10$ ms and corresponding value of l^2t



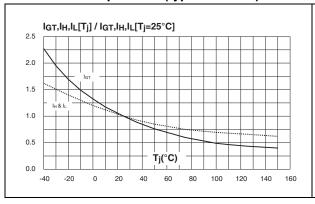


Characteristics

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Figure 7. Relative variation of gate trigger current, holding current and latching current versus junction temperature (typical values)

Figure 8. Relative variation of critical rate of decrease of main current versus (dV/dt)c (typical values)



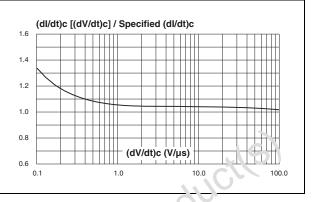
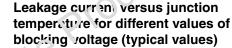


Figure 9. Relative variation of critical rate of Figure 10. decrease of main current versus junction temperature



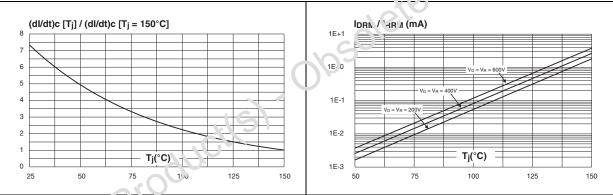
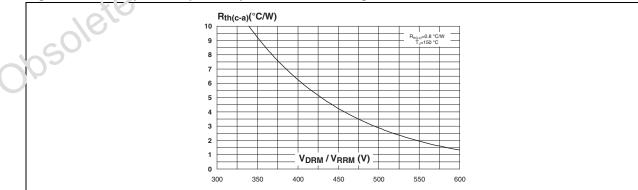


Figure 11. Acceptable repetitive peak off-state voltage versus case-ambient thermal resistance

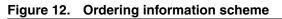




T2550H

2 Ordering information scheme

obsolete Product(s)



Triac series	T 25 50 H - 600 T RG
Current	
25 = 25 A	
Sensitivity	
50 = 50 mÅ	
Temperature	
H = High	
Voltage	
600 = 600 V	
Package	
T = TO-220AB	
Packing mode	
RG = Tube	lete





Package information

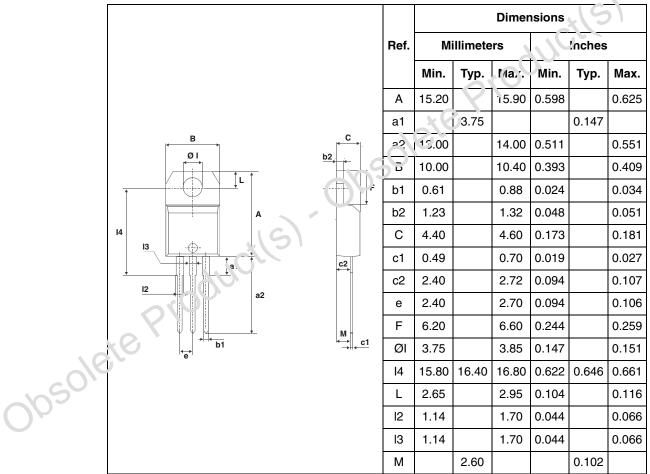
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3 Package information

- Epoxy meets UL94, V0
- Recommended torque 0.4 to 0.6 N·m

In order to meet environmental requirements, ST offers these devices in ECOPACK[®] packages. These packages have a lead-free second level interconnect. The category of second level interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available at *www.st.com*.

Table 6. TO-220AB Dimensions





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Ordering information

4 Ordering information

Table 7. Ordering information

Order code	Marking	Package	Weight	Base qty	Delivery mode
T2550H-600TRG	T2550H600T	TO-220AB	2.3 g	50	Tube

5 Revision history

	Date	Revision	Changes
A	Apr-2002	5A	Last update.
13	-Feb-2006	6	TO-220AB delivery mode changed frc.n Lulk to tube. ECOPACK statement added.
20	-Jun-2006	7	Reformatted to current strandaros. Figures 6 and 11 replaced.
27-	-May-2008	8	Reformatted to current standards. ECOPACK statement updated.
olete		an ctl	5)





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