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STMicroelectronics TS821AILT

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1.225V micropower shunt voltage reference

Features

- 1.225V typical output voltage
- Ultra low operating current: 45µA maximum at 25°C
- High precision @ 25°C
 - +/- 2% (standard version)
 - +/- 1% (A grade)
 - +/- 0.5% (B grade)
- High stability when used with capacitive loads
- Industrial temperature range: -40°C to +85°C
- 120ppm/°C maximum temperature coefficient

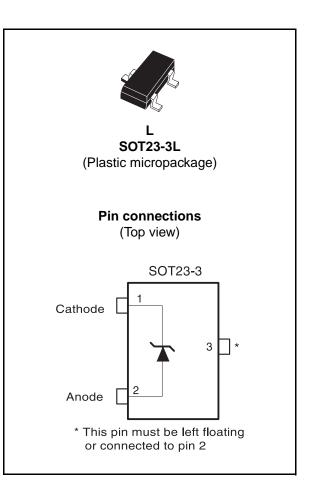
Applications

- Computers
- Instrumentation
- Battery chargers
- Switch mode power supply
- Battery operated equipment

Description

The TS821 is a low power shunt voltage reference providing a stable 1.225V output voltage over the industrial temperature range (-40°C to +85°C). Availabe in SOT23-3 surface mount package, it can be designed in applications where space saving is critical.

The low operating current is a key advantage for power restricted designs. In addition, the TS821 is very stable and can be used in a broad range of application conditions.





Absolute maximum ratings and operating conditions

TS821

1 Absolute maximum ratings and operating conditions

Symbol	Parameter	Value	Unit	
۱ _k	Reverse breakdown current	20	mA	
۱ _f	Forward current	10	mA	
Pd	Power dissipation ⁽¹⁾ SOT23-3	360	mW	
T _{stg}	Storage temperature	-65 to +150	°C	
ESD	Human body model (HBM) ⁽²⁾	2	kV	
E3D	Machine model (MM) ⁽³⁾	200	V	
T _{lead}	Lead temperature (soldering, 10 seconds)	260	°C	

1. P_d is calculated with $T_{amb} = 25^{\circ}C$ and $T_j = 150^{\circ}C$ and $R_{thja} = 340^{\circ}C/W$ for the SOT23-3L package.

2. Human body model: 100pF discharged through a $1.5k\Omega$ resistor between two pins of the device, done for all couples of pin combinations with other pins floating.

3. Machine model: a 200pF cap is charged to the specified voltage, then discharged directly between two pins of the device with no external series resistor (internal resistor < 5Ω), done for all couples of pin combinations with other pins floating.

Table 2.Operating conditions

Symbol	Parameter	Value	Unit
I _{min}	Minimum operating current	45	μΑ
I _{max}	Maximum operating current	12	mA
T _{oper}	Operating free air temperature range	-40 to +85	°C

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Electrical characteristics

2 Electrical characteristics

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
	Reverse breakdown voltage	I _k = 100μA	1.200	1.225	1.250	V
V _k	Reverse breakdown voltage tolerance	$I_{k} = 100\mu A$ -40°C < T _{amb} < +85°C	-25 +34		+25 +34	mV
1	Minimum operating current	$T_{amb} = 25^{\circ}C$		40	45	
I _{k-min}		-40°C < T _{amb} < +85°C			50	μA
$\Delta V_{ref} / \Delta T$	Average temperature coefficient	I _k = 100μA			120	ppm/°C
	Reverse breakdown voltage change	I _{k-min} < I _k < 1mA -40°C < T _{amb} < +85°C		0.3	0.7 1	mV
$\Delta V_k / \Delta I_k$	with operating current range	1mA < I _k < 12mA -40°C < T _{amb} < +85°C		2.5	8 10	
R _{ka}	Static impedance	$\Delta I_k = 45 \mu A$ to 1mA		0.25	0.5	Ω
K _{vh}	Long term stability	$I_{k} = 100 \mu A, t = 1000 hrs$		120		ppm
En	Wide band noise	I _k = 100μA, 10Hz < f < 10kHz		200		nV/√Hz

Table 3. TS821 (2% precision) T_{amb} = 25°C ⁽¹⁾ (unless otherwise specified)

1. Limits are 100% production tested at 25°C. Behavior at the temperature range limits is guaranteed through correlation and by design.

Table 4.TS821A (1% precision) $T_{amb} = 25^{\circ}C^{(1)}$ (unless otherwise specified)

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
	Reverse breakdown voltage	I _k = 100μA	1.213	1.225	1.237	V
V _k	Reverse breakdown voltage tolerance	$I_{k} = 100\mu A$ -40°C < T _{amb} < +85°C	-12 -22		+12 +22	mV
I	Minimum operating current	$T_{amb} = 25^{\circ}C$		40	45	μA
I _{k-min}		$-40^{\circ}\text{C} < \text{T}_{\text{amb}} < +85^{\circ}\text{C}$			50	μΛ
$\Delta V_{ref} / \Delta T$	Average temperature coefficient	I _k = 100μA			120	ppm/°C
$\Delta V_k / \Delta I_k$	Reverse breakdown voltage change	I _{k-min} < I _k < 1mA -40°C < T _{amb} < +85°C		0.3	0.7 1	mV
Δv _k /Δι _k	with operating current range	1mA < I _k < 12mA -40°C < T _{amb} < +85°C		2.5	5 7	IIIV
R _{ka}	Static impedance	$\Delta I_k = 45 \mu A$ to 1mA		0.25	0.5	Ω
K _{vh}	Long term stability	$I_{k} = 100 \mu A, t = 1000 hrs$		120		ppm
En	Wide band noise	$I_k = 100 \mu A \ 10 Hz < f < 10 kHz$		200		nV/√Hz

1. Limits are 100% production tested at 25°C. Behavior at the temperature range limits is guaranteed through correlation and by design.





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Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
	Reverse breakdown voltage	I _k = 100μA	1.219	1.225	1.231	V
V _k	Reverse breakdown voltage tolerance	$I_{k} = 100\mu A$ -40°C < T _{amb} < +85°C	-6 -16		+6 +16	mV
I	Minimum operating current	$T_{amb} = 25^{\circ}C$		40	45	
I _{k-min}		$-40^{\circ}\text{C} < \text{T}_{\text{amb}} < +85^{\circ}\text{C}$			50	μA
$\Delta V_{ref} / \Delta T$	Average temperature coefficient	I _k = 100μA			120	ppm/°C
	Reverse breakdown voltage change	I _{k-min} < I _k < 1mA -40°C < T _{amb} < +85°C		0.3	0.7 1	mV
$\Delta V_k / \Delta I_k$	with operating current range	1mA < I _k < 12mA -40°C < T _{amb} < +85°C		2.5	5 7	IIIV
R _{ka}	Static impedance	$\Delta I_k = 45 \mu A$ to 1mA		0.25	0.5	W
K _{vh}	Long term stability	$I_k = 100 \mu A, t = 1000 hrs$		120		ppm
En	Wide band noise	I _k = 100μA 10Hz < f < 10kHz		200		nV/√Hz

Table 5. TS821B (0.5% precision) $T_{amb} = 25^{\circ}C^{(1)}$ (unless otherwise specified)

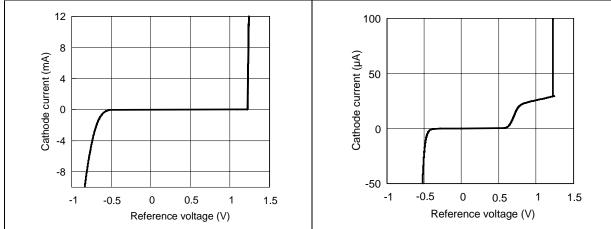
1. Limits are 100% production tested at 25°C. Behavior at the temperature range limits is guaranteed through correlation and by design.

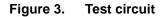




Electrical characteristics





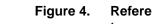


R

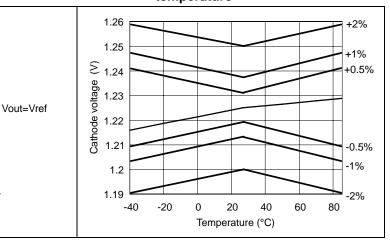
171

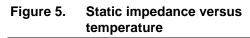
lk=(Vin-Vref)/R

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Reference voltage versus temperature





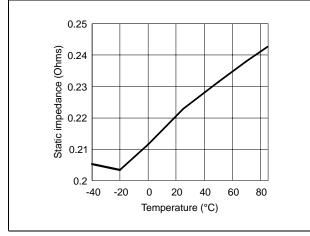


Figure 6. Noise voltage versus frequency

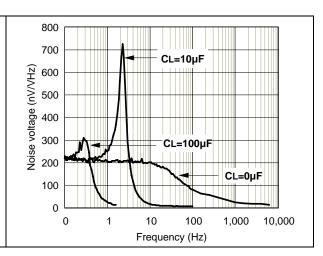






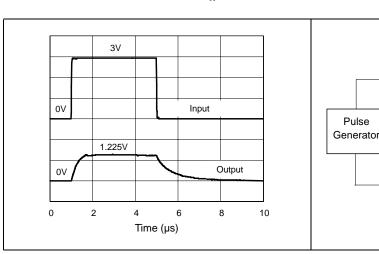
Figure 7.

Figure 8.

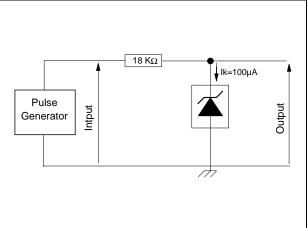
Package information

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Pulse response for Ik=100µA

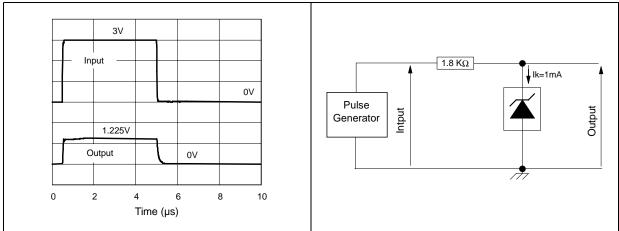


I_k=100µA

Test circuit for pulse response at

Figure 9. Pulse response for I_k=1mA

Figure 10. Test circuit for pulse response at Ik=1mA



3 Package information

In order to meet environmental requirements, STMicroelectronics offers these devices in ECOPACK[®] packages. These packages have a lead-free second level interconnect. The category of second level interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an STMicroelectronics trademark. ECOPACK specifications are available at: <u>www.st.com</u>.



Package information

	Dimensions						
Ref.		Millimeters		Mils			
	Min.	Тур.	Max.	Min.	Тур.	Max.	
А	0.890		1.120	35.05		44.12	
A1	0.010		0.100	0.39		3.94	
A2	0.880	0.950	1.020	34.65	37.41	40.17	
b	0.300		0.500	11.81		19.69	
С	0.080		0.200	3.15		7.88	
D	2.800	2.900	3.040	110.26	114.17	119.72	
Е	2.100		2.64	82.70		103.96	
E1	1.200	1.300	1.400	47.26	51.19	55.13	
е		0.950			37.41		
e1		1.900			74.82		
L	0.400		0.600	15.75		23.63	
L1		0.540			21.27		
k	0°		8°	0°		8°	
SEATING PLANE							



Ordering information

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4 Ordering information

Table 6. Order codes

Part number	Precision	Temperature range	Package	Packing	Marking
TS821ILT	2%				L213
TS821AILT	1%	-40°C to +85°C	SOT23-3	Tape & reel	L212
TS821BILT	0.5%				L211

5 Revision history

Table 7.Document revision history

Date	Revision	Changes
10-Nov-2005	1	Initial release.
24-Jul-2007	2	Removed information related to TO-92. Format update.



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