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Fairchild Semiconductor FJV4112RMTF

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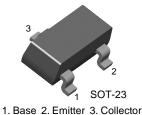




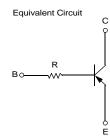
FJV4112R

Switching Application (Bias Resistor Built In)

- Switching circuit, Inverter, Interface circuit, Driver Circuit
- Built in bias Resistor (R=47KΩ)
- Complement to FJV3112R







PNP Epitaxial Silicon Transistor

Absolute Maximum Ratings Ta=25°C unless otherwise noted

Symbol	Parameter	Value	Units
V _{CBO}	Collector-Base Voltage	-40	V
V _{CEO}	Collector-Emitter Voltage	-40	V
V_{EBO}	Emitter-Base Voltage	-5	V
I _C	Collector Current	-100	mA
P _C	Collector Power Dissipation	200	mW
TJ	Junction Temperature	150	°C
T _{STG}	Storage Temperature	-55 ~ 150	°C

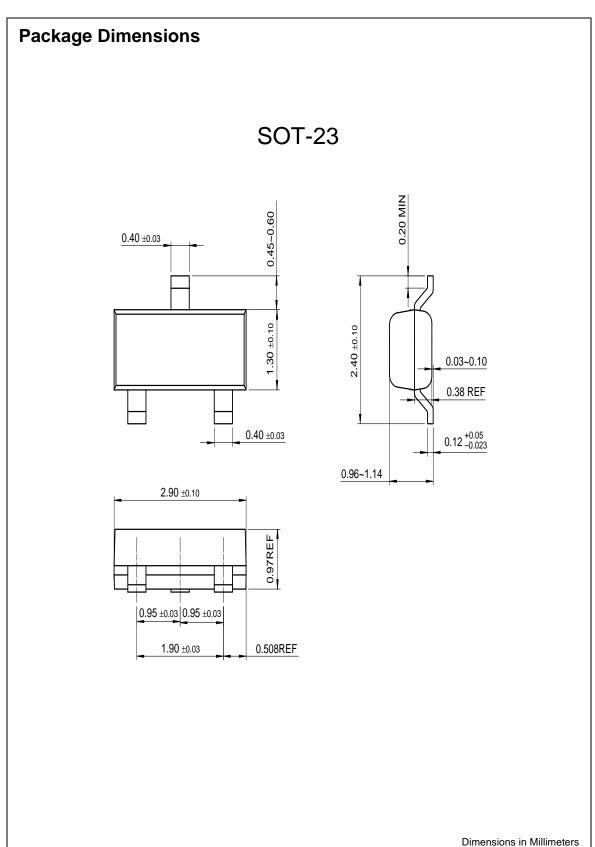
Electrical Characteristics T_a=25°C unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units
BV _{CBO}	Collector-Base Breakdown Voltage	I_{C} = -100 μ A, I_{E} =0	-40			V
BV _{CEO}	Collector-Emitter Breakdown Voltage	$I_C = -1 \text{mA}, I_B = 0$	-40			V
I _{CBO}	Collector Cut-off Current	V_{CB} = -30V, I_{E} =0			-0.1	μΑ
h _{FE}	DC Current Gain	V_{CE} = -5V, I_{C} = -1mA	100		600	
V _{CE} (sat)	Collector-Emitter Saturation Voltage	I_C = -10mA, I_B = -1mA			-0.3	V
C _{ob}	Output Capacitance	V _{CB} = -10V, I _E =0 f=1MHz		5.5		pF
f _T	Current Gain Bandwidth Product	$V_{CE} = -10V, I_{C} = -5mA$		200		MHz
R	Input Resistor		32	47	62	ΚΩ

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Distributor of Fairchild Semiconductor: Excellent Integrated System Limited Datasheet of FJV4112RMTF - TRANS PREBIAS PNP 200MW SOT23-3

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EnSigna™	I ² C™	OCX^{TM}	RapidConfigure™	UHC™
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