

# **Excellent Integrated System Limited**

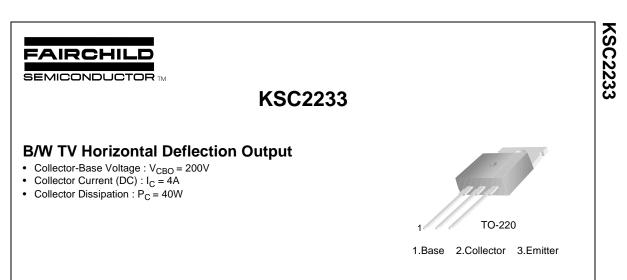
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Fairchild Semiconductor KSC2233

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## **NPN Epitaxial Silicon Transistor**

Absolute Maximum Ratings T<sub>C</sub>=25°C unless otherwise noted

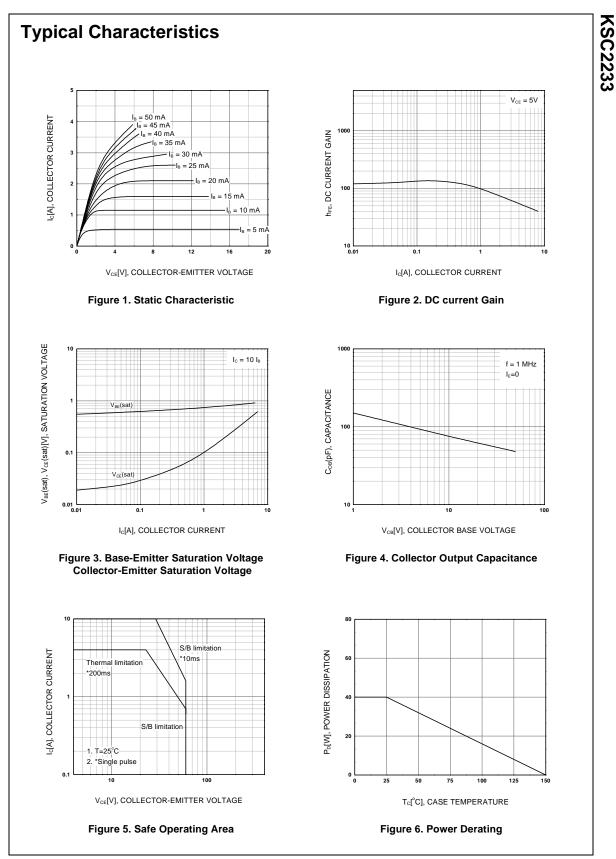
Symbol	Parameter	Value	Units	
V <sub>CBO</sub>	Collector-Base Voltage	200	V	
V <sub>CEO</sub>	Collector-Emitter Voltage	60	V	
V <sub>EBO</sub>	Emitter-Base Voltage	5	V	
I <sub>C</sub>	Collector Current	4	Α	
P <sub>C</sub>	Collector Dissipation (T <sub>C</sub> =25°C)	40	W	
TJ	Junction Temperature	150	°C	
T <sub>STG</sub>	Storage Temperature	-55 ~ +150	°C	

### Electrical Characteristics T<sub>C</sub>=25°C unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units
BV <sub>CBO</sub>	Collector-Base Breakdown Voltage	$I_{\rm C} = 1$ mA, $I_{\rm E} = 0$	200			V
BV <sub>CEO</sub>	Collector-Emitter Breakdown Voltage	I <sub>C</sub> = 20mA, I <sub>B</sub> =0	60			V
BV <sub>EBO</sub>	Emitter-Base Breakdown Voltage	I <sub>E</sub> = 1mA, I <sub>C</sub> = 0	5			V
I <sub>CBO</sub>	Collector Cut-off Current	V <sub>CB</sub> = 170V, I <sub>E</sub> = 0			10	μΑ
h <sub>FE1</sub>	DC Current Gain	V <sub>CE</sub> = 5V, I <sub>C</sub> = 1A	30		150	
h <sub>FE2</sub>		$V_{CE} = 5V, I_{C} = 4A$	20	40		
V <sub>CE</sub> (sat)	Collector-Emitter Saturation Voltage	$I_{\rm C} = 4$ A, $I_{\rm B} = 0.4$ A			1	V
V <sub>BE</sub> (sat)	Base-Emitter Saturation Voltage	$I_{\rm C} = 4$ A, $I_{\rm B} = 0.4$ A			1.5	V
f <sub>T</sub>	Current Gain Bandwidth Product	V <sub>CE</sub> = 5V, I <sub>C</sub> = 0.5A		10		MHz

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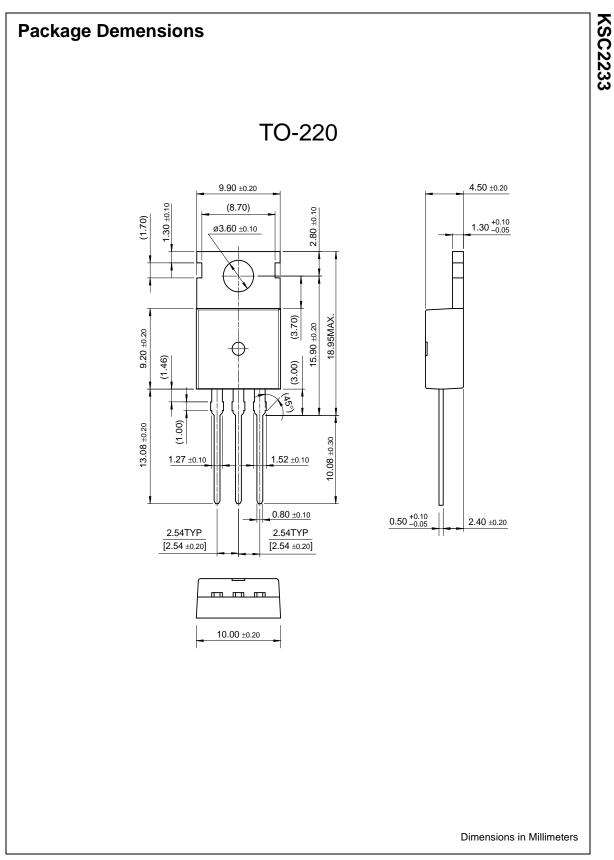




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- SuperSOT<sup>™</sup>-8 SyncFET<sup>™</sup> TinyLogic<sup>™</sup> UHC<sup>™</sup> VCX<sup>™</sup>

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