

Excellent Integrated System Limited

Stocking Distributor

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<u>Vishay Semiconductor/Diodes Division</u> <u>ES3B-E3/57T</u>

For any questions, you can email us directly: sales@integrated-circuit.com

Distributor of Vishay Semiconductor/Diodes Division: Excellent Integrated System Limite Datasheet of ES3B-E3/57T - DIODE GEN PURP 100V 3A DO214AB

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ES3A, ES3B, ES3C, ES3D

Vishay General Semiconductor

Surface Mount Ultrafast Plastic Rectifier



DO-214AB (SMC)

PRIMARY CHARACTERISTICS					
I _{F(AV)}	3.0 A				
V_{RRM}	50 V, 100 V, 150 V, 200 V				
I _{FSM} 100 A					
t _{rr}	20 ns				
V_{F}	0.90 V				
T _J max.	150 °C				
Package	DO-214AB (SMC)				
Diode variations Single die					

FEATURES

- · Glass passivated pallet chip junction
- Ideal for automated placement
- Ultrafast recovery times for high efficiency
- Low forward voltage, low power losses
- High forward surge capability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

TYPICAL APPLICATIONS

For use in high frequency rectification and freewheeling application in switching mode converters and inverters for consumer, computer, and telecommunication.

MECHANICAL DATA

Case: DO-214AB (SMC)

Molding compound meets UL 94 V-0 flammability rating Base P/N-E3 - RoHS-compliant, commercial grade Base P/NHE3_X - RoHS-compliant and AEC-Q101 qualified ("_X" denotes revision code e.g. A, B,)

Terminals: Matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

E3 suffix meets JESD 201 class 2 whisker test, HE3 suffix meets JESD 201 class 2 whisker test

Polarity: Color band denotes cathode end

MAXIMUM RATINGS (T _A = 25 °C unless otherwise noted)						
PARAMETER	SYMBOL	ES3A	ES3B	ES3C	ES3D	UNIT
Device marking code		EA	EB	EC	ED	
Maximum repetitive peak reverse voltage	V_{RRM}	50	100	150	200	V
Maximum RMS voltage	V _{RMS}	35	70	105	140	V
Maximum DC blocking voltage	V_{DC}	50	100	150	200	V
Maximum average forward rectified current at $T_L = 100 ^{\circ}\text{C}$	I _{F(AV)}		Α			
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load	I _{FSM}	100				Α
Operating junction and storage temperature range	T _J , T _{STG}	-55 to +150				°C

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ELECTRICAL CHARACTERISTICS (T _A = 25 °C unless otherwise noted)								
PARAMETER	TEST CONDITIONS		SYMBOL	ES3A	ES3B	ES3C	ES3D	UNIT
Maximum instantaneous forward voltage	3.0 A		V _F ⁽¹⁾	0.90				V
Maximum DC reverse current at		$T_A = 25 ^{\circ}C$	1_	10				
rated DC blocking voltage		T _A = 100 °C	I _R		μΑ			
Maximum reverse recovery time	I _F = 0.5 A, I _R = 1.0 A, I _{rr} = 0.25 A		t _{rr}	20			ns	
Maximum rayaraa raaayan tima	$I_F = 3.0 \text{ A}, V_R = 30 \text{ V},$	T _J = 25 °C		30				ns
Maximum reverse recovery time	$dI/dt = 50 \text{ A/}\mu\text{s}, I_{rr} = 10 \% I_{RM}$	T _J = 100 °C	t _{rr}	50				
Maximum stored charge	I _E = 3.0 A, V _B = 30 V,	T _J = 25 °C	Q _{rr}	15				nC
	$dI/dt = 50 \text{ A/}\mu\text{s}, I_{rr} = 10 \% I_{RM}$	T _J = 100 °C		35				
Typical junction capacitance	4.0 V, 1 MHz		CJ	45			pF	

Note

⁽¹⁾ Pulse test: 300 µs pulse width, 1 % duty cycle

THERMAL CHARACTERISTICS (T _A = 25 °C unless otherwise noted)							
PARAMETER	SYMBOL	ES3A	ES3B	ES3C	ES3D	UNIT	
Typical thermal resistance	R _{0JA} (1)		°C/W				
Typical thermal resistance	R ₀ JL (1)		C/VV				

Note

⁽¹⁾ Units mounted on PCB with 0.31" x 0.31" (8.0 mm x 8.0 mm) copper pad areas

ORDERING INFORMATION (Example)						
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE		
ES3D-E3/57T	0.211	57T	850	7" diameter plastic tape and reel		
ES3D-E3/9AT	0.211	9AT	3500	13" diameter plastic tape and reel		
ES3DHE3_A/H (1)	0.211	Н	850	7" diameter plastic tape and reel		
ES3DHE3_A/I (1)	0.211	I	3500	13" diameter plastic tape and reel		

Note

⁽¹⁾ AEC-Q101 qualified

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RATINGS AND CHARACTERISTICS CURVES (T_A = 25 °C unless otherwise noted)

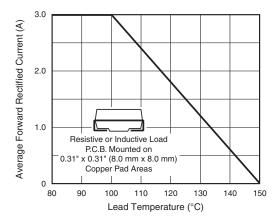


Fig. 1 - Maximum Forward Current Derating Curve

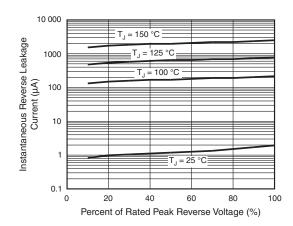


Fig. 4 - Typical Reverse Leakage Characteristics

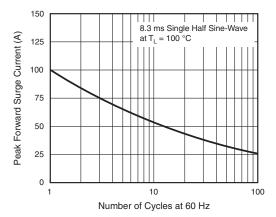


Fig. 2 - Maximum Non-Repetitive Peak Forward Surge Current

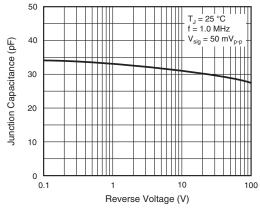


Fig. 5 - Typical Junction Capacitance

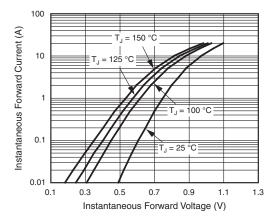


Fig. 3 - Typical Instantaneous Forward Characteristics



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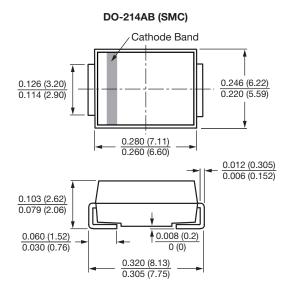
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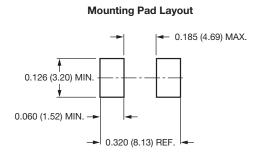


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PACKAGE OUTLINE DIMENSIONS in inches (millimeters)







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Revision: 13-Jun-16 1 Document Number: 91000