

Excellent Integrated System Limited

Stocking Distributor

Click to view price, real time Inventory, Delivery & Lifecycle Information:

NXP Semiconductors MPVZ4006G6T1

For any questions, you can email us directly: <u>sales@integrated-circuit.com</u>



Scale Semiconductor Technical Data

Pressure Sensor Inditioned MPVZ4006G

MPVZ4006G Rev 2, 02/2008

Integrated Silicon Pressure Sensor On-Chip Signal Conditioned, Temperature Compensated and Calibrated

The MPVZ4006G series piezoresistive transducers are state-of-the-art monolithic silicon pressure sensors designed for the appliance, consumer, health care and industrial market. The analog output can be read directly into the A/D input of Freescale microcontrollers. This transducer combines advanced micromachining techniques, thin-film metallization, and bipolar processing to provide an accurate, high level analog output signal that is proportional to the applied pressure. The axial port has been modified to accommodate industrial grade tubing.

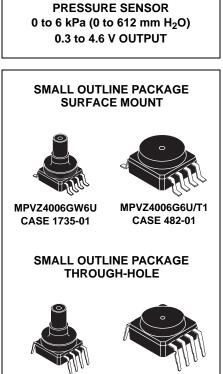
Features

- 2.5% Maximum Error over +10°C to +60°C with Auto Zero
- 5% Maximum Error over +10°C to +60°C without Auto Zero
- Durable Thermoplastic (PPS) Package
- Available in Surface Mount (SMT) or Through-Hole (DIP) Configurations

Application Examples

- Washing Machine Water Level Measurement (Reference AN1950)
- Ideally Suited for Microprocessor or Microcontroller-Based Systems
- Appliance Liquid Level and Pressure Measurement
- Respiratory Equipment

ORDERING INFORMATION							
Device Type	Options	Case No. MPX Series Packin Order No. Option			Marking		
	Surface Mount	1735-01	MPVZ4006GW6U	Rails	MZ4006GW		
	Through-Hole	1560-02	MPVZ4006GW7U	Rails	MZ4006GW		
Ported Element	Surface Mount	482-01	MPVZ4006G6U	Rails	MPVZ4006G		
	Surface Mount	482-01	MPVZ4006G6T1	Tape & Reel	MPVZ4006G		
	Through-Hole	482B-03	MPVZ4006G7U	Rails	MPVZ4006G		



INTEGRATED

MPVZ4006GW7U CASE 1560-02 MPVZ4006G7U CASE 482B-03

PIN NUMBERS ⁽¹⁾					
1 N/C 5 N/C					
2	V _S	6	N/C		
3	Gnd	7	N/C		
4	V _{out}	8	N/C		

1. Pins 1, 5, 6, 7, and 8 are internal device connections. Do not connect to external circuitry or ground. Pin 1 is noted by the notch in the lead.



© Freescale Semiconductor, Inc., 2006, 2008. All rights reserved.



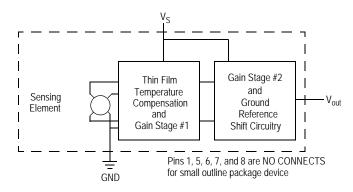


Figure 1. Fully Integrated Pressure Sensor Schematic

Table 1. Maximum Ratings⁽¹⁾

Parametric	Symbol	Value	Units
Maximum Pressure (P1 > P2)	P _{max}	75	kPa
Storage Temperature	T _{stg}	-30° to +100°	°C
Operating Temperature	T _A	+10° to +60°	°C

1. Exposure beyond the specified limits may cause permanent damage or degradation to the device.

Table 2. Operating Characteristics (V_S = 5.0 Vdc, T_A = 25°C unless otherwise noted, P1 > P2)

Characteristic	Symbol	Min	Тур	Max	Unit
Pressure Range	P _{OP}	0	—	6.0 612	kPa mm H ₂ O
Supply Voltage ⁽¹⁾	V _S	4.75	5.0	5.25	Vdc
Supply Current	۱ _S	—	—	10	mAdc
Full Scale Output ⁽²⁾	V _{FSS}	4.374	4.6	4.826	V
Offset ⁽³⁾⁽⁵⁾	V _{off}	0.152	0.265	0.378	V
Sensitivity	V/P	_	766 7.511	_	mV/kPa mV/mm H ₂ O
Accuracy ⁽⁴⁾⁽⁵⁾ (10 to 60°C)	-	-	—	±2.46	%V _{FSS} with auto zero
	_	_	_	±5.0	%V _{FSS} without auto zero

1. Device is ratiometric within this specified excitation range.

2. Full Scale Span (V_{FSS}) is defined as the algebraic difference between the output voltage at full rated pressure and the output voltage at the minimum rated pressure.

- 3. Offset (Voff) is defined as the output voltage at the minimum rated pressure.
- 4. Accuracy (error budget) consists of the following:

Linearity: Output deviation from a straight line relationship with pressure over the specified pressure range.

- Temperature Hysteresis: Output deviation at any temperature within the operating temperature range, after the temperature is cycled to and from the minimum or maximum operating temperature points, with zero differential pressure applied.
- Pressure Hysteresis: Output deviation at any pressure within the specified range, when this pressure is cycled to and from minimum or maximum rated pressure, at 25°C.
- Offset Stability: Output deviation, after 1000 temperature cycles, -30 to 100°C, and 1.5 million pressure cycles, with minimum rated pressure applied.
- TcSpan: Output deviation over the temperature range of 10° to 60°C, relative to 25°C.
- TcOffset: Output deviation with minimum pressure applied, over the temperature range of 10° to 60°C, relative to 25°C.

5. Auto Zero at Factory Installation: Due to the sensitivity of the MPVZ4006G, external mechanical stresses and mounting position can affect the zero pressure output reading. To obtain the 2.46% FSS accuracy, the device output must be "autozeroed" after installation. Autozeroing is defined as storing the zero pressure output reading and subtracting this from the device's output during normal operations. The specified accuracy assumes a maximum temperature change of ±5°C between autozero and measurement.



ON-CHIP TEMPERATURE COMPENSATION, CALIBRATION, AND SIGNAL CONDITIONING

The performance over temperature is achieved by integrating the shear-stress strain gauge, temperature compensation, calibration and signal conditioning circuitry onto a single monolithic chip.

Figure 2 illustrates the Differential or Gauge configuration in the basic chip carrier (Case 482). A gel die coat isolates the die surface and wire bonds from the environment, while allowing the pressure signal to be transmitted to the silicon diaphragm.

The MPVZ4006G series sensor operating characteristics are based on use of dry air as pressure media. Media, other than dry air, may have adverse effects on sensor performance and long-term reliability. Internal reliability and

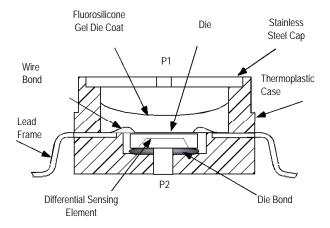
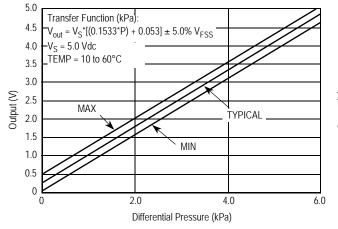
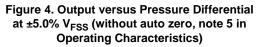


Figure 2. Cross Sectional Diagram SOP (Not to Scale)





qualification test for dry air, and other media, are available from the factory. Contact the factory for information regarding media tolerance in your application.

Figure 3 shows the recommended decoupling circuit for interfacing the output of the integrated sensor to the A/D input of a microprocessor or microcontroller. Proper decoupling of the power supply is recommended.

Figure 4 and Figure 5 shows the sensor output signal relative to pressure input. Typical, minimum and maximum output curves are shown for operation over a temperature range of 10°C to 60°C using the decoupling circuit shown in Figure 3. The output will saturate outside of the specified pressure range.

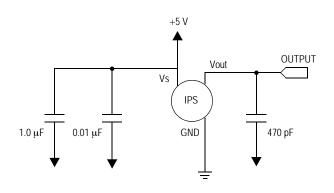
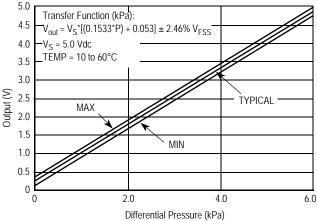
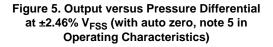


Figure 3. Recommended Power Supply Decoupling and Output Filtering Recommendations (For additional output filtering, please refer to Application Note AN1646.)







PRESSURE (P1)/VACUUM (P2) SIDE IDENTIFICATION TABLE

Freescale designates the two sides of the pressure sensor as the Pressure (P1) side and the Vacuum (P2) side. The Pressure (P1) side is the side containing a gel die coat which isolates the die from the environment. The pressure sensor is designed to operate with positive differential pressure applied, P1 > P2. The Pressure (P1) side may be identified by using the

table below:

Table 3. Pressure (P1)/Vacuum (P2) Side Identification Table

Part Number	Case Type	Pressure (P1) Side Identifier
MPVZ4006GW6U	1735-01	Vertical Port Attached
MPVZ4006GW7U	1560-02	Vertical Port Attached
MPVZ4006G6U/T1	482-01	Stainless Steel Cap
MPVZ4006G7U	482B-03	Stainless Steel Cap

MINIMUM RECOMMENDED FOOTPRINT FOR SURFACE MOUNTED APPLICATIONS

Surface mount board layout is a critical portion of the total design. The footprint for the surface mount packages must be the correct size to ensure proper solder connection interface between the board and the package. With the correct

footprint, the packages will self align when subjected to a solder reflow process. It is always recommended to design boards with a solder mask layer to avoid bridging and shorting between solder pads.

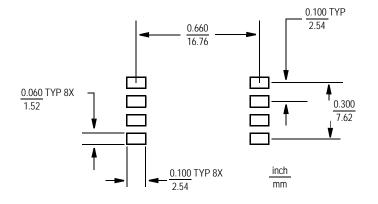
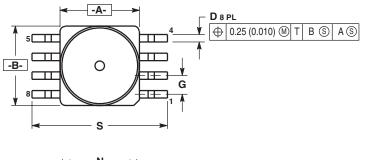
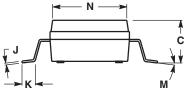


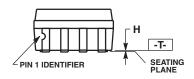
Figure 6. SOP Footprint (Case 482)



PACKAGE DIMENSIONS





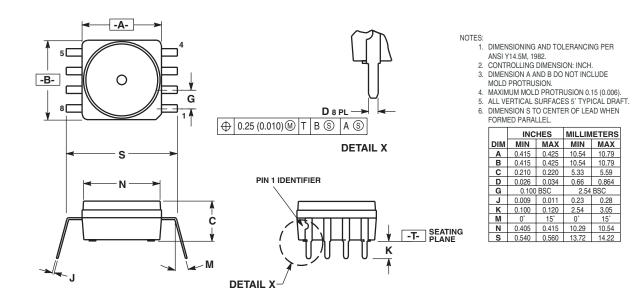


NOTES: 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982. 2. CONTROLLING DIMENSION: INCH. 3. DIMENSION A AND B DO NOT INCLUDE MOLD PROTRUSION. YMANIA DEPOTRUSION 0.15 (0.006).

PROTRUSION.
MAXIMUM MOLD PROTRUSION 0.15 (0.006).
ALL VERTICAL SURFACES 5' TYPICAL DRAFT.

	INC	HES	MILLIM	ETERS
DIM	MIN MAX		MIN	MAX
Α	0.415	0.425	10.54	10.79
В	0.415	0.425	10.54	10.79
С	0.212	0.230	5.38	5.84
D	0.038	0.042	0.96	1.07
G	0.100	BSC	2.54	BSC
Н	0.002	0.010	0.05	0.25
J	0.009	0.011	0.23	0.28
K	0.061	0.071	1.55	1.80
М	0°	7°	0°	7°
N	0.405	0.415	10.29	10.54
S	0.709	0.725	18.01	18.41

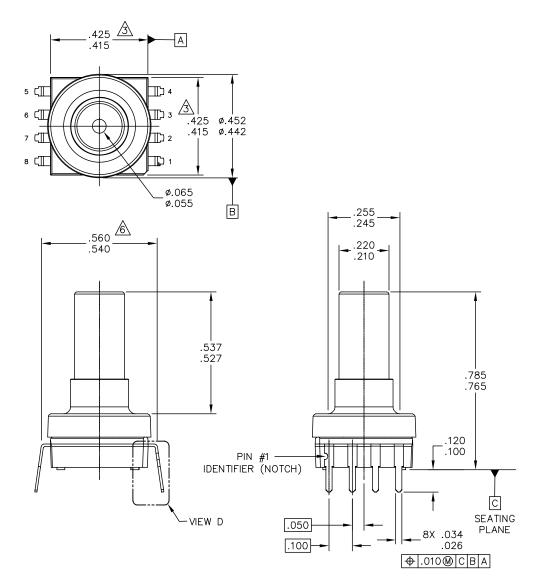
CASE 482-01 ISSUE O SMALL OUTLINE PACKAGE



CASE 482B-03 **ISSUE B** SMALL OUTLINE PACKAGE



PACKAGE DIMENSIONS



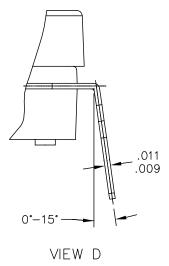
© FREESCALE SEMICONDUCTOR, INC. ALL RIGHTS RESERVED.	MECHANICA	L OUTLINE	PRINT VERSION NO	T TO SCALE
TITLE:		DOCUMENT NO	: 98ASA10611D	REV: C
SO, 8 I/O, .420 X .420 PKG, .100 IN PITCH		CASE NUMBER	: 1560–02	26 MAY 2005
		STANDARD: NO	N-JEDEC	

PAGE 1 OF 3

CASE 1560-02 ISSUE C SMALL OUTLINE PACKAGE



PACKAGE DIMENSIONS



© FREESCALE SEMICONDUCTOR, INC. ALL RIGHTS RESERVED.	MECHANICAL OUTLINE		PRINT VERSION NO	T TO SCALE
TITLE:		DOCUMENT NO): 98ASA10611D	REV: C
SO, 8 1/0, .420 X .4	CASE NUMBER	2: 1560–02	26 MAY 2005	
.100 IN PITCH		STANDARD: NO	N-JEDEC	

PAGE 2 OF 3

CASE 1560-02 ISSUE C SMALL OUTLINE PACKAGE



PACKAGE DIMENSIONS

NOTES:

- 1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M 1994.
- 2. CONTROLLING DIMENSION: INCH.
- 3. DIMENSIONS DO NOT INCLUDE MOLD PROTRUSION.
- 4. MAXIMUM MOLD PROTRUSION IS .006.
- 5. ALL VERTICAL SURFACES 5' TYPICAL DRAFT.
- 6. DIMENSION TO CENTER OF LEAD WHEN FORMED PARALLEL.

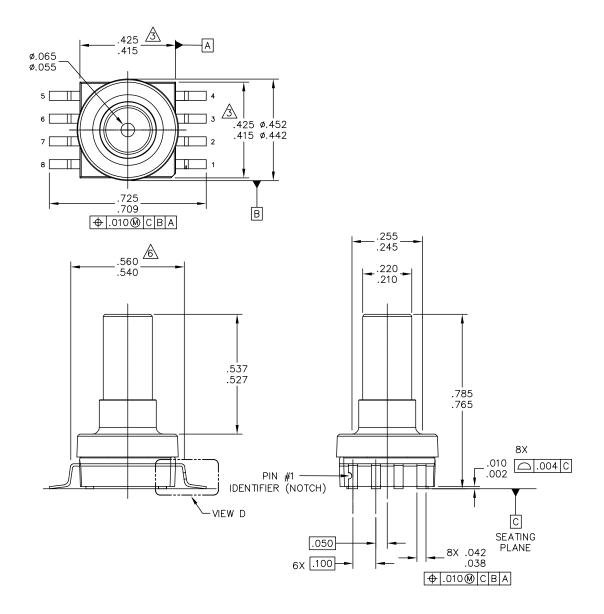
© FREESCALE SEMICONDUCTOR, INC. All RIGHTS RESERVED.	MECHANICA	L OUTLINE	PRINT VERSION NO	T TO SCALE
TITLE:		DOCUMENT NO): 98ASA10611D	REV: C
SO, 8 1/0, .420 X .420	CASE NUMBER	: 1560–02	26 MAY 2005	
.100 IN PITCH		STANDARD: NO	N-JEDEC	

PAGE 3 OF 3

CASE 1560-02 ISSUE C SMALL OUTLINE PACKAGE



PACKAGE DIMENSIONS



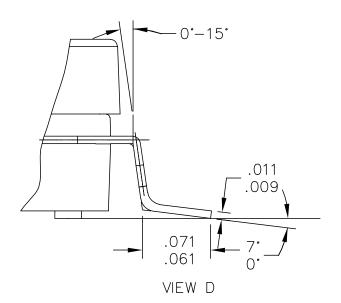
© FREESCALE SEMICONDUCTOR, INC. ALL RIGHTS RESERVED.	MECHANICA	LOUTLINE	PRINT VERSION NO	T TO SCALE
TITLE:		DOCUMENT NO): 98ASA10686D	REV: A
SO, 8 I/O, .420 X .420 PKG, .100 IN PITCH		CASE NUMBER	: 1735–01	16 AUG 2005
		STANDARD: NO	N-JEDEC	

PAGE 1 OF 3

CASE 1735-01 ISSUE A SMALL OUTLINE PACKAGE



PACKAGE DIMENSIONS



© FREESCALE SEMICONDUCTOR, INC. ALL RIGHTS RESERVED.	ECHANICA	LOUTLINE	PRINT VERSION NO	T TO SCALE
TITLE:		DOCUMENT NO): 98ASA10686D	REV: A
SO, 8 1/0, .420 X .420	PKG,	CASE NUMBER	: 1735–01	18 AUG 2005
.100 IN PITCH		STANDARD: NO	N-JEDEC	·

PAGE 2 OF 3

CASE 1735-01 ISSUE A SMALL OUTLINE PACKAGE



PACKAGE DIMENSIONS

NOTES:

- 1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M 1994.
- 2. CONTROLLING DIMENSION: INCH.

 $\underline{\mathcal{A}}$ dimensions do not include mold protrusion.

4. MAXIMUM MOLD PROTRUSION IS .006.

5. ALL VERTICAL SURFACES 5' TYPICAL DRAFT.

6. DIMENSION TO CENTER OF LEAD WHEN FORMED PARALLEL.

© FREESCALE SEMICONDUCTOR, INC. ALL RIGHTS RESERVED.		L OUTLINE	PRINT VERSION NO	T TO SCALE
TITLE:		DOCUMENT NO): 98ASA10686D	REV: A
SO, 8 1/0, .420 X .4	CASE NUMBER	8: 1735–01	18 AUG 2005	
.100 IN PITCH		STANDARD: NO	DN-JEDEC	

PAGE 3 OF 3

CASE 1735-01 ISSUE A SMALL OUTLINE PACKAGE

Sensors Freescale Semiconductor



How to Reach Us:

Home Page: www.freescale.com

Web Support:

http://www.freescale.com/support

USA/Europe or Locations Not Listed:

Freescale Semiconductor, Inc. Technical Information Center, EL516 2100 East Elliot Road Tempe, Arizona 85284 +1-800-521-6274 or +1-480-768-2130 www.freescale.com/support

Europe, Middle East, and Africa:

Freescale Halbleiter Deutschland GmbH Technical Information Center Schatzbogen 7 81829 Muenchen, Germany +44 1296 380 456 (English) +46 8 52200080 (English) +49 89 92103 559 (German) +33 1 69 35 48 48 (French) www.freescale.com/support

Japan:

Freescale Semiconductor Japan Ltd. Headquarters ARCO Tower 15F 1-8-1, Shimo-Meguro, Meguro-ku, Tokyo 153-0064 Japan 0120 191014 or +81 3 5437 9125 support.japan@freescale.com

Asia/Pacific:

Freescale Semiconductor Hong Kong Ltd. Technical Information Center 2 Dai King Street Tai Po Industrial Estate Tai Po, N.T., Hong Kong +800 2666 8080 support.asia@freescale.com

For Literature Requests Only:

Freescale Semiconductor Literature Distribution Center P.O. Box 5405 Denver, Colorado 80217 1-800-441-2447 or 303-675-2140 Fax: 303-675-2150 LDCForFreescaleSemiconductor@hibbertgroup.com Information in this document is provided solely to enable system and software implementers to use Freescale Semiconductor products. There are no express or implied copyright licenses granted hereunder to design or fabricate any integrated circuits or integrated circuits based on the information in this document.

Freescale Semiconductor reserves the right to make changes without further notice to any products herein. Freescale Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does Freescale Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation consequential or incidental damages. "Typical" parameters that may be provided in Freescale Semiconductor data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals", must be validated for each customer application by customer's technical experts. Freescale Semiconductor does not convey any license under its patent rights nor the rights of others. Freescale Semiconductor products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the Freescale Semiconductor product could create a situation where personal injury or death may occur. Should Buyer purchase or use Freescale Semiconductor products for any such unintended or unauthorized application, Buyer shall indemnify and hold Freescale Semiconductor and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that Freescale Semiconductor was negligent regarding the design or manufacture of the part.

Freescale[™] and the Freescale logo are trademarks of Freescale Semiconductor, Inc. All other product or service names are the property of their respective owners. © Freescale Semiconductor, Inc. 2008. All rights reserved.

RoHS-compliant and/or Pb-free versions of Freescale products have the functionality and electrical characteristics of their non-RoHS-compliant and/or non-Pb-free counterparts. For further information, see http://www.freescale.com or contact your Freescale sales representative.

For information on Freescale's Environmental Products program, go to http://www.freescale.com/epp.



MPVZ4006G Rev. 2 02/2008