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# 1PS70SB15

Dual Schottky barrier diode 17 December 2012

**Product data sheet** 

## 1. General description

Dual Planar Schottky barrier diode in common cathode configuration with an integrated guard ring for stress protection, encapsulated in a very small SOT323 (SC-70) Surface-Mounted Device (SMD) plastic package.

#### 2. Features and benefits

- Low forward voltage
- Low capacitance
- AEC-Q101 qualified

#### 3. Applications

- Ultra high-speed switching
- Line termination
- Voltage clamping
- Line termination

## 4. Quick reference data

uick reference data						
Parameter	Conditions		Min	Тур	Мах	Unit
	· · · · · · · · · · · · · · · · · · ·					_
forward current			-	-	200	mA
reverse voltage			-	-	30	V
forward voltage	I <sub>F</sub> = 10 mA; T <sub>amb</sub> = 25 °C		-	-	400	mV
	Parameter    forward current    reverse voltage	Parameter  Conditions    forward current	Parameter  Conditions    forward current	Parameter  Conditions  Min    forward current  -    reverse voltage  -	Parameter  Conditions  Min  Typ    forward current  -  -  -    reverse voltage  -  -  -	Parameter    Conditions    Min    Typ    Max      forward current    -    -    200      reverse voltage    -    -    30







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## 5. Pinning information

Table 2.	Pinning	information		
Pin	Symbol	Description	Simplified outline	Graphic symbol
1	A1	anode (diode 1)	3	К1, К2
2	A2	anode (diode 2)		
3	K1, K2	common cathode		aaa-004975
			SC-70 (SOT323)	

## 6. Ordering information

Table 3. Ordering information					
Type number Package					
	Name	Description	Version		
1PS70SB15	SC-70	plastic surface-mounted package; 3 leads	SOT323		

## 7. Marking

Table 4. Marking codes	
Type number	Marking code
	[1]
1PS70SB15	7%5

[1] % = placeholder for manufacturing site code

## 8. Limiting values

#### Table 5.Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
Per diode		1			
V <sub>R</sub>	reverse voltage		-	30	V
l <sub>F</sub>	forward current		-	200	mA
I <sub>FRM</sub>	repetitive peak forward current	t <sub>p</sub> ≤ 1 s; δ ≤ 0.5	-	300	mA
I <sub>FSM</sub>	non-repetitive peak forward current	t <sub>p</sub> < 10 ms; T <sub>j(init)</sub> = 25 °C	-	600	mA
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> < 25 °C	-	200	mW
Tj	junction temperature		-	150	°C
T <sub>amb</sub>	ambient temperature		-55	150	°C

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Symbol	Parameter	Conditions	Min	Max	Unit
T <sub>stg</sub>	storage temperature		-65	150	°C

## 9. Thermal characteristics

Table 6. T	hermal characteristics						
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
Per device			· · ·				
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient	in free air	[1]	-	-	625	K/W

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

## **10. Characteristics**

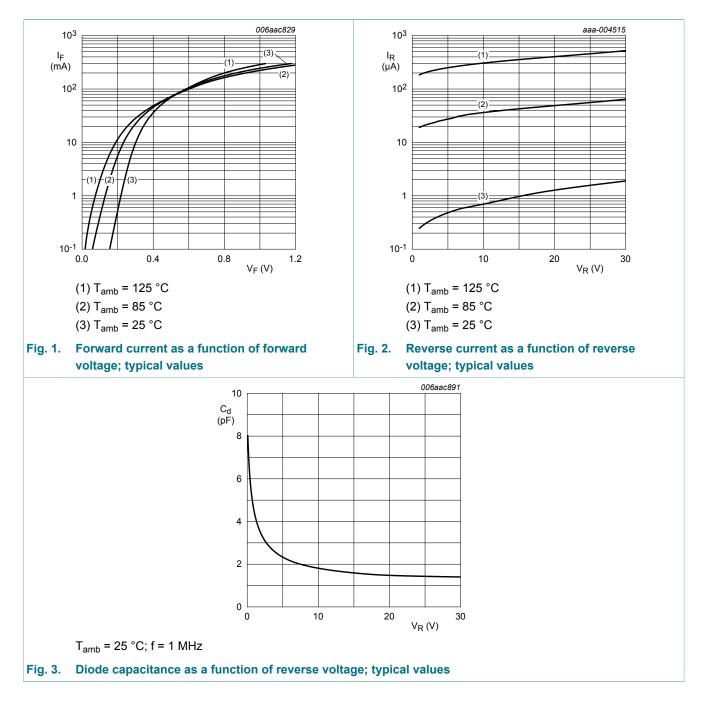
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Per diode						
V <sub>F</sub>	forward voltage	I <sub>F</sub> = 0.1 mA; T <sub>amb</sub> = 25 °C	-	-	240	mV
		I <sub>F</sub> = 1 mA; T <sub>amb</sub> = 25 °C	-	-	320	mV
		I <sub>F</sub> = 10 mA; T <sub>amb</sub> = 25 °C	-	-	400	mV
		I <sub>F</sub> = 30 mA; T <sub>amb</sub> = 25 °C	-	-	500	mV
		I <sub>F</sub> = 100 mA; T <sub>amb</sub> = 25 °C	-	-	800	mV
I <sub>R</sub>	reverse current	$V_R$ = 25 V; pulsed; t <sub>p</sub> = 300 µs; $\delta$ = 0.02 ; T <sub>amb</sub> = 25 °C	-	-	2	μA
C <sub>d</sub>	diode capacitance	V <sub>R</sub> = 1 V; f = 1 MHz; T <sub>amb</sub> = 25 °C	-	-	10	pF



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## 11. Test information

#### **11.1 Quality information**

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - Stress test qualification for discrete semiconductors, and is suitable for use in automotive applications.

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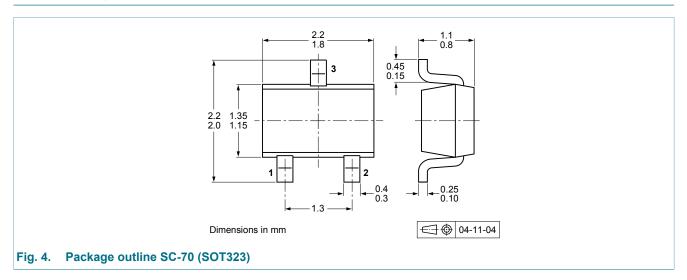


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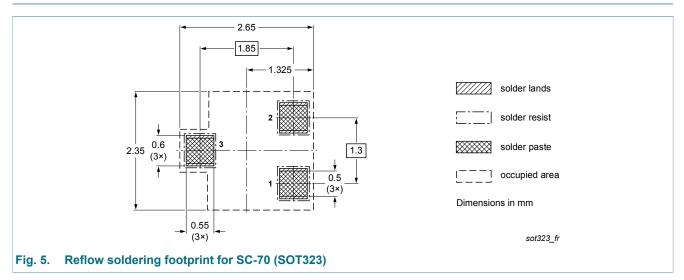
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#### 12. Package outline



## 13. Soldering

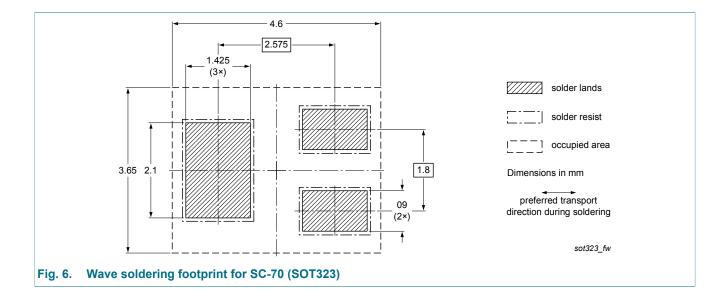




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## 14. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
				-
1PS70SB15 v.2	20121217	Product data sheet	-	1PS70SB10_14_15_16 v.1
Modifications:	of NXP Semiconduc Legal texts have be Sections 1 to 3 upda Section 4 "Quick ref Section 6 "Ordering Section 7 "Marking" Table 5 "Limiting va Figues 1, 2 and 3 up Section 11 "Test info	ctors. en adapted to the new co ated ference data" added information" added updated lues": ambient temperatu pdated prmation" added ed by minimized package ng" added	igned to comply with the ompany name where app are T <sub>amb</sub> and junction tem outline drawing	ropriate.
1PS70SB10_14_15_16 v.1	19990426	Product data sheet	-	-



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## 15. Legal information

#### **15.1 Data sheet status**

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

 Please consult the most recently issued document before initiating or completing a design.

- [2] The term 'short data sheet' is explained in section "Definitions".
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