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SIDC20D60C6

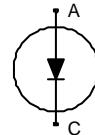
Fast switching diode chip in EMCN 3 -Technology

FEATURES:

- 600V EMCN 3 technology 70 μm chip
- soft, fast switching
- low reverse recovery charge
- small temperature coefficient

This chip is used for:

- power module
- discrete components



Applications:

- drives

Chip Type	V_R	I_F	Die Size	Package
SIDC20D60C6	600V	75A	5.37 x 3.75 mm ²	sawn on foil

MECHANICAL PARAMETER:

Raster size	5.37 x 3.75	mm ²
Area total / active	20.14 / 16.66	
Anode pad size	4.67 x 3.05	
Thickness	70	μm
Wafer size	150	mm
Flat position	180	deg
Max. possible chips per wafer	701 pcs	
Passivation frontside	Photoimide	
Anode metallization	3200 nm AlSiCu	
Cathode metallization	Ni Ag –system suitable for epoxy and soft solder die bonding	
Die bond	electrically conductive glue or solder	
Wire bond	Al, $\leq 500\mu\text{m}$	
Reject ink dot size	$\varnothing 0.65\text{mm}$; max 1.2mm	
Recommended storage environment	store in original container, in dry nitrogen, < 6 month at an ambient temperature of 23°C	



SIDC20D60C6

Maximum Ratings

Parameter	Symbol	Condition	Value	Unit
Repetitive peak reverse voltage	V_{RRM}		600	V
Continuous forward current limited by T_{jmax}	I_F		1)	A
Maximum repetitive forward current limited by T_{jmax}	I_{FRM}		150	
Operating junction and storage temperature	T_j , T_{stg}		-40...+175	°C

1) depending on thermal properties of assembly

Static Electrical Characteristics (tested on chip), $T_j=25$ °C, unless otherwise specified

Parameter	Symbol	Conditions		Value			Unit
				min.	Typ.	max.	
Reverse leakage current	I_R	$V_R=600V$	$T_j=25$ °C			27	µA
Cathode-Anode breakdown Voltage	V_{Br}	$I_R=0.25mA$	$T_j=25$ °C	600			V
Forward voltage drop	V_F	$I_F=75A$	$T_j=25$ °C	1.2	1.6	1.9	V

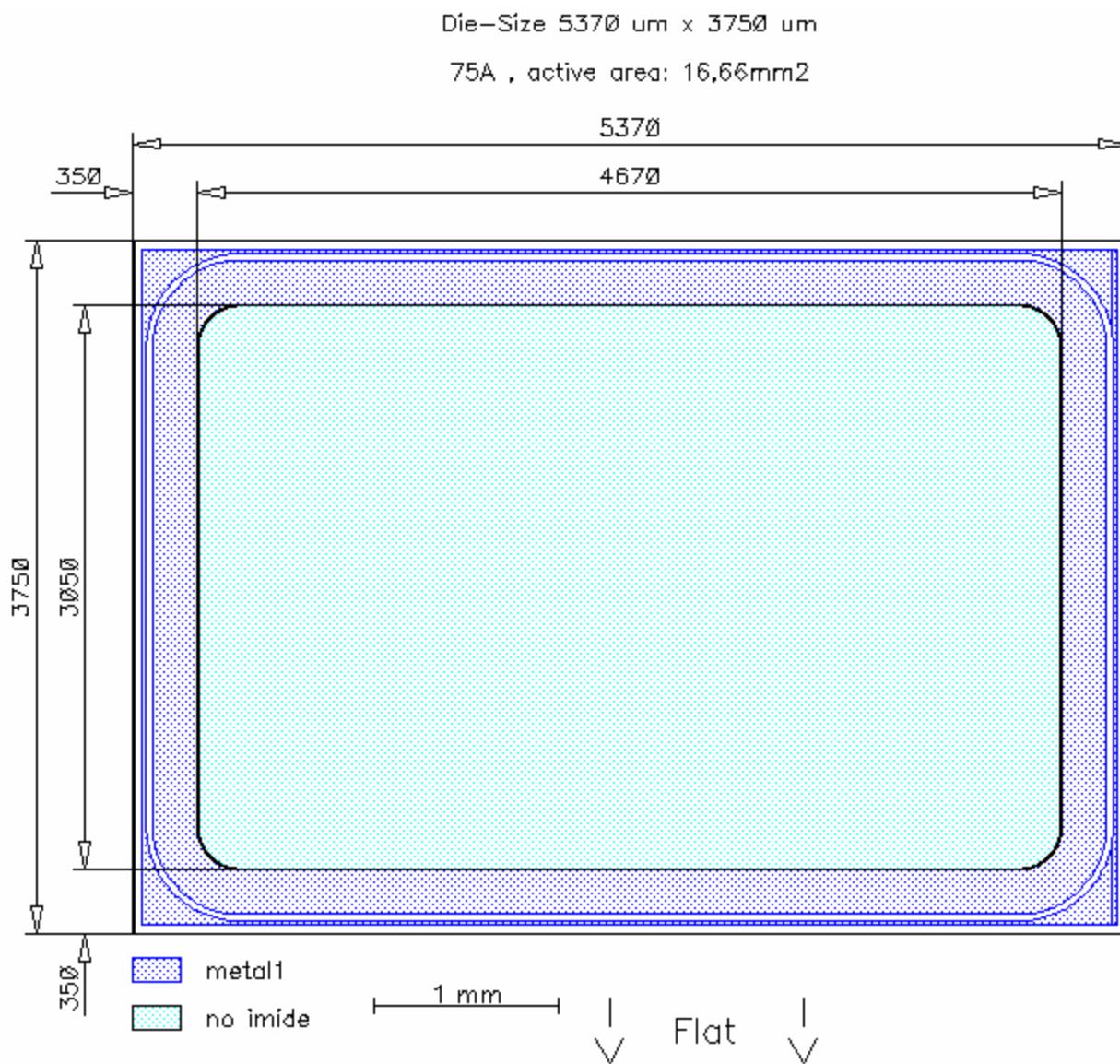
Dynamic Electrical Characteristics (verified by design/characterization), inductive load

Parameter	Symbol	Conditions		Value			Unit
				min.	Typ.	max.	
Peak reverse recovery current	I_{RM}	$I_F=75A$	$T_j = 25$ °C		100		A
		$di/dt=4000A/\text{ms}$	$T_j = 125$ °C		115		
		$V_R=300V$	$T_j = 150$ °C		125		
		$V_{GE} = -15V$					
Recovered charge	Q_r	$I_F=75A$	$T_j = 25$ °C		3.00		µC
		$di/dt=4000A/\text{ms}$	$T_j = 125$ °C		6.00		
		$V_R=300V$	$T_j = 150$ °C		7.50		
		$V_{GE} = -15V$					
Reverse recovery energy	E_{rec}	$I_F=75A$	$T_j = 25$ °C		0.95		mJ
		$di/dt=4000A/\text{ms}$	$T_j = 125$ °C		1.50		
		$V_R=300V$	$T_j = 150$ °C		1.85		
		$V_{GE} = -15V$					

2) values also influenced by parasitic L- and C- in measurement and package.



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CHIP DRAWING:



SIDC20D60C6

FURTHER ELECTRICAL CHARACTERISTICS:

This chip data sheet refers to the
device data sheet FS75R06KE3

Description:

AQL 0,65 for visual inspection according to failure catalog

Electrostatic Discharge Sensitive Device according to MIL-STD 883

Test-Normen Villach/Prüffeld

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