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STS17NH3LL

N-channel 30 V - 0.004 Ω - 17 A - SO-8
STripFET™ Power MOSFET for DC-DC conversion

Features

Type	V _{DSS}	R _{DS(on)}	I _D
STS17NH3LL	30V	<0.0057 Ω	17A ⁽¹⁾

1. This value is rated according to R_{thj-pcb}

- Optimal R_{DS(on)} x Q_g trade-off @ 4.5 V
- Conduction losses reduced
- Improved junction-case thermal resistance
- Low threshold device

Applications

- Switching application

Description

This device utilizes the latest advanced design rules of ST's proprietary STripFET™ technology. This process coupled to unique metallization techniques realizes the most advanced low voltage Power MOSFET in SO-8 ever produced.

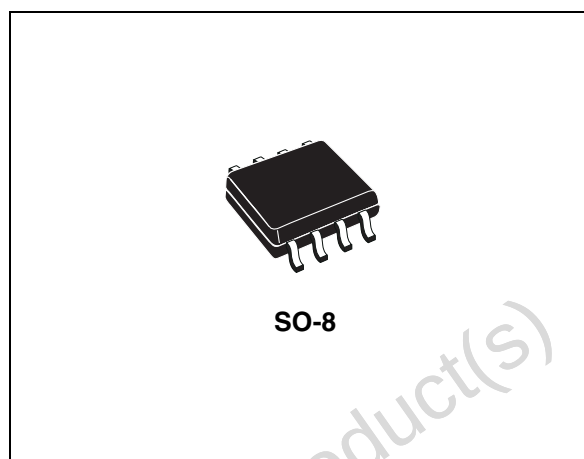


Figure 1. Internal schematic diagram

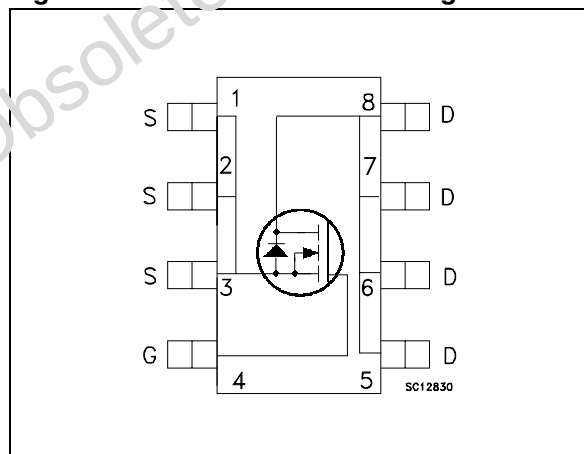


Table 1. Device summary

Order code	Marking	Package	Packaging
STS17NH3LL	17H3LL-	SO-8	Tape & reel

Contents

1	Electrical ratings	3
2	Electrical characteristics	4
	2.1 Electrical characteristics (curves)	6
3	Test circuit	8
4	Package mechanical data	9
5	Revision history	11

STS17NH3LL

Electrical ratings

1 Electrical ratings

Table 2. Absolute maximum ratings

Symbol	Parameter	Value	Unit
V_{DS}	Drain-source voltage ($V_{GS} = 0$)	30	V
V_{GS}	Gate- source voltage	± 16	V
$I_D^{(1)}$	Drain current (continuous) at $T_C = 25^\circ\text{C}$	17	A
I_D	Drain current (continuous) at $T_C = 100^\circ\text{C}$	10.6	A
$I_{DM}^{(2)}$	Drain current (pulsed)	68	A
$P_{tot}^{(1)}$	Total dissipation at $T_C = 25^\circ\text{C}$	2.7	W
T_{stg}	Storage temperature	-55 to 150	$^\circ\text{C}$
T_j	Operating junction temperature		

1. This value is rated according to $R_{thj-pcb}$
2. Pulse width limited by safe operating area

Table 3. Thermal resistance

Symbol	Parameter	Value	Unit
$R_{thj-pcb}^{(1)}$	Thermal resistance junction-ambient max	47	$^\circ\text{C/W}$

1. When mounted on 1inch² FR-4 board, 2oz of Cu and $t < 10\text{sec}$

Table 4. Avalanche data

Symbol	Parameter	Value	Unit
I_{AV}	Not-repetitive avalanche current	7.5	A
E_{AS}	Single pulse avalanche energy (starting $T_j = 25^\circ\text{C}$, $I_d = I_{AV}$)	150	mJ

Electrical characteristics

STS17NH3LL

2 Electrical characteristics

(T_{CASE}=25°C unless otherwise specified)

Table 5. On/off states

Symbol	Parameter	Test conditions	Min	Typ.	Max	Unit
V _{(BR)DSS}	Drain-source breakdown voltage	I _D = 250 µA, V _{GS} = 0	30			V
I _{DSS}	Zero gate voltage drain current (V _{GS} = 0)	V _{DS} = Max rating V _{DS} = Max rating @ 125°C			1 10	µA µA
I _{GSS}	Gate-body leakage current (V _{DS} = 0)	V _{GS} = ± 16 V			±100	nA
V _{GS(th)}	Gate threshold voltage	V _{DS} = V _{GS} , I _D = 250 µA	1			V
R _{DS(on)}	Static drain-source on resistance	V _{GS} = 10 V, I _D = 8.5 A V _{GS} = 4.5 V, I _D = 8.5 A		0.004 0.005	0.0057 0.0075	Ω Ω

Table 6. Dynamic

Symbol	Parameter	Test conditions	Min	Typ.	Max	Unit
C _{iss} C _{oss} C _{rss}	Input capacitance Output capacitance Reverse transfer capacitance	V _{DS} =25 V, f=1 MHz V _{GS} = 0		1810 565 41		pF pF pF
Q _g Q _{gs} Q _{gd}	Total gate charge Gate-source charge Gate-drain charge	V _{DD} =15 V, I _D =17 A V _{GS} =4.5 V (see Figure 14)		18 4.8 5.3	24	nC nC nC
R _G	Gate input resistance	f=1 MHz Gate DC Bias = 0 Test signal level = 20 mV open drain	0.5	1.5	3	Ω

STS17NH3LL

Electrical characteristics

Table 7. Switching times

Symbol	Parameter	Test conditions	Min	Typ.	Max	Unit
$t_{d(on)}$	Turn-on delay time	$V_{DD} = 15\text{ V}$, $I_D = 8.5\text{ A}$		8		ns
t_r	Rise time	$R_G = 4.7\ \Omega$, $V_{GS} = 10\text{ V}$ (see Figure 16)		65		ns
$t_{d(off)}$	Turn-off delay time	$V_{DD} = 15\text{ V}$, $I_D = 8.5\text{ A}$		38		ns
t_f	Fall time	$R_G = 4.7\ \Omega$, $V_{GS} = 10\text{ V}$ (see Figure 16)		20		ns

Table 8. Source drain diode

Symbol	Parameter	Test conditions	Min	Typ.	Max	Unit
I_{SD}	Source-drain current				17	A
I_{SDM}	Source-drain current (pulsed)				68	A
$V_{SD}^{(1)}$	Forward on voltage	$I_{SD} = 17\text{ A}$, $V_{GS} = 0$			1.3	V
t_{rr}	Reverse recovery time	$I_{SD} = 17\text{ A}$, $di/dt = 100\text{ A}/\mu\text{s}$		22		ns
Q_{rr}	Reverse recovery charge	$V_{DD} = 15\text{ V}$, $T_j = 25^\circ\text{C}$		32		nC
I_{RRM}	Reverse recovery current	(see Figure 15)		1.9		A

1. Pulsed: pulse duration=300μs, duty cycle 1.5%

Electrical characteristics

STS17NH3LL

2.1 Electrical characteristics (curves)

Figure 2. Safe operating area

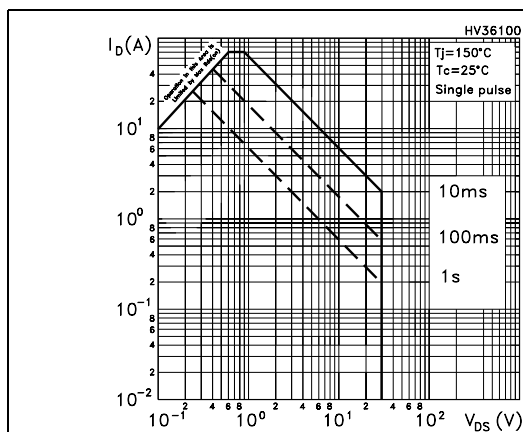


Figure 3. Thermal impedance

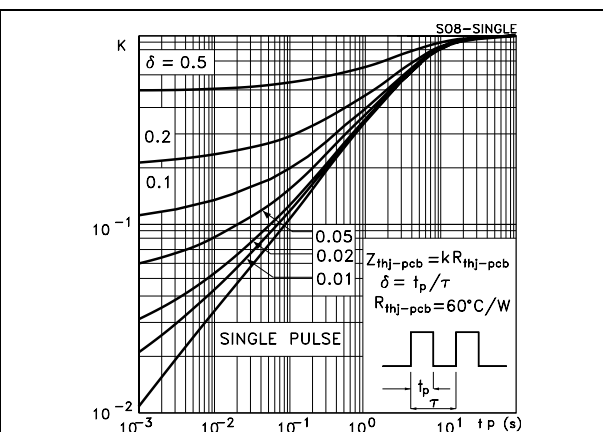


Figure 4. Output characteristics

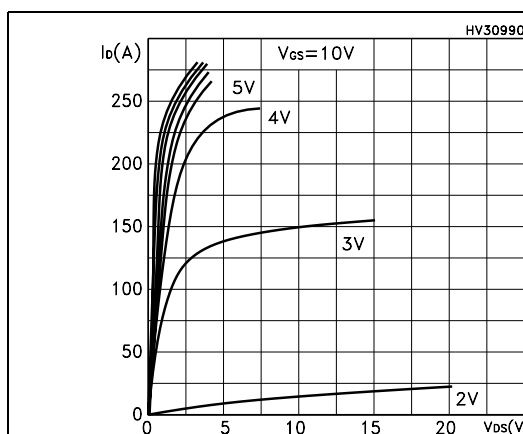


Figure 5. Transfer characteristics

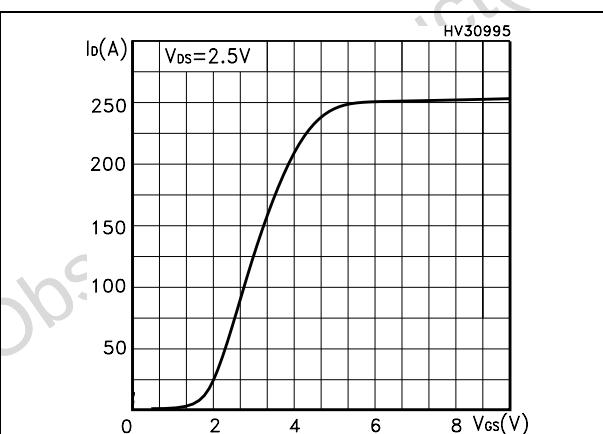


Figure 6. Normalized $B_{V_{DS}}$ vs temperature

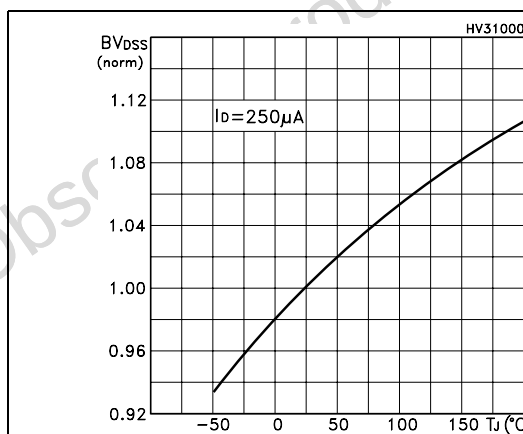
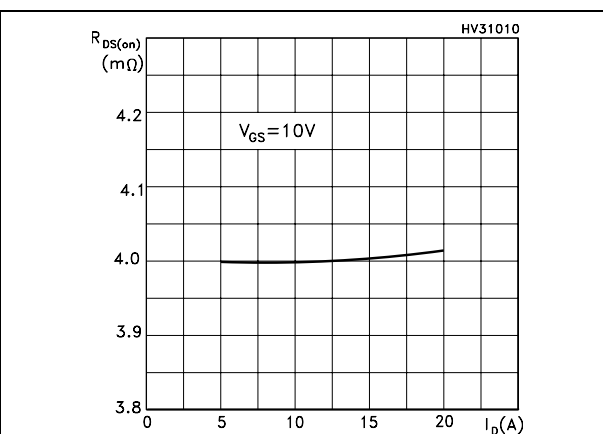


Figure 7. Static drain-source on resistance



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Electrical characteristics

Figure 8. Gate charge vs gate-source voltage **Figure 9. Capacitance variations**

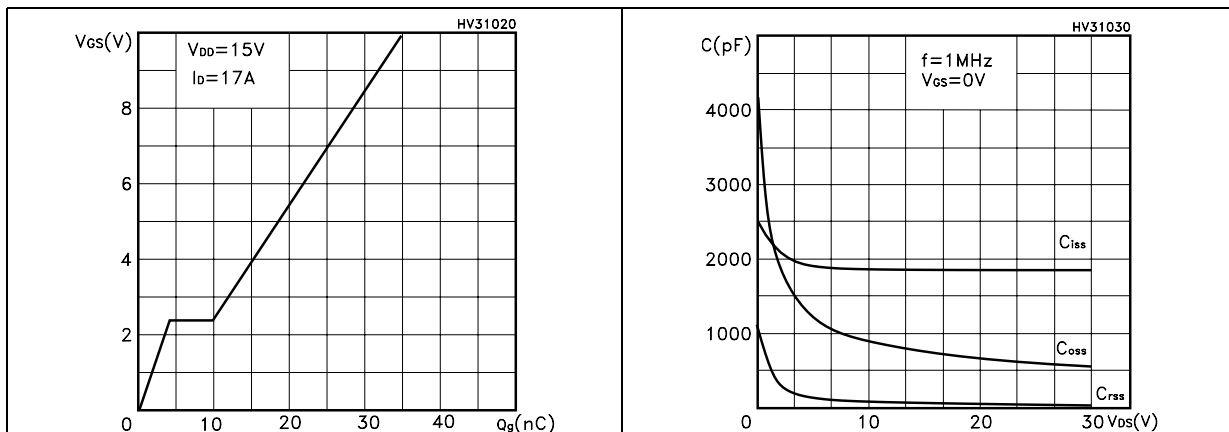


Figure 10. Normalized gate threshold voltage vs temperature **Figure 11. Normalized on resistance vs temperature**

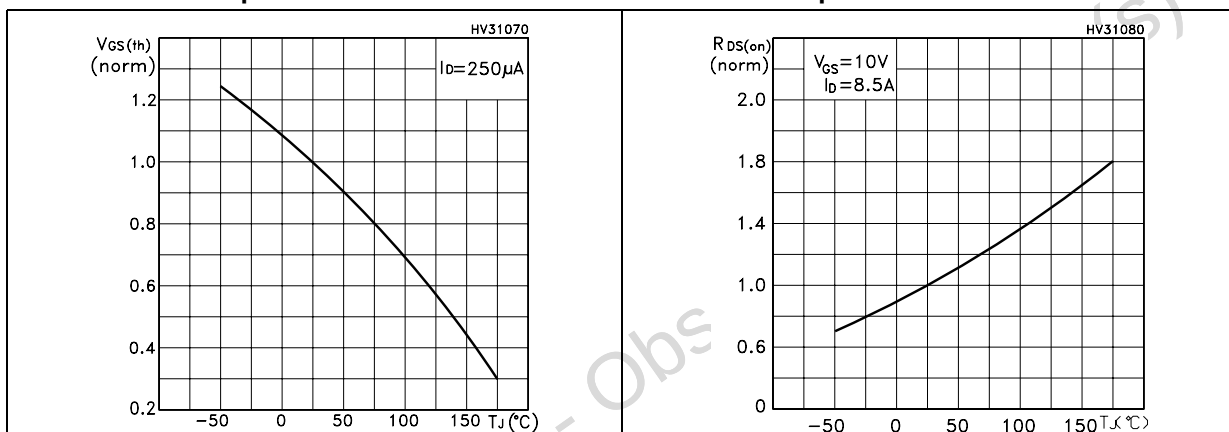
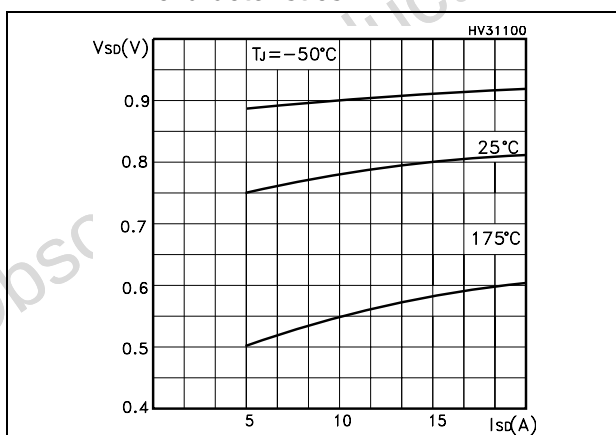


Figure 12. Source-drain diode forward characteristics



Test circuit

STS17NH3LL

3 Test circuit

Figure 13. Switching times test circuit for resistive load

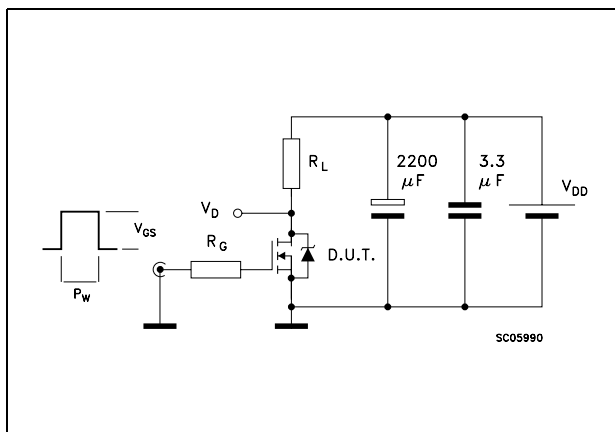


Figure 14. Gate charge test circuit

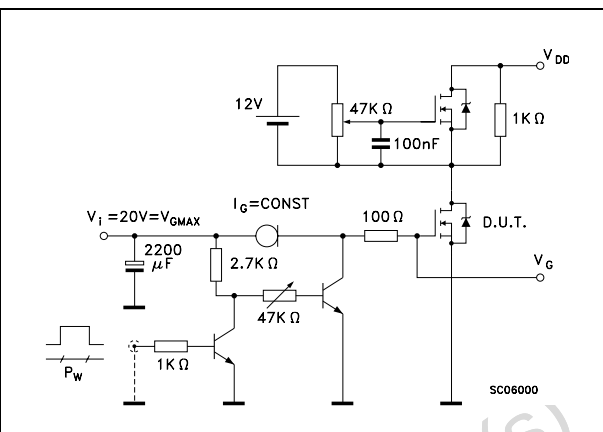


Figure 15. Test circuit for inductive load switching and diode recovery times

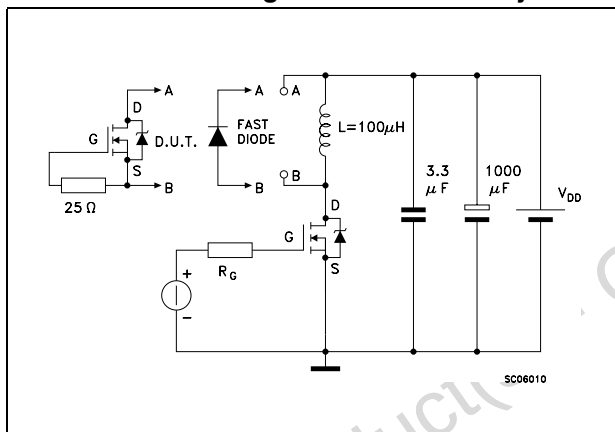


Figure 16. Unclamped inductive load test circuit

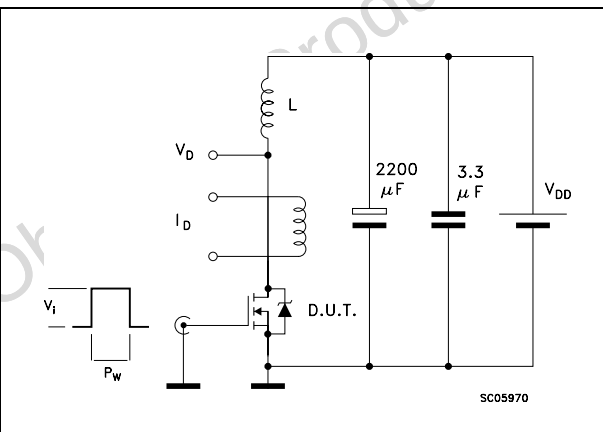


Figure 17. Unclamped inductive waveform

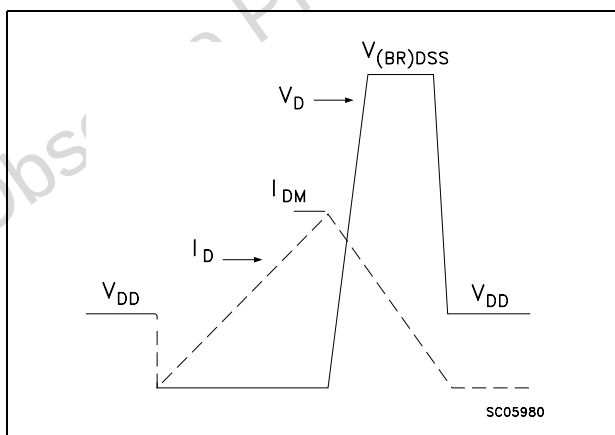
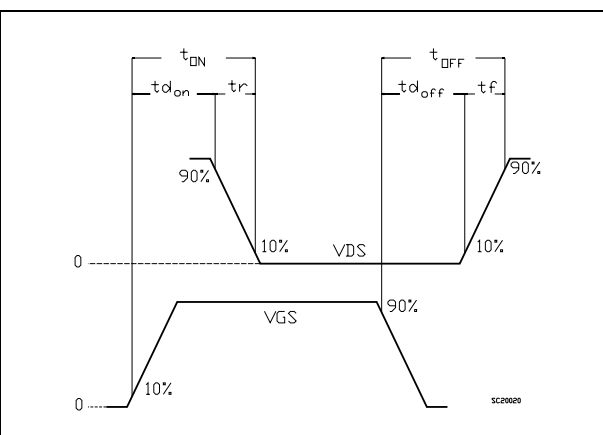


Figure 18. Switching time waveform



4 Package mechanical data

In order to meet environmental requirements, ST offers these devices in ECOPACK® packages. These packages have a lead-free second level interconnect . The category of second level interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available at: www.st.com

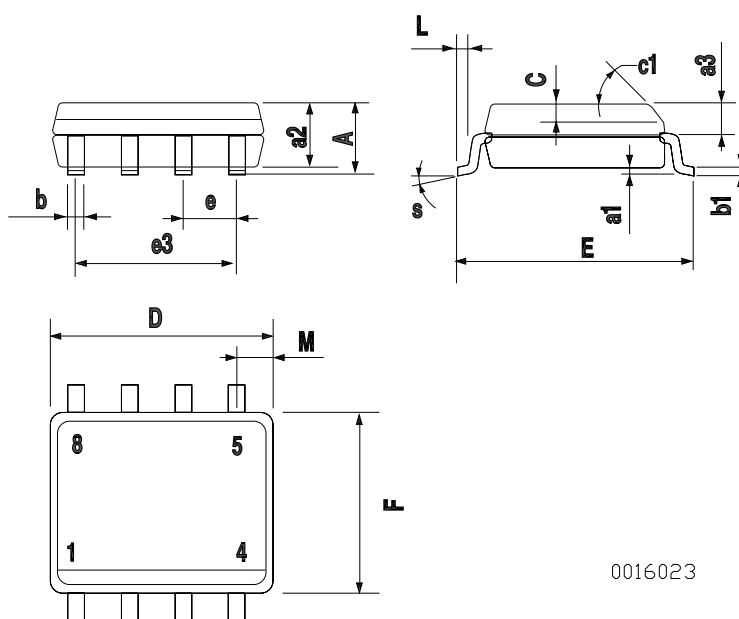
Obsolete Product(s) - Obsolete Product(s)

Package mechanical data

STS17NH3LL

SO-8 MECHANICAL DATA

DIM.	mm.			inch		
	MIN.	TYP	MAX.	MIN.	TYP.	MAX.
A			1.75			0.068
a1	0.1		0.25	0.003		0.009
a2			1.65			0.064
a3	0.65		0.85	0.025		0.033
b	0.35		0.48	0.013		0.018
b1	0.19		0.25	0.007		0.010
C	0.25		0.5	0.010		0.019
c1	45 (typ.)					
D	4.8		5.0	0.188		0.196
E	5.8		6.2	0.228		0.244
e		1.27			0.050	
e3		3.81			0.150	
F	3.8		4.0	0.14		0.157
L	0.4		1.27	0.015		0.050
M			0.6			0.023
S	8 (max.)					



0016023

5 Revision history

Table 9. Document revision history

Date	Revision	Changes
01-Aug-2006	1	First release
09-Jan-2007	2	Complete version
12-Dec-2007	3	Inserted new Table 4: Avalanche data

STS17NH3LL

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