

Excellent Integrated System Limited

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Fairchild Semiconductor FJY4010R

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FAIRCHILD SEMICONDUCTOR®

FJY4010R PNP Epitaxial Silicon Transistor

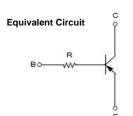
Features

- Switching circuit, Inverter, Interface circuit, Driver Circuit
- Built in bias Resistor (R=10K Ω)
- Complement to FJY3010R





SOT - 523F



Absolute Maximum Ratings * T_a = 25°C unless otherwise noted

Symbol	Parameter	Value	Units
V _{CBO}	Collector-Base Voltage	-40	V
V _{CEO} Collector-Emitter Voltage		-40	V
V _{EBO}	Emitter-Base Voltage	-5	V
I _C	Collector Current	-100	mA
T _{STG}	Storage Temperature Range	-55~150	°C
TJ	Junction Temperature	150	°C
P _C	Collector Power Dissipation, by $R_{\theta JA}$	200	mW

* These ratings are limiting values above which the serviceability of any semiconductor device may by impaired.

Thermal Characteristics* Ta=25°C unless otherwise noted

Symbol	Parameter	Max	Units
R_{\thetaJA}	Thermal Resistance, Junction to Ambient	600	°C/W
* Minimum land had size			

Electrical Characteristics* T_c = 25°C unless otherwise noted

Symbol	Parameter	Test Condition	MIN	Тур	MAX	Units
V(BR)CBO	Collector-Emitter Breakdown Voltage	Ic = -100 uA, IE = 0	-40			V
V(BR)CEO	Collector-Base Breakdown Voltage	Ic = -1mA, I _B = 0	-40			V
Ісво	Collector-Cutoff Current	Vcb = -30 V, IE = 0			-0.1	uA
hfe	DC Current Gain	Vce = -5 V, Ic = -1 mA	100		600	
Vce(sat)	Collector-Emitter Saturation Voltage	Ic = -10 mA, I _B = -1 mA			-0.3	V
f⊤	Current Gain - Bandwidth Product	Vce = -10V, Ic =- 5 mA		200		MHz
Ccb	Output Capacitance	V _{CB} = -10 V, I _E = 0, f = 1.0 MHz		5.5		pF
R	Input Resistor		7	10	13	KΩ

* Pulse Test: PW≤300μs, Duty Cycle≤2%

July 2007



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Typical Performance Characteristics

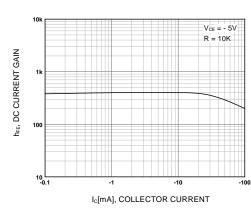


Figure 1. DC current Gain

Figure 3. Power Derating

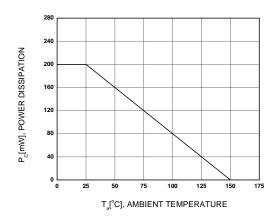
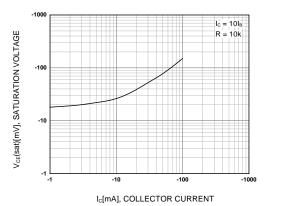
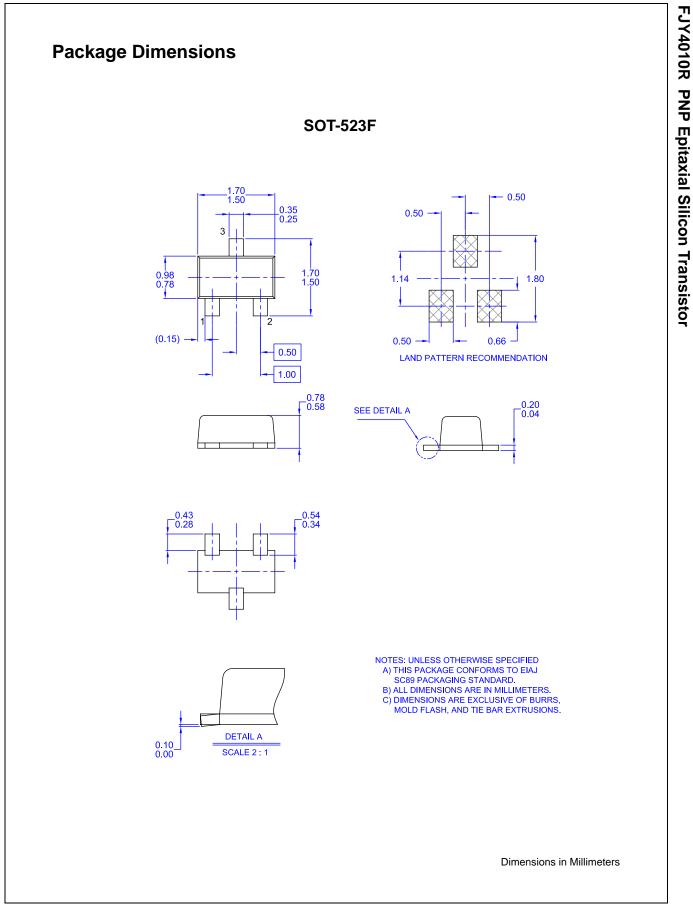


Figure 2. Collector-Emitter Saturation Voltage





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