Excellent Integrated System Limited

Stocking Distributor

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ON Semiconductor 2SA1020

For any questions, you can email us directly: sales@integrated-circuit.com

Distributor of ON Semiconductor: Excellent Integrated System Limited

Datasheet of 2SA1020 - TRANS PNP 50V 2A TO-92

Contact us: sales@integrated-circuit.com Website: www.integrated-circuit.com

2SA1020

Preferred Device

One Watt High Current PNP Transistor

Features

• Pb-Free Packages are Available*

MAXIMUM RATINGS

| Rating | Symbol | Value | Unit |
|--|-----------------------------------|----------------|-------------|
| Collector - Emitter Voltage | V _{CE} | 50 | Vdc |
| Collector - Base Voltage | V _{CB} | 50 | Vdc |
| Emitter – Base Voltage | V _{EB} | 5.0 | Vdc |
| Collector Current - Continuous | Ic | 2.0 | Adc |
| Total Power Dissipation @ T _A = 25°C Derate above 25°C | P _D | 900 5.0 | mW mW/°C |
| Total Power Dissipation @ T _C = 25°C Derate above 25°C | P _D | 1.5 12 | W mW/°C |
| Operating and Storage Junction Temperature Range | T _J , T _{stg} | -55 to +150 | °C |

THERMAL CHARACTERISTICS

| Characteristic | Symbol | Max | Unit |
|---|-----------------|------|------|
| Thermal Resistance, Junction-to-Ambient | $R_{\theta JA}$ | 125 | °C/W |
| Thermal Resistance, Junction-to-Case | $R_{\theta JC}$ | 83.3 | °C/W |

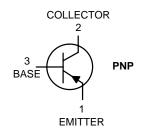
Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.



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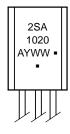
http://onsemi.com

VOLTAGE AND CURRENT ARE NEGATIVE FOR PNP TRANSISTORS





MARKING DIAGRAM



A = Assembly Location

Y = Year

WW = Work Week
■ Pb–Free Package

(Note: Microdot may be in either location)

ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 2 of this data sheet.

Preferred devices are recommended choices for future use and best overall value.

^{*}For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise noted)

| Characteristic | Symbol | Min | Max | Unit |
|--|----------------------|----------|----------|------|
| OFF CHARACTERISTICS | <u>.</u> | | | |
| Collector – Emitter Breakdown Voltage (Note 1) (I _C = 10 mAdc, I _B = 0) | V _{(BR)CEO} | 50 | _ | Vdc |
| Collector Cutoff Current $(V_{CB} = 50 \text{ Vdc}, I_E = 0)$ | I _{CBO} | _ | 1.0 | μAdc |
| Emitter Cutoff Current (V _{EB} = 5.0 V, I _C = 0) | I _{EBO} | - | 1.0 | μAdc |
| ON CHARACTERISTICS (Note 2) | <u>.</u> | | | |
| DC Current Gain $(I_C = 500 \text{ mA}, V_{CE} = 2.0 \text{ V})$ $(I_C = 1.5 \text{ A}, V_{CE} = 2.0 \text{ V})$ | h _{FE} | 70 40 | 240 - | - |
| Collector – Emitter Saturation Voltage (I _C = 1.0 A, I _B = 50 mA) | V _{CE(sat)} | _ | 0.5 | Vdc |
| Base – Emitter Saturation Voltage (I _C = 1.0 A, I _B = 50 mA) | V _{BE(sat)} | _ | 1.2 | Vdc |
| SMALL-SIGNAL CHARACTERISTICS | • | • | | • |
| Current – Gain – Bandwidth Product (Note 3) (I _C = 500 mAdc, V _{CE} = 2.0 Vdc, f = 100 MHz) | f _T | 100 | _ | MHz |

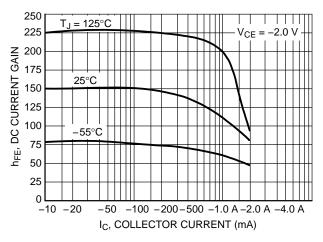
ORDERING INFORMATION

| Device | Package | Shipping [†] | |
|--------------|--------------------|-----------------------|--|
| 2SA1020 | TO-92 | | |
| 2SA1020G | TO-92 (Pb-Free) | 5000 Units / Box | |
| 2SA1020RLRA | TO-92 | | |
| 2SA1020RLRAG | TO-92 (Pb-Free) | 2000 / Tape & Reel | |

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

Pulse Test: Pulse Width ≤ 300 μs, Duty Cycle = 2.0%.
 Pulse Test: Pulse Width ≤ 300 μs, Duty Cycle = 2.0%.
 f_T is defined as the frequency at which |h_{fe}| extrapolates to unity.

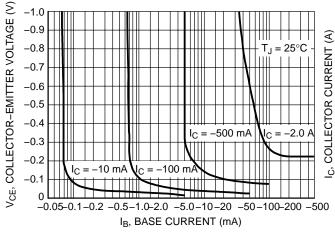
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-1.8 -1.6 VOLTAGE (VOLTS) -1.4V_{BE(sat)} @ I_C/I_B = 10 -1.0-0.8 $V_{BE(on)}$ -0.6 -0.4 $V_{CE(sat)} @ I_C/I_B = 10$ -0.20 l -50 -200 –500 –1.0 A –2.0 A –4.0 A IC, COLLECTOR CURRENT (mA)

Figure 1. Typical DC Current Gain

Figure 2. On Voltages



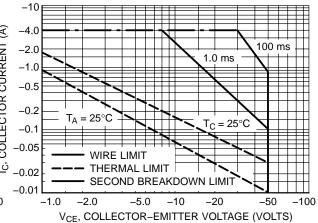


Figure 3. Collector Saturation Region

Figure 4. Safe Operating Area



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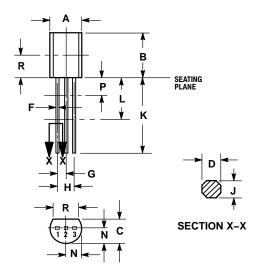
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PACKAGE DIMENSIONS

TO-92 (TO-226) CASE 29-10 **ISSUE AL**



- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M. 1982.
- 114.5M, 1992.

 2. CONTROLLING DIMENSION: INCH.

 3. CONTOUR OF PACKAGE BEYOND DIMENSION R
 IS UNCONTROLLED.
- IS UNCONTROLLED.

 A DIMENSION F APPLIES BETWEEN P AND L.

 DIMENSIONS D AND J APPLY BETWEEN L AND K.

 MIMIMUM. LEAD DIMENSION IS UNCONTROLLED. IN P AND BEYOND DIMENSION K MINIMUM.

| | INC | INCHES MILLIMETER | | IETERS |
|-----|-------|-------------------|-------|--------|
| DIM | MIN | MAX | MIN | MAX |
| Α | 0.175 | 0.205 | 4.44 | 5.21 |
| В | 0.290 | 0.310 | 7.37 | 7.87 |
| С | 0.125 | 0.165 | 3.18 | 4.19 |
| D | 0.018 | 0.021 | 0.457 | 0.533 |
| F | 0.016 | 0.019 | 0.407 | 0.482 |
| G | 0.045 | 0.055 | 1.15 | 1.39 |
| Н | 0.095 | 0.105 | 2.42 | 2.66 |
| J | 0.018 | 0.024 | 0.46 | 0.61 |
| K | 0.500 | | 12.70 | |
| L | 0.250 | | 6.35 | |
| N | 0.080 | 0.105 | 2.04 | 2.66 |
| P | | 0.100 | | 2.54 |
| R | 0.135 | | 3.43 | |

STYLE 14: PIN 1. EMITTER

2. COLLECTOR

3. BASE

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