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ON Semiconductor MC10H136FN

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MC10H136

Universal Hexadecimal Counter

Description

The MC10H136 is a high speed synchronous hexadecimal counter. This 10H part is a functional/pinout duplication of the standard MECL $10K^{TM}$ family part, with 100% improvement in counting frequency and no increase in power-supply current.

Features

- Counting Frequency, 250 MHz Minimum
- Power Dissipation, 625 mW Typical
- Improved Noise Margin 150 mV (Over Operating Voltage and Temperature Range)
- Voltage Compensated
- MECL 10K Compatible
- Pb-Free Packages are Available*



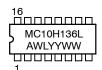
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MARKING DIAGRAMS*







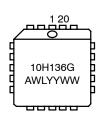


PDIP-16 P SUFFIX CASE 648





PLLC-20 FN SUFFIX CASE 775



A = Assembly Location

WL = Wafer Lot
 YY = Year
 WW = Work Week
 G = Pb-Free Package

ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 5 of this data sheet.

^{*}For additional marking information, refer to Application Note AND8002/D.

^{*}For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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Table 1. FUNCTION SELECT TABLE

CĪN	S1	S2	Operating Mode
Х	L	L	Preset (Program)
L	L	Н	Increment (Count Up)
Н	L	Н	Hold Count
L	Н	L	Decrement (Count Down)
Н	Н	L	Hold Count
Х	Н	Н	Hold (Stop Count)

Table 2. SEQUENTIAL TRUTH TABLE*

	INPUTS						OI	UTP	UTS			
S1	S2	D0	D1	D2	D3	Carry In	Clock * *	Q	Q1	Q2	QЗ	Carry Out
L	L	Г	L	Н	Н	Х	Н	L	Г	Η	Н	L
L	Н	Х	Х	X	Х	L	Н	Н	L	Н	Н	Н
L	Н	Х	Х	Х	Х	L	Н	L	Н	Н	Н	Н
L	Н	Х	Х	Х	Х	L	Н	Н	Н	Н	Н	L
L	Н	Х	Х	Х	Χ	Н	L	Н	Н	Н	Н	Н
L	Н	Х	Χ	X	Х	Н	Н	Н	Н	Н	Н	Н
н	Н	Х	Χ	X	Х	Х	Н	Н	Н	Н	Н	Н
L	L	Н	Н	L	L	Х	Н	Н	Н	L	L	L
Н	L	Х	Χ	Х	Х	L	Н	L	Н	Ь	┙	Н
н	L	Х	Х	Х	Х	L	Н	Н	L	L	L	Н
Н	L	Х	Χ	Х	Х	L	Н	L	L	L	L	L
Н	L	Х	Х	Χ	Х	L	Н	Н	Н	Η	Н	Н

Pin assignment is for Dual-in-Line Package.

Figure 1. Pin Assignment

Table 3. MAXIMUM RATINGS

Symbol	Characteristic	Rating	Unit
V _{EE}	Power Supply (V _{CC} = 0)	-8.0 to 0	Vdc
VI	Input Voltage (V _{CC} = 0)	0 to V _{EE}	Vdc
l _{out}	Output Current - Continuous - Surge	50 100	mA
T _A	Operating Temperature Range	0 to +75	°C
T _{stg}	Storage Temperature Range - Plastic - Ceramic	−55 to +150 −55 to +165	°C °C

Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.

 V_{CC2} Q2 Q1 15 Q3 3 14 Q0 $\overline{C_{\text{OUT}}}$ 13 CLOCK D3 D0 D2 D1 10 S2 CIN 8 9 S1 V_{EE}

^{*} Truth table shows logic states assuming inputs vary in sequence shown from top to bottom.
** A clock H is defined as a clock input transition from a low to a high logic level.

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Table 4. ELECTRICAL CHARACTERISTICS (V_{EE} = -5.2 V $\pm 5\%$) (Note 1)

		O) °	2	5°	7	75°	
Symbol	Characteristic	Min	Max	Min	Max	Min	Max	Unit
ΙE	Power Supply Current	-	165	-	150	-	165	mA
I _{inH}	Input Current High							μΑ
	Pins 5, 6, 11, 12, 13	-	430	_	275	_	275	
	Pin 9	-	670	_	420	_	420	
	Pin 7	-	535	_	335	_	335	
	Pin 10	-	380	_	240	_	240	
I _{inL}	Input Current Low	0.5	-	0.5	-	0.3	-	μΑ
V _{OH}	High Output Voltage	-1.02	-0.84	-0.98	-0.81	-0.92	-0.735	Vdc
V _{OL}	Low Output Voltage	-1.95	-1.63	-1.95	-1.63	-1.95	-1.60	Vdc
V _{IH}	High Input Voltage	-1.17	-0.84	-1.13	-0.81	-1.07	-0.735	Vdc
V _{IL}	Low Input Voltage	-1.95	-1.48	-1.95	-1.48	-1.95	-1.45	Vdc

Each MECL 10H™ series circuit has been designed to meet the dc specifications shown in the test table, after thermal equilibrium has been established. The circuit is in a test socket or mounted on a printed circuit board and transverse air flow greater than 500 linear fpm is maintained. Outputs are terminated through a 50 Ω resistor to −2.0 V.

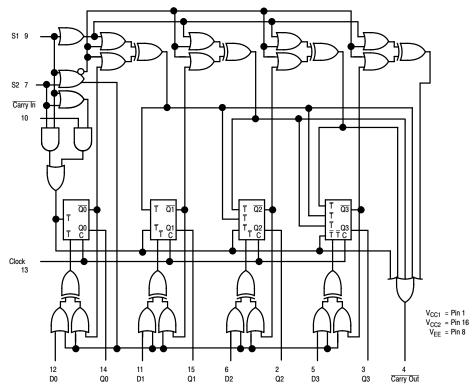


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Table 5. AC CHARACTERISTICS

		0 °		25	5°	7	75°	
Symbol	Characteristic	Min	Max	Min	Max	Min	Max	Unit
t _{pd}	Propagation Delay							ns
	Clock to Q	0.7	2.3	0.7	2.4	0.7	2.5	
	Clock to Carry Out	1.0	4.8	1.0	4.9	1.0	5.0	
	Carry in to Carry							
	Out	0.7	2.5	0.7	2.6	0.7	2.7	
t _{set}	Set-up Time							ns
	Data (D0 to C)	2.0	-	2.0	-	2.0	_	
	Select (S to C)	3.5	-	3.5	_	3.5	_	
	Carry In (C _{in} to C)	2.0	-	2.0	_	2.0	_	
	(C to C _{in})	0	-	0	-	0	-	
t _{hold}	Hold Time							ns
	Data (C to D0)	0	-	0	-	0	_	
	Select (C to S)	-0.5	_	-0.5	-	-0.5	_	
	Carry In (C to C _{in})	0	-	0	-	0	_	
	(C _{in} to C)	2.2	-	2.2	-	2.2	-	
f _{count}	Counting Frequency	250	-	250	-	250	-	MHz
t _r	Rise Time	0.5	2.3	0.5	2.4	0.5	2.5	ns
t _f	Fall Time	0.5	2.3	0.5	2.4	0.5	2.5	ns

NOTE: Device will meet the specifications after thermal equilibrium has been established when mounted in a test socket or printed circuit board with maintained transverse airflow greater than 500 lfpm. Electrical parameters are guaranteed only over the declared operating temperature range. Functional operation of the device exceeding these conditions is not implied. Device specification limit values are applied individually under normal operating conditions and not valid simultaneously.



NOTE: FLIP-FLOPS WILL TOGGLE WHEN ALL $\overline{\mathsf{T}}$ INPUTS ARE LOW.

Figure 2. Logic Diagram



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APPLICATION INFORMATION

The MC10H136 is a high speed synchronous counter that operates at 250 MHz. Counter operating modes include count up, count down, pre-set and hold count. This device allows the designer to use one basic counter for many applications.

The S1, S2, control lines determine the operating modes of the counter. In the pre-set mode, a clock pulse is necessary to load the counter with the information present on the data inputs (D0, D1, D2, and D3). Carry out goes low on the terminal count or when the counter is being pre-set.

ORDERING INFORMATION

Device	Package	Shipping [†]
MC10H136FN	PLLC-20	46 Units / Rail
MC10H136FNG	PLLC-20 (Pb-Free)	46 Units / Rail
MC10H136FNR2	PLLC-20	500 / Tape & Reel
MC10H136FNR2G	PLLC-20 (Pb-Free)	500 / Tape & Reel
MC10H136L	CDIP-16	25 Unit / Rail
MC10H136P	PDIP-16	25 Unit / Rail
MC10H136PG	PDIP-16 (Pb-Free)	25 Unit / Rail

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

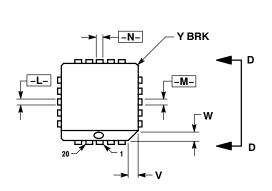
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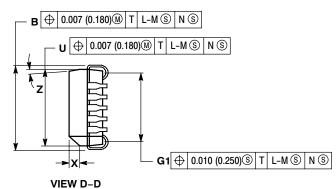
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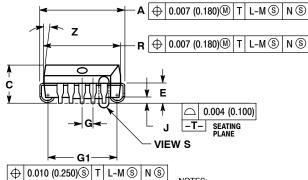
MC10H136

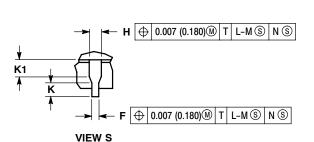
PACKAGE DIMENSIONS

20 LEAD PLLC CASE 775-02 **ISSUE E**









- NOTES:
 1. DIMENSIONS AND TOLERANCING PER ANSI Y14.5M,

- DIMENSIONS AND TOLERANGING FER ANSI T14.5M, 1982.
 DIMENSIONS IN INCHES.
 DATUMS -L-, -M-, AND -N- DETERMINED WHERE TOP OF LEAD SHOULDER EXITS PLASTIC BODY AT MOLD PARTING LINE.
 DIMENSION G1, TRUE POSITION TO BE MEASURED AT DATUM -T-, SEATING PLANE.
 DIMENSIONS R AND U DO NOT INCLUDE MOLD FLASH. ALLOWABLE MOLD FLASH IS 0.010 (0.250) PER SIDE.
 DIMENSIONS IN THE PACKAGE TOP MAY BE SMALLER THAN THE PACKAGE BOTTOM BY UP TO 0.012 (0.300). DIMENSIONS R AND U ARE DETERMINED AT THE OUTERMOST EXTREMES OF THE PLASTIC BODY EXCLUSIVE OF MOLD FLASH, TIE BAR BURRS, GATE BURRS AND INTERLEAD FLASH, BUT INCLUDING ANY MISMATCH BETWEEN THE TOP AND BOTTOM OF THE
- BURRS AND INTERLEAD FLASH, BUT INCLUDING ANY MISMATCH BETWEEN THE TOP AND BOTTOM OF THE PLASTIC BODY.
 DIMENSION H DOES NOT INCLUDE DAMBAR PROTRUSION OR INTRUSION. THE DAMBAR PROTRUSION(S) SHALL NOT CAUSE THE H DIMENSION TO BE GREATER THAN 0.037 (0.940). THE DAMBAR INTRUSION(S) SHALL NOT CAUSE THE H DIMENSION TO BE SMALLER THAN 0.025 (0.635).

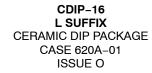
	INC	HES	MILLIM	IETERS		
DIM	MIN	MAX	MIN	MAX		
Α	0.385	0.395	9.78	10.03		
В	0.385	0.395	9.78	10.03		
ဂ	0.165	0.180	4.20	4.57		
Е	0.090	0.110	2.29	2.79		
F	0.013	0.019	0.33	0.48		
G	0.050	BSC	1.27	BSC		
H	0.026	0.032	0.66	0.81		
J	0.020	-	0.51			
K	0.025		0.64			
R	0.350	0.356	8.89	9.04		
U	0.350	0.356	8.89	9.04		
٧	0.042	0.048	1.07	1.21		
W	0.042	0.048	1.07	1.21		
Х	0.042	0.056	1.07	1.42		
Υ		0.020		0.50		
Ζ	2°	10°	2 °	10 °		
G1	0.310	0.330	7.88	8.38		
K1	0.040		1.02			

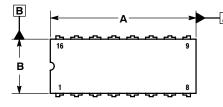
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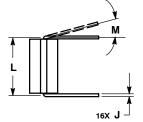
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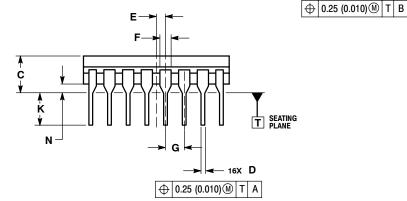
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PACKAGE DIMENSIONS









NOTES:

- TES:

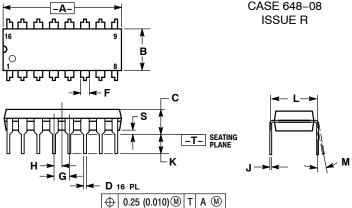
 DIMENSIONING AND TOLERANCING PER
 ASME Y14.5M, 1994.

 CONTROLLING DIMENSION: INCH.
 DIMENSION L TO CENTER OF LEAD WHEN
 FORMED PARALLEL.

 DIMENSION F MAY NARROW TO 0.76 (0.030)
 WHERE THE LEAD ENTERS THE CERAMIC
 BODY.
 THIS DRAWING REPLACES OBSOLETE
 CASE OUTLINE 620-10.
- CASE OUTLINE 620-10.

	INC	HES	MILLIMETERS		
DIM	MIN MAX		MIN	MAX	
Α	0.750 0.785		19.05	19.93	
В	0.240	0.295	6.10	7.49	
С	0.200			5.08	
D	0.015	0.020	0.39	0.50	
Е	0.050	BSC	1.27 BSC		
F	0.055	0.065	1.40	1.65	
G	0.100	BSC	2.54 BSC		
Н	0.008	0.015	0.21	0.38	
K	0.125	0.170	3.18	4.31	
L	0.300 BSC		7.62 BSC		
M	0°	15°	0°	15°	
N	0.020	0.040	0.51	1.01	

PDIP-16 **P SUFFIX** PLASTIC DIP PACKAGE CASE 648-08



- TES:
 DIMENSIONING AND TOLERANCING PER ANSI
 Y14.5M, 1982.
 CONTROLLING DIMENSION: INCH.
 DIMENSION L TO CENTER OF LEADS WHEN
 FORMED PARALLEL.
 DIMENSION B DOES NOT INCLUDE MOLD FLASH.
 ROUNDED CORNERS OPTIONAL.

	INC	HES	MILLIN	IETERS	
DIM	MIN	MAX	MIN	MAX	
Α	0.740	0.770	18.80	19.55	
В	0.250	0.270	6.35	6.85	
С	0.145	0.175	3.69	4.44	
D	0.015	0.021	0.39	0.53	
F	0.040	0.70	1.02	1.77	
G	0.100	BSC	2.54 BSC		
Н	0.050	BSC	1.27 BSC		
J	0.008	0.015	0.21	0.38	
K	0.110	0.130	2.80	3.30	
L	0.295	0.305	7.50	7.74	
M	0°	10°	0 °	10 °	
S	0.020	0.040	0.51	1.01	



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