

Excellent Integrated System Limited

Stocking Distributor

Click to view price, real time Inventory, Delivery & Lifecycle Information:

[Diodes Incorporated](#)
[DMP3030SN-7](#)

For any questions, you can email us directly:

sales@integrated-circuit.com



DMP3030SN

P-CHANNEL ENHANCEMENT MODE FIELD EFFECT TRANSISTOR

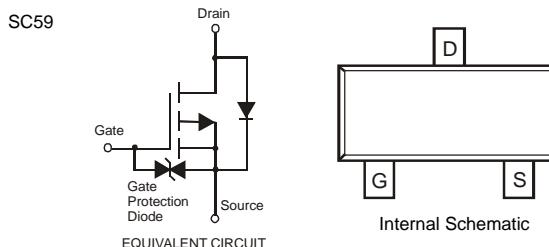
Features

- Low On-Resistance
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- **Lead Free By Design/RoHS Compliant (Note 2)**
- **ESD Protected Gate**
- "Green" Device (Note 4)
- Qualified to AEC-Q101 standards for High Reliability



Mechanical Data

- Case: SC59
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture sensitivity: Level 1 per J-STD-020C
- Terminals: Finish — Matte Tin annealed over Copper leadframe. Solderable per MIL-STD-202, Method 208
- Terminal Connections: See Diagram
- Marking Information: See Page 3
- Ordering & Date Code Information: See Page 3
- Weight: 0.014 grams (approximate)



Maximum Ratings

$\text{@T}_A = 25^\circ\text{C}$ unless otherwise specified

Characteristic	Symbol	Value	Unit
Drain-Source Voltage	V_{DSS}	-30	V
Gate-Source Voltage	V_{GSS}	± 20	V
Drain Current (Note 1) Steady State	I_D	-0.7	A
Pulsed Drain Current (Note 3)	I_{DM}	-2.8	A

Thermal Characteristics

$\text{@T}_A = 25^\circ\text{C}$ unless otherwise specified

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 1)	P_d	500	mW
Thermal Resistance, Junction to Ambient	$R_{\theta JA}$	250	$^\circ\text{C}/\text{W}$
Operating and Storage Temperature Range	T_j, T_{STG}	-65 to +150	$^\circ\text{C}$

Electrical Characteristics

$\text{@T}_A = 25^\circ\text{C}$ unless otherwise specified

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 5)						
Drain-Source Breakdown Voltage	BV_{DSS}	-30	—	—	V	$V_{GS} = 0\text{V}, I_D = -250\mu\text{A}$
Zero Gate Voltage Drain Current	I_{DSS}	—	—	-10	μA	$V_{DS} = -30\text{V}, V_{GS} = 0\text{V}$
Gate-Body Leakage	I_{GSS}	—	—	± 10	μA	$V_{GS} = \pm 20\text{V}, V_{DS} = 0\text{V}$
ON CHARACTERISTICS (Note 5)						
Gate Threshold Voltage	$V_{GS(th)}$	-1.0	—	-3.0	V	$V_{DS} = -10\text{V}, I_D = -1.0\text{mA}$
Static Drain-Source On-Resistance	$R_{DS(\text{ON})}$	—	0.20 0.35	0.25 0.45	Ω	$V_{GS} = -10\text{V}, I_D = -0.4\text{A}$ $V_{GS} = -4.5\text{V}, I_D = -0.4\text{A}$
Forward Transfer Admittance	$ Y_{fs} $	—	1	—	S	$V_{DS} = -10\text{V}, I_D = -0.4\text{A}$
Diode Forward Voltage (Note 5)	V_{SD}	—	-0.8	-1.1	V	$V_{GS} = 0\text{V}, I_S = -0.7\text{A}$
DYNAMIC CHARACTERISTICS						
Input Capacitance	C_{iss}	—	160	—	pF	$V_{DS} = -10\text{V}, V_{GS} = 0\text{V}$ $f = 1.0\text{MHz}$
Output Capacitance	C_{oss}	—	120	—	pF	
Reverse Transfer Capacitance	C_{rss}	—	50	—	pF	
SWITCHING CHARACTERISTICS						
Turn-On Delay Time	$t_{D(\text{ON})}$	—	10	—	ns	$V_{DD} = -10\text{V}, I_D = -0.4\text{A},$ $V_{GS} = -5.0\text{V}, R_{\text{GEN}} = 50\Omega$
Turn-Off Delay Time	$t_{D(\text{OFF})}$	—	25	—	ns	
Turn-On Rise Time	t_r	—	25	—	ns	
Turn-Off Fall Time	t_f	—	40	—	ns	

Notes:

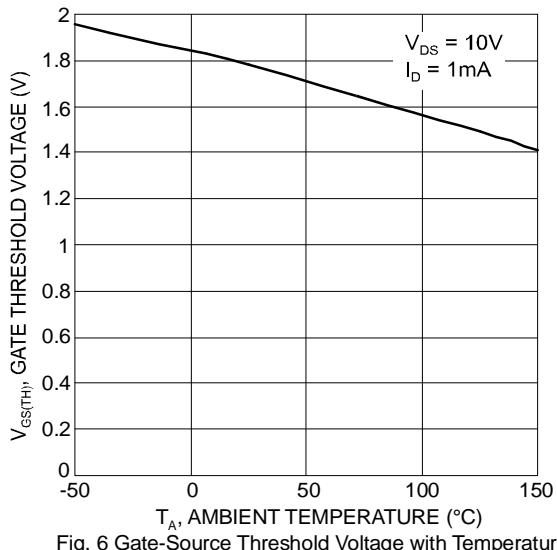
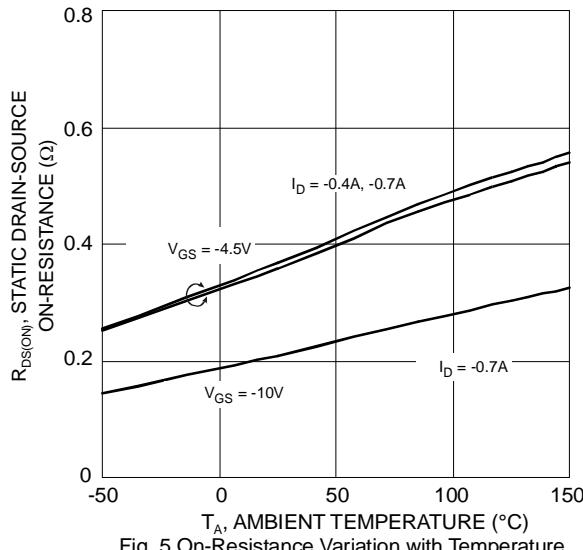
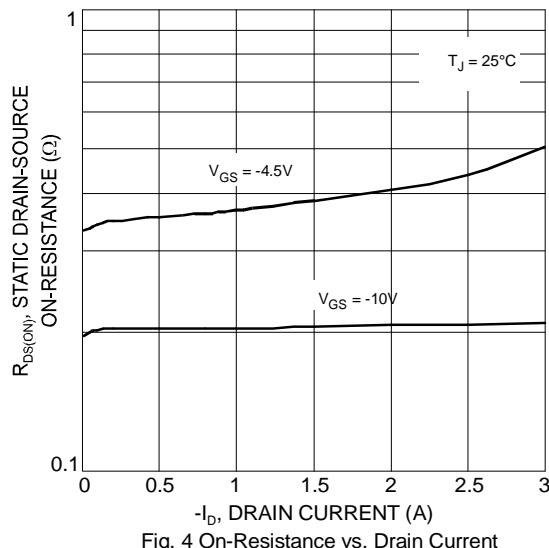
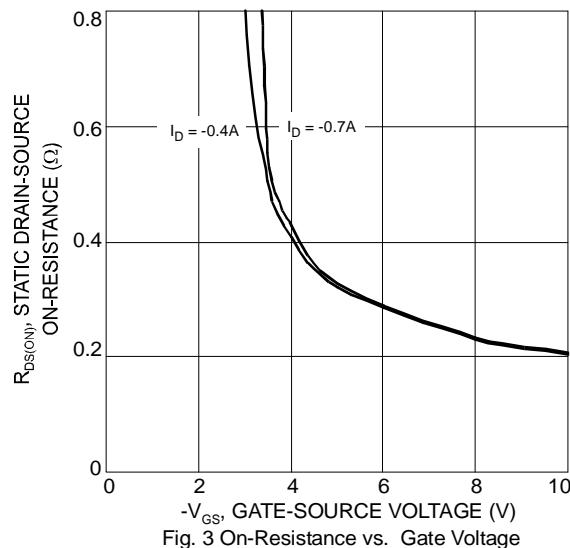
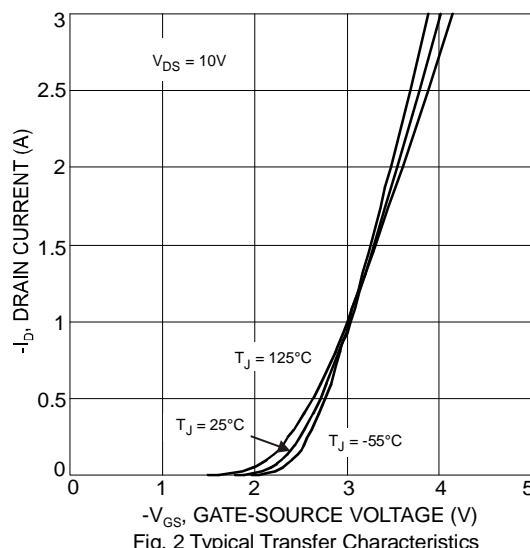
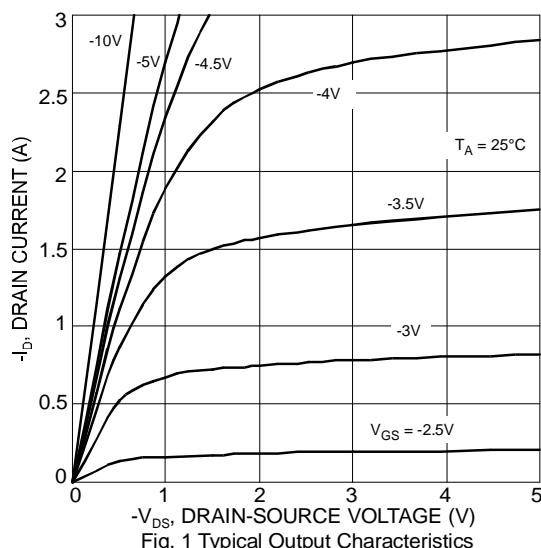
1. Device mounted on FR-4 PCB.
2. No purposefully added lead.
3. Pulse width $\leq 10\mu\text{S}$, Duty Cycle $\leq 1\%$.
4. Diodes Inc.'s "Green" policy can be found on our website at http://www.diodes.com/products/lead_free/index.php.
5. Short duration pulse test used to minimize self-heating effect.

DMP3030SN

Document number: DS30787 Rev. 5 - 2

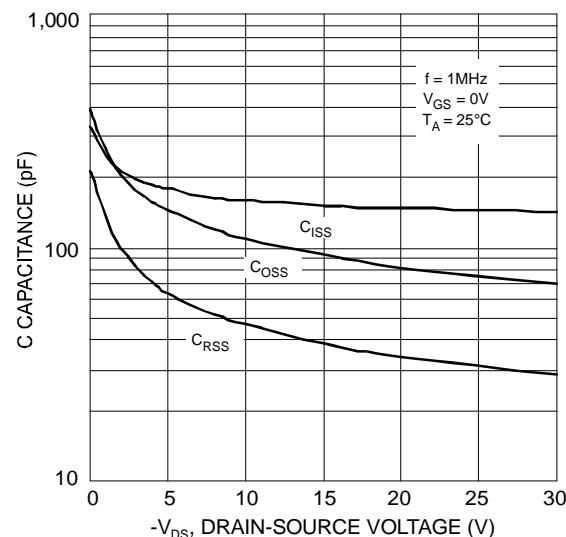
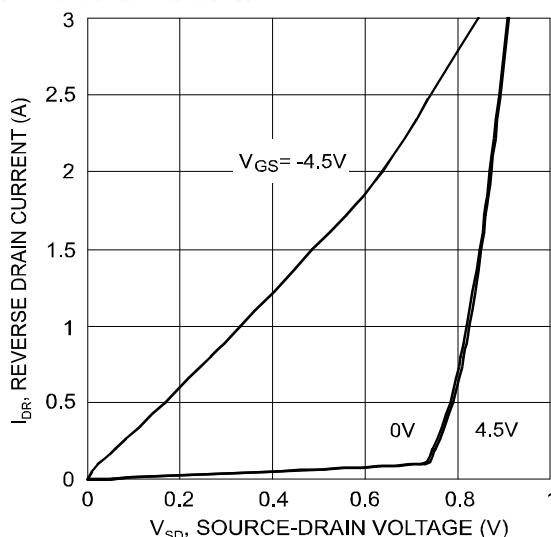


DMP3030SN





DMP3030SN

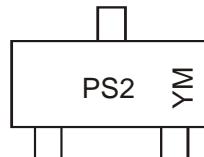


Ordering Information (Note 6)

Part Number	Case	Packaging
DMP3030SN-7	SC59	3000/Tape & Reel

Notes: 6. For packaging details, go to our website at <http://www.diodes.com/datasheets/ap02007.pdf>.

Marking Information

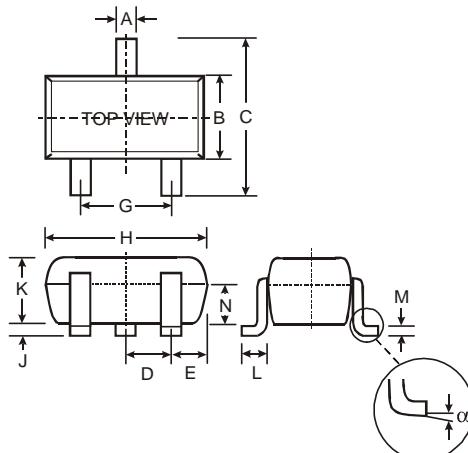


PS2 = Product Type Marking Code
YM = Date Code Marking
Y = Year ex: T = 2006
M = Month ex: 9 = September

Date Code Key

Year	2006	2007	2008	2009	2010	2011	2012					
Code	T	U	V	W	X	Y	Z					
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

Package Outline Dimensions



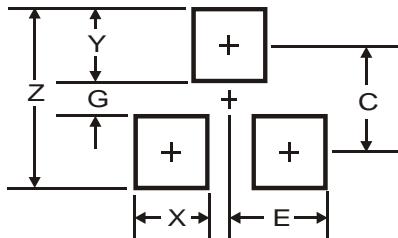
SC59		
Dim	Min	Max
A	0.35	0.50
B	1.50	1.70
C	2.70	3.00
D	0.95	
E	—	—
G	1.90	
H	2.90	3.10
J	0.013	0.10
K	1.00	1.30
L	0.35	0.55
M	0.10	0.20
N	0.70	0.80
α	0°	8°

All Dimensions in mm



DMP3030SN

Suggested Pad Layout



Dimensions	Value (in mm)
Z	4.0
G	1.2
X	0.9
Y	1.4
C	2.6
E	0.95

IMPORTANT NOTICE

DIODES INCORPORATED MAKES NO WARRANTY OF ANY KIND, EXPRESS OR IMPLIED, WITH REGARDS TO THIS DOCUMENT, INCLUDING, BUT NOT LIMITED TO, THE IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A PARTICULAR PURPOSE (AND THEIR EQUIVALENTS UNDER THE LAWS OF ANY JURISDICTION).

Diodes Incorporated and its subsidiaries reserve the right to make modifications, enhancements, improvements, corrections or other changes without further notice to this document and any product described herein. Diodes Incorporated does not assume any liability arising out of the application or use of this document or any product described herein; neither does Diodes Incorporated convey any license under its patent or trademark rights, nor the rights of others. Any Customer or user of this document or products described herein in such applications shall assume all risks of such use and will agree to hold Diodes Incorporated and all the companies whose products are represented on Diodes Incorporated website, harmless against all damages.

Diodes Incorporated does not warrant or accept any liability whatsoever in respect of any products purchased through unauthorized sales channel. Should Customers purchase or use Diodes Incorporated products for any unintended or unauthorized application, Customers shall indemnify and hold Diodes Incorporated and its representatives harmless against all claims, damages, expenses, and attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized application.

Products described herein may be covered by one or more United States, international or foreign patents pending. Product names and markings noted herein may also be covered by one or more United States, international or foreign trademarks.

LIFE SUPPORT

Diodes Incorporated products are specifically not authorized for use as critical components in life support devices or systems without the express written approval of the Chief Executive Officer of Diodes Incorporated. As used herein:

A. Life support devices or systems are devices or systems which:

1. are intended to implant into the body, or
2. support or sustain life and whose failure to perform when properly used in accordance with instructions for use provided in the labeling can be reasonably expected to result in significant injury to the user.

B. A critical component is any component in a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or to affect its safety or effectiveness.

Customers represent that they have all necessary expertise in the safety and regulatory ramifications of their life support devices or systems, and acknowledge and agree that they are solely responsible for all legal, regulatory and safety-related requirements concerning their products and any use of Diodes Incorporated products in such safety-critical, life support devices or systems, notwithstanding any devices- or systems-related information or support that may be provided by Diodes Incorporated. Further, Customers must fully indemnify Diodes Incorporated and its representatives against any damages arising out of the use of Diodes Incorporated products in such safety-critical, life support devices or systems.

Copyright © 2011, Diodes Incorporated

www.diodes.com