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Stocking Distributor

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Vishay/Siliconix SI1410EDH-T1-E3

For any questions, you can email us directly: <u>sales@integrated-circuit.com</u>





Si1410EDH

Vishay Siliconix

RoHS

COMPLIANT HALOGEN

FREE

Availabl

N-Channel 20 V (D-S) MOSFET

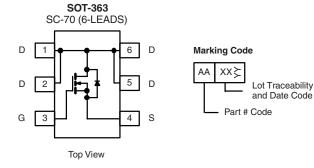
PRODUCT SUMMARY					
V _{DS} (V)	R_{DS(on)} (Ω)	I _D (A)			
20	0.070 at V _{GS} = 4.5 V	3.7			
	0.080 at V _{GS} = 2.5 V	3.4			
	0.100 at V _{GS} = 1.8 V	3.0			

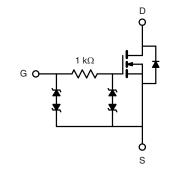
FEATURES

- Halogen-free According to IEC 61249-2-21
 Definition
- TrenchFET[®] Power MOSFETs: 1.8 V Rated
- ESD Protected: 2000 V
- Thermally Enhanced SC-70 Package
- Compliant to RoHS Directive 2002/95/EC

APPLICATIONS

Load Switching





Ordering Information: Si1410EDH-T1-E3 (Lead (Pb)-free) Si1410EDH-T1-GE3 (Lead (Pb)-free and Halogen-free)

ABSOLUTE MAXIMUM RATINGS	A = 25 °C, unle	ss otherwise r	noted			
Parameter		Symbol	5 s	Steady State	Unit	
Drain-Source Voltage		V _{DS}	20		V	
Gate-Source Voltage		V _{GS}	± 12			
Continuous Drain Current (T _{.1} = 150 °C) ^a	T _A = 25 °C	- I _D	3.7	2.9	А	
Continuous Drain Current (1) = 150°C)	T _A = 85 °C		2.6	2.0		
Pulsed Drain Current		I _{DM}	8		A	
Continuous Diode Current (Diode Conduction) ^a		۱ _S	1.4	0.9		
Maximum Power Dissipation ^a	T _A = 25 °C	- P _D	1.56	1.0	w	
	T _A = 85 °C		0.81	0.52		
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150		°C	

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient ^a	$t \le 5 s$	- R _{thJA}	60	80	
Maximum Junction-to-Ambient*	Steady State		100	125	°C/W
Maximum Junction-to-Foot (Drain)	Steady State	R _{thJF}	34	45	

Notes:

a. Surface mounted on 1" x 1" FR4 board.



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SPECIFICATIONS $T_J = 25 \text{ °C}$, unless otherwise noted								
Parameter	Symbol	Test Conditions Min.		Тур.	Max.	Unit		
Static								
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_D = 250 \ \mu A$	0.45			V		
Gate-Body Leakage		V_{DS} = 0 V, V_{GS} = ± 4.5 V			± 1	μA		
	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 12 V$			± 10	mA		
Zero Gate Voltage Drain Current		$V_{DS} = 16 \text{ V}, V_{GS} = 0 \text{ V}$			1	- μΑ		
	IDSS	V_{DS} = 16 V, V_{GS} = 0 V, T_{J} = 85 °C			5			
On-State Drain Current ^a	I _{D(on)}	$V_{DS} = 5 V, V_{GS} = 4.5 V$	4			А		
Drain-Source On-State Resistance ^a	R _{DS(on)}	$V_{GS} = 4.5 \text{ V}, \text{ I}_{D} = 3.7 \text{ A}$		0.055	0.070	Ω		
		$V_{GS} = 2.5 \text{ V}, \text{ I}_{D} = 3.4 \text{ A}$		0.065	0.080			
		$V_{GS} = 1.8 \text{ V}, \text{ I}_{D} = 1.7 \text{ A}$		0.080	0.100			
Forward Transconductance ^a	9 _{fs}	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 3.7 \text{ A}$		10		S		
Diode Forward Voltage ^a	V _{SD}	$I_{S} = 1.4 \text{ A}, V_{GS} = 0 \text{ V}$		0.75	1.1	V		
Dynamic ^b								
Total Gate Charge	Qg			5.6	8			
Gate-Source Charge	Q _{gs}	V_{DS} = 10 V, V_{GS} = 4.5 V, I_D = 3.7 A		0.75		nC		
Gate-Drain Charge	Q _{gd}			1.10				
Turn-On Delay Time	t _{d(on)}	$V_{DD} = 10 \text{ V}, \text{ R}_{\text{L}} = 10 \Omega$ $\text{I}_{\text{D}} \cong 1 \text{ A}, \text{ V}_{\text{GEN}} = 4.5 \text{ V}, \text{ R}_{\text{g}} = 6 \Omega$		0.15	0.25			
Rise Time	t _r			0.4	0.6			
Turn-Off Delay Time	t _{d(off)}			1.9	2.8	μs		
Fall Time	t _f			1.2	1.8			

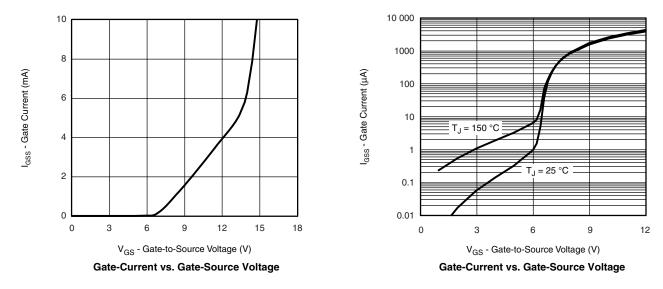
Notes:

a. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %.

b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted







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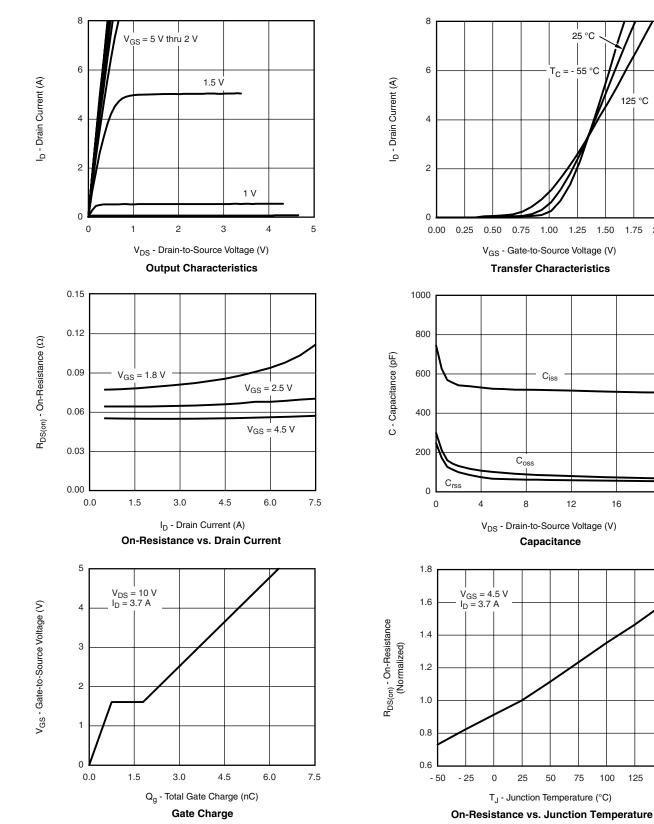
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125 °C

1.75 2.00

16

20



TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

Document Number: 71409 S10-0935-Rev. B, 19-Apr-10 125

150

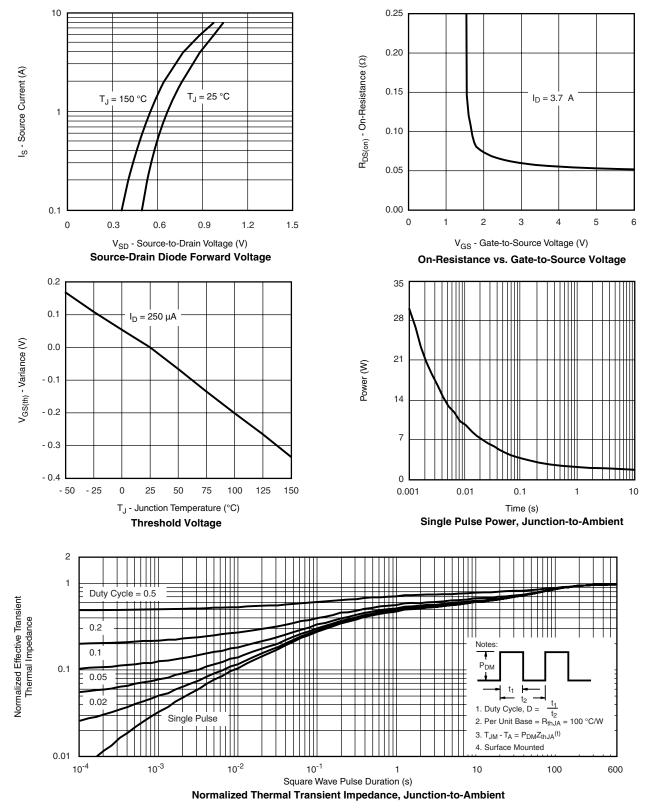


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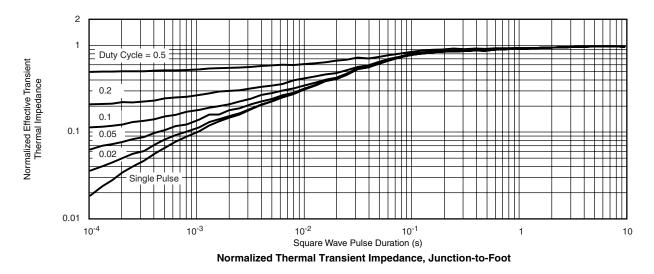




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