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[SI3454ADV-T1-E3](#)

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N-Channel 30-V (D-S) MOSFET

PRODUCT SUMMARY

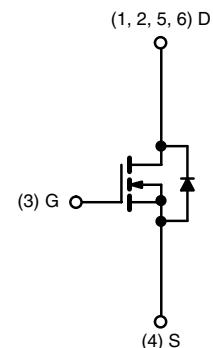
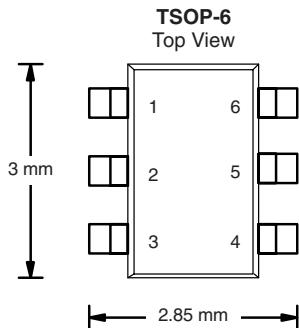
V_{DS} (V)	$R_{DS(on)}$ (Ω)	I_D (A)
30	0.060 at $V_{GS} = 10$ V	4.5
	0.085 at $V_{GS} = 4.5$ V	3.8

FEATURES

- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET® Power MOSFET
- 100 % R_g Tested
- Compliant to RoHS Directive 2002/95/EC



RoHS
COMPLIANT
HALOGEN
FREE
Available



Ordering Information: Si3454ADV-T1-E3 (Lead (Pb)-free)
Si3454ADV-T1-GE3 (Lead (Pb)-free and Halogen-free)

Marking Code: A4xxx

N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS $T_A = 25$ °C, unless otherwise noted

Parameter	Symbol	5 s	Steady State	Unit	
Drain-Source Voltage	V_{DS}	30	± 20	V	
Gate-Source Voltage	V_{GS}				
Continuous Drain Current ($T_J = 150$ °C) ^a	I_D	4.5	3.4	A	
		3.6	2.7		
Pulsed Drain Current (10 μ s Pulse Width)	I_{DM}	20			
Continuous Source Current (Diode Conduction) ^a	I_S	1.7	1.0		
Maximum Power Dissipation ^a	P_D	2.0	1.14	W	
		1.3	0.73		
Operating Junction and Storage Temperature Range	T_J, T_{stg}	- 55 to 150		°C	

THERMAL RESISTANCE RATINGS

Parameter	Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient ^a	R_{thJA}	50	62.5	°C/W
		90	110	
Maximum Junction-to-Foot (Drain)	R_{thJF}	30	36	

Notes:

a. Surface Mounted on 1" x 1" FR4 board.

Si3454ADV

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SPECIFICATIONS $T_J = 25^\circ\text{C}$, unless otherwise noted

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Static						
Gate Threshold Voltage	$V_{GS(\text{th})}$	$V_{DS} = V_{GS}$, $I_D = 250 \mu\text{A}$	1.0		3.0	V
Gate-Body Leakage	I_{GSS}	$V_{DS} = 0 \text{ V}$, $V_{GS} = \pm 20 \text{ V}$			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 30 \text{ V}$, $V_{GS} = 0 \text{ V}$		1		μA
		$V_{DS} = 30 \text{ V}$, $V_{GS} = 0 \text{ V}$, $T_J = 70^\circ\text{C}$			25	
On-State Drain Current ^a	$I_{D(\text{on})}$	$V_{DS} \geq 5 \text{ V}$, $V_{GS} = 10 \text{ V}$	15			A
Drain-Source On-State Resistance ^a	$R_{DS(\text{on})}$	$V_{GS} = 10 \text{ V}$, $I_D = 4.5 \text{ A}$		0.048	0.060	Ω
		$V_{GS} = 4.5 \text{ V}$, $I_D = 3.8 \text{ A}$		0.070	0.085	
Forward Transconductance ^a	g_{fs}	$V_{DS} = 10 \text{ V}$, $I_D = 4.5 \text{ A}$		10		S
Diode Forward Voltage ^a	V_{SD}	$I_S = 1.7 \text{ A}$, $V_{GS} = 0 \text{ V}$		0.8	1.2	V
Dynamic^b						
Total Gate Charge	Q_g	$V_{DS} = 15 \text{ V}$, $V_{GS} = 10 \text{ V}$, $I_D = 4.5 \text{ A}$		9	15	nC
Gate-Source Charge	Q_{gs}			2.5		
Gate-Drain Charge	Q_{gd}			1.5		
Gate Resistance	R_g		0.5		2.9	Ω
Turn-On Delay Time	$t_{d(\text{on})}$	$V_{DD} = 15 \text{ V}$, $R_L = 15 \Omega$ $I_D \equiv 1 \text{ A}$, $V_{GEN} = 10 \text{ V}$, $R_g = 6 \Omega$		10	20	ns
Rise Time	t_r			10	20	
Turn-Off Delay Time	$t_{d(\text{off})}$			20	35	
Fall Time	t_f			7	15	
Source-Drain Reverse Recovery Time	t_{rr}	$I_F = 1.7 \text{ A}$, $dI/dt = 100 \text{ A}/\mu\text{s}$		40	80	

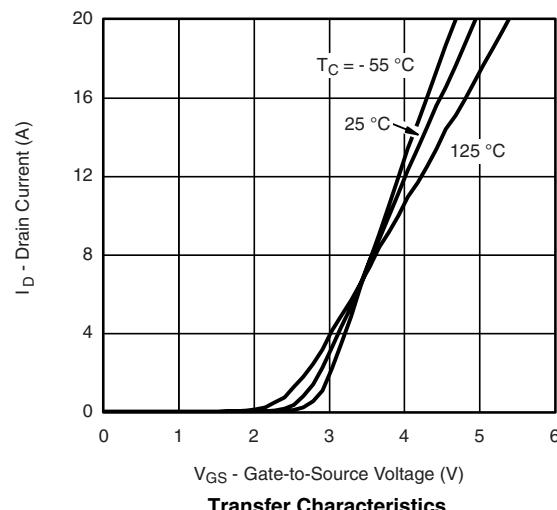
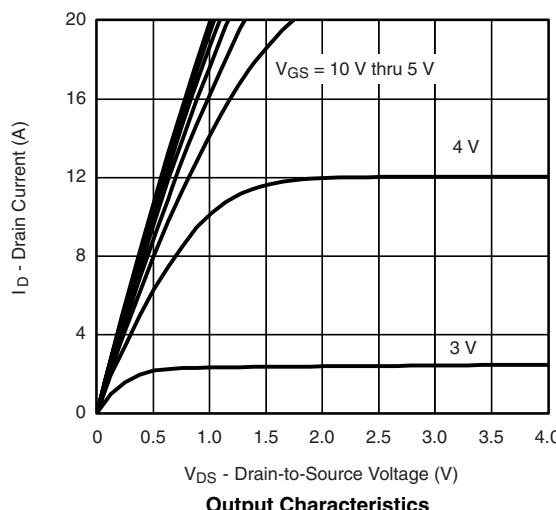
Notes:

a. Pulse test; pulse width $\leq 300 \mu\text{s}$, duty cycle $\leq 2\%$.

b. Guaranteed by design, not subject to production testing.

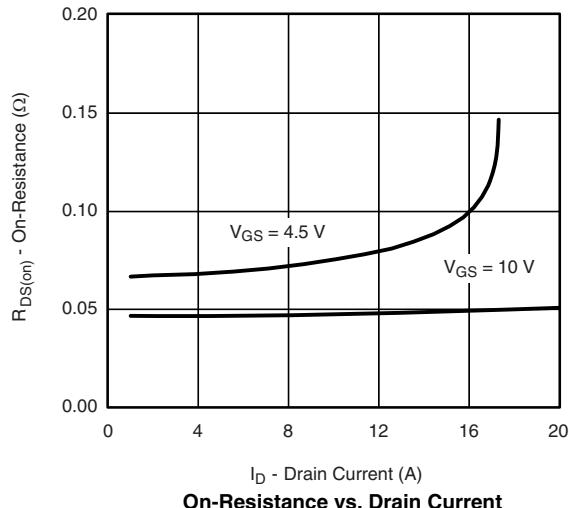
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS 25°C , unless otherwise noted

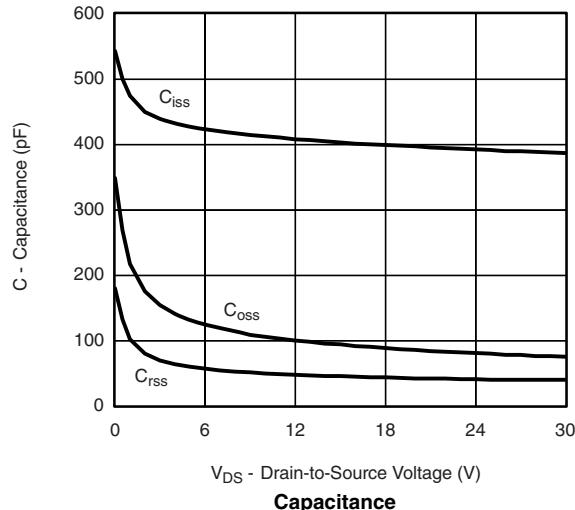




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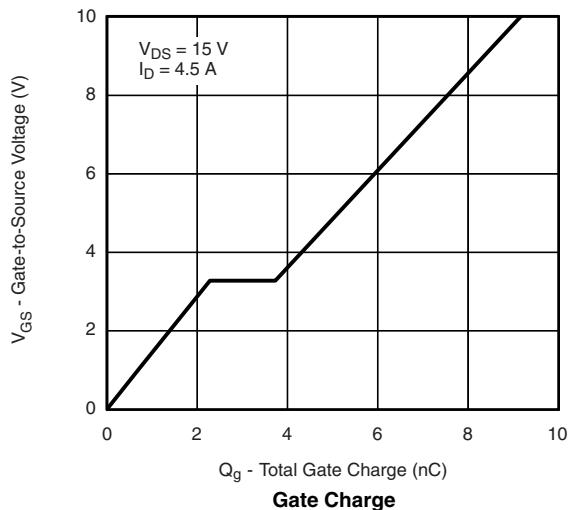


On-Resistance vs. Drain Current

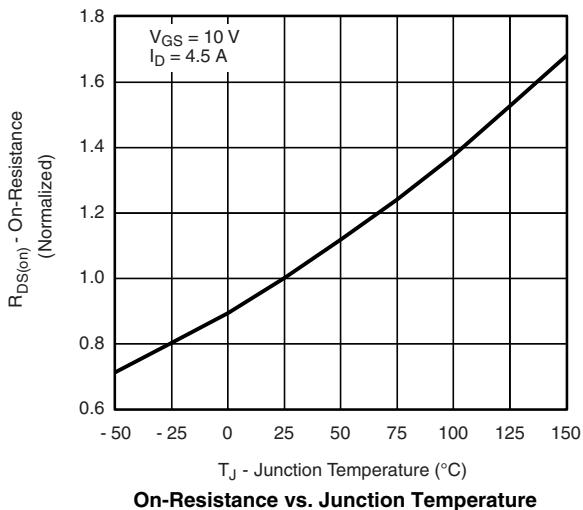


V_{DS} - Drain-to-Source Voltage (V)

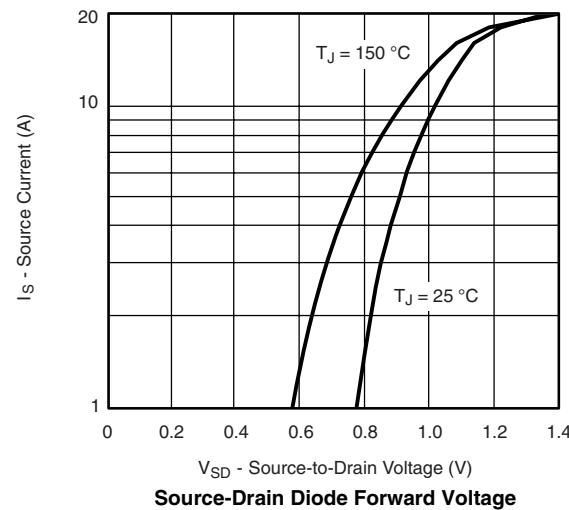
Capacitance



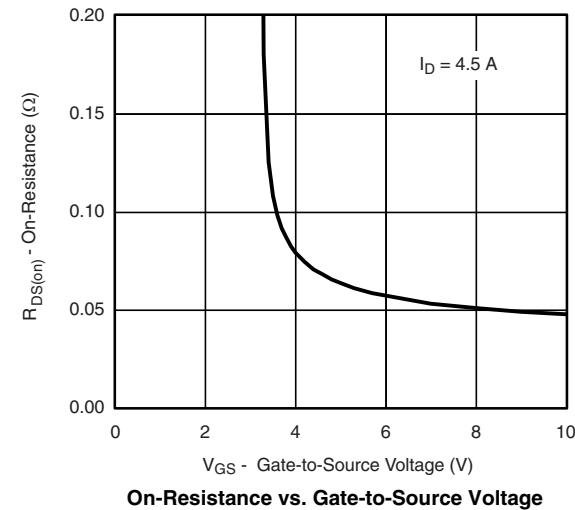
Gate Charge



On-Resistance vs. Junction Temperature



Source-Drain Diode Forward Voltage



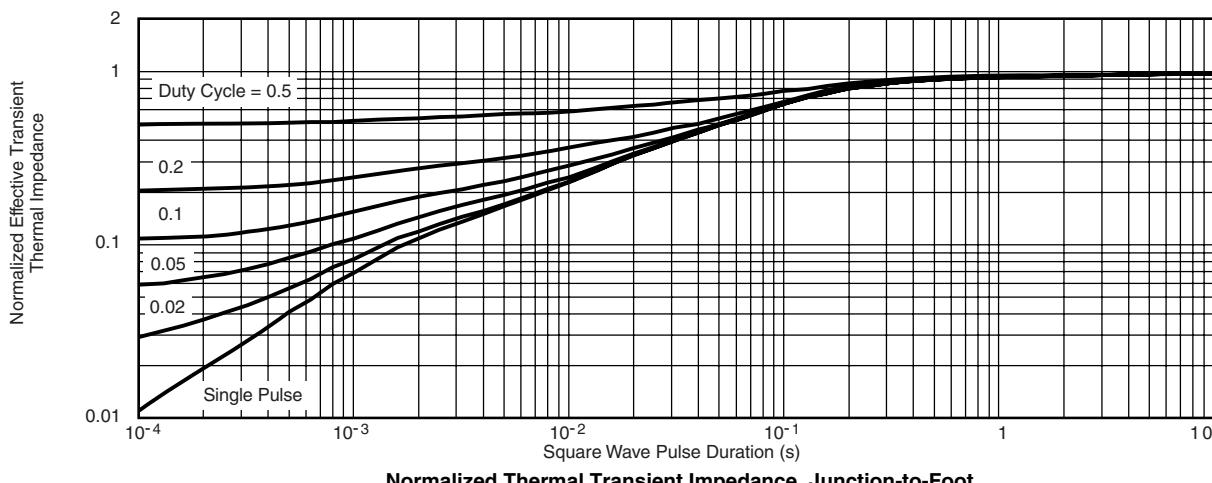
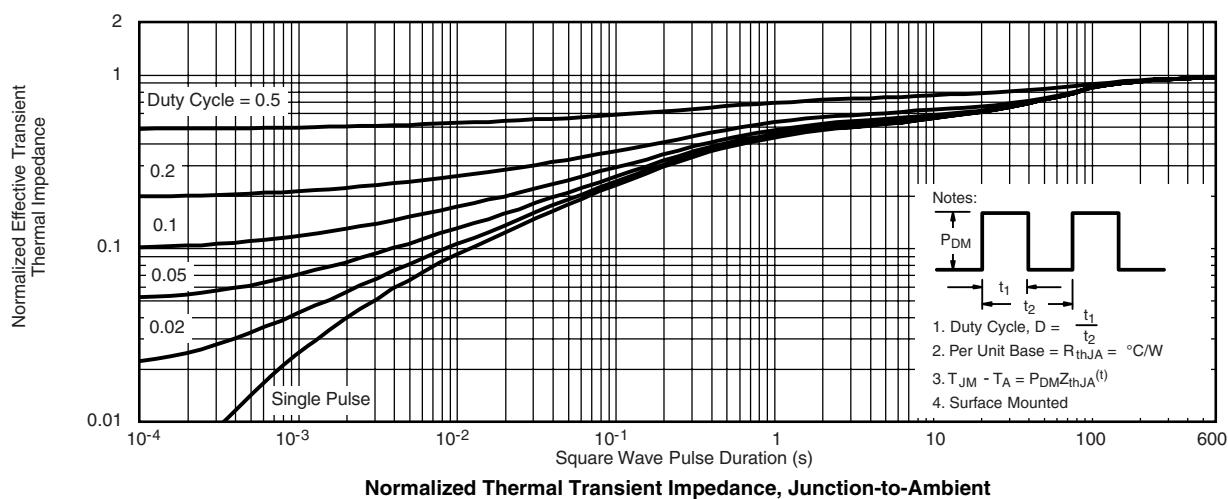
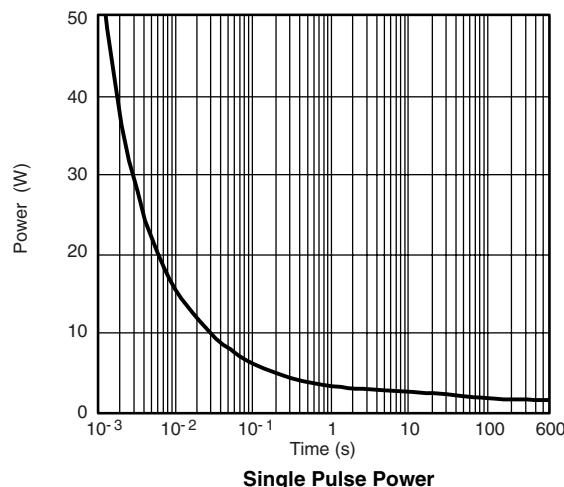
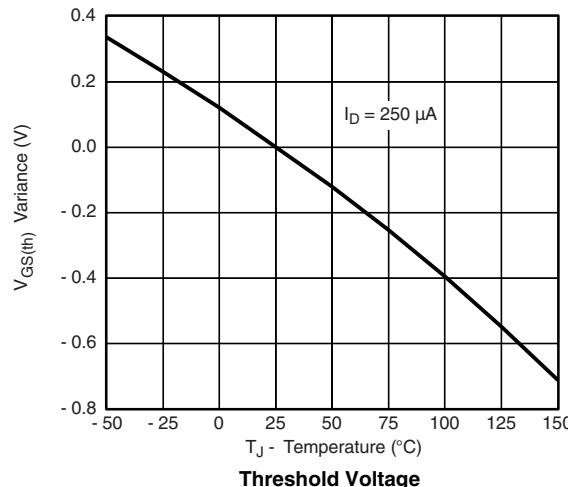
On-Resistance vs. Gate-to-Source Voltage

Si3454ADV

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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



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