

Excellent Integrated System Limited

Stocking Distributor

Click to view price, real time Inventory, Delivery & Lifecycle Information:

<u>Vishay Semiconductor/Diodes Division</u> <u>VS-1N3209</u>

For any questions, you can email us directly: sales@integrated-circuit.com

Datasheet of VS-1N3209 - DIODE GEN PURP 100V 15A DO203AB

Contact us: sales@integrated-circuit.com Website: www.integrated-circuit.com



www.vishay.com

VS-1N3208 Series

COMPLIANT

Vishay Semiconductors

Silicon Rectifier Diodes, (Stud Version) 15 A

(Stud Version) 13



DO-203AB (DO-5)

|--|

- · Low thermal impedance
- High case temperature
- Excellent reliability
- · Maximum design flexibility
- Material categorization: for definitions of compliance please see www.vishav.com/doc?99912

PRODUCT SUMMARY		
I _{F(AV)}	15 A	
Package	DO-203AB (DO-5)	
Circuit configuration	Single diode	

MAJOR RATINGS AND CHARACTERISTICS				
PARAMETER	TEST CONDITIONS	VALUES	UNITS	
1		15 ⁽¹⁾	А	
I _{F(AV)}	T _C	150 ⁽¹⁾	°C	
I _{FSM}	50 Hz	239	^	
	60 Hz	250 ⁽¹⁾	А	
I ² t	50 Hz	286	A ² s	
1-1	60 Hz	260	A-S	
I ² √t		3870	A²√s	
V _{RRM}	Range	50 to 600	V	
TJ		-65 to +175	°C	

Note

ELECTRICAL SPECIFICATIONS

VOLTAGE RATINGS				
TYPE NUMBER	V_{RRM} , MAXIMUM REPETITIVE PEAK REVERSE VOLTAGE (T_J = -65 °C TO 175 °C)	V_{RM} , MAXIMUM DIRECT REVERSE VOLTAGE (T _J = -65 °C TO 175 °C) V		
VS-1N3208	50 ⁽¹⁾	50 (1)		
VS-1N3209	100 (1)	100 (1)		
VS-1N3210	200 (1)	200 (1)		
VS-1N3211	300 (1)	300 (1)		
VS-1N3212	400 (1)	400 (1)		
VS-1N3213	500 ⁽¹⁾	500 ⁽¹⁾		
VS-1N3214	600 ⁽¹⁾	600 ⁽¹⁾		

Notes

- (1) JEDEC registered values
- Basic type number indicates cathode to case. For anode to case, add "R" to part number, e.g. 1N3208R, 1N3209R

Revision: 12-Nov-15 1 Document Number: 93496

⁽¹⁾ JEDEC® registered values

Datasheet of VS-1N3209 - DIODE GEN PURP 100V 15A DO203AB Contact us: sales@integrated-circuit.com Website: www.integrated-circuit.com



VS-1N3208 Series

Vishay Semiconductors

FORWARD CONDUCTION					
PARAMETER	SYMBOL	TEST CO	NDITIONS	VALUES	UNITS
Maximum average forward current	I _{F(AV)}	180° sinusoidal conduction		15 ⁽¹⁾	A
at case temperature				150 ⁽¹⁾	°C
		Half cycle 50 Hz sine wave or 6 ms rectangular pulse	Following any rated load	239	А
Maximum peak one cycle	I _{FSM}	Half cycle 60 Hz sine wave or 5 ms rectangular pulse	condition and with rated V _{RRM} applied	250 ⁽¹⁾	
non-repetitive surge current		Half cycle 50 Hz sine wave or 6 ms rectangular pulse	Following any rated load	284	
		Half cycle 60 Hz sine wave or 5 ms rectangular pulse	condition and with V _{RRM} applied following surge = 0	297	
Maximum I ² t for fusing		t = 10 ms	With rated V _{RRM} applied following surge,	286	
Maximum F Clor lusing	l ² t	t = 8.3 ms	initial T _J = 150 °C	260	A ² s
Maximum I ² t for individual		t = 10 ms	With V _{RRM} = 0 following	403	Α 5
device fusing		t = 8.3 ms	surge, initial T _J = 150 °C	368	
Maximum I²√t for individual device fusing	I ² √t ⁽²⁾	t = 0.1 ms to 10 ms, V _{RRM} = 0 following surge		3870	A²√s
Maximum forward voltage drop	V_{FM}	I _{F(AV)} = 15 A (47.1 A peak), T _C = 150 °C		1.5 ⁽¹⁾	V
Maximum average reverse current	I _{R(AV)}	Maximum rated $I_{F(AV)}$ and $T_C = 150 ^{\circ}C$ 10 $^{(1)}$ n		mA	

Notes

(1) JEDEC registered values

(2) I²t for time $t_x = I^2 \sqrt{t} \times \sqrt{t_x}$

THERMAL AND MECHANI	CAL SPE	CIFICATIONS			
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction operating and storage temperature range	T _J , T _{Stg}		-65 to 175 ⁽¹⁾	°C	
Maximum internal thermal resistance, junction to case	R _{thJC}	DC operation	0.65	°C/W	
Thermal resistance, case to sink	R _{thCS}	Mounting surface, smooth, flat and greased	0.25		
		Not lubricated thread, tighting on nut (2)	3.4	(30)	
Maximum allowable mounting torque	Lubricated thread, tighting on nut (2)		2.3	(20)	
(+0 %, -10 %)		Not lubricated thread, tighting on hexagon (3)	4.2	(37)	
		Lubricated thread, tighting on hexagon (3)	3.2	(28)	
NA/-:I-4			28.5	g	
Weight			1	OZ.	
Case style		JEDEC	DO-203A	B (DO-5)	

Notes

(1) JEDEC registered values

(2) Recommended for pass-through holes

(3) Recommended for holed threaded heatsinks

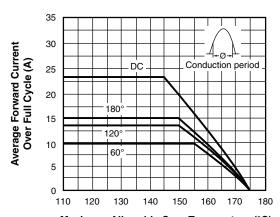
Datasheet of VS-1N3209 - DIODE GEN PURP 100V 15A DO203AB

Contact us: sales@integrated-circuit.com Website: www.integrated-circuit.com

VS-1N3208 Series

www.vishay.com

Vishay Semiconductors



Maximum Allowable Case Temperature (°C)

Fig. 1 - Average Forward Current vs. Maximum Allowable Case Temperature

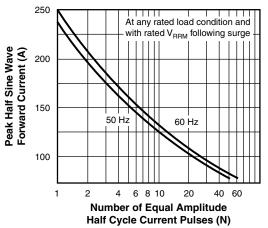


Fig. 2 - Maximum Non-Repetitive Surge Current vs. Number of Current Pulses

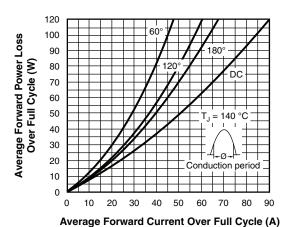
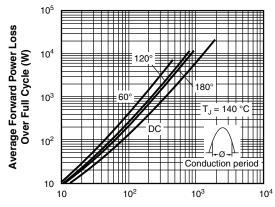


Fig. 3 - Maximum Low Level Forward Power Loss vs. Average Forward Current



Average Forward Current Over Full Cycle (A)

Fig. 4 - Maximum High Level Forward Power Loss vs. Average Forward Current

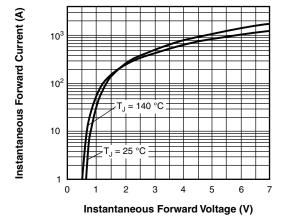


Fig. 5 - Maximum Forward Voltage vs. Forward Current

LINKS TO RELATED DOCUMENTS			
Dimensions	www.vishay.com/doc?95360		

Revision: 12-Nov-15 3 Document Number: 93496

Datasheet of VS-1N3209 - DIODE GEN PURP 100V 15A DO203AB

Contact us: sales@integrated-circuit.com Website: www.integrated-circuit.com

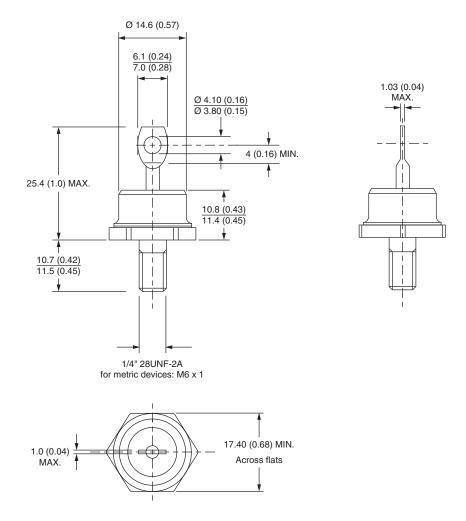


Outline Dimensions

Vishay Semiconductors

DO-203AB (DO-5) for 1N1183, 1N3765, 1N1183A, 1N2128A, 1N3208 Series

DIMENSIONS in millimeters (inches)





Distributor of Vishay Semiconductor/Diodes Division: Excellent Integrated System Limite Datasheet of VS-1N3209 - DIODE GEN PURP 100V 15A DO203AB

Contact us: sales@integrated-circuit.com Website: www.integrated-circuit.com



Vishav

Disclaimer

ALL PRODUCT, PRODUCT SPECIFICATIONS AND DATA ARE SUBJECT TO CHANGE WITHOUT NOTICE TO IMPROVE RELIABILITY, FUNCTION OR DESIGN OR OTHERWISE.

Vishay Intertechnology, Inc., its affiliates, agents, and employees, and all persons acting on its or their behalf (collectively, "Vishay"), disclaim any and all liability for any errors, inaccuracies or incompleteness contained in any datasheet or in any other disclosure relating to any product.

Vishay makes no warranty, representation or guarantee regarding the suitability of the products for any particular purpose or the continuing production of any product. To the maximum extent permitted by applicable law, Vishay disclaims (i) any and all liability arising out of the application or use of any product, (ii) any and all liability, including without limitation special, consequential or incidental damages, and (iii) any and all implied warranties, including warranties of fitness for particular purpose, non-infringement and merchantability.

Statements regarding the suitability of products for certain types of applications are based on Vishay's knowledge of typical requirements that are often placed on Vishay products in generic applications. Such statements are not binding statements about the suitability of products for a particular application. It is the customer's responsibility to validate that a particular product with the properties described in the product specification is suitable for use in a particular application. Parameters provided in datasheets and / or specifications may vary in different applications and performance may vary over time. All operating parameters, including typical parameters, must be validated for each customer application by the customer's technical experts. Product specifications do not expand or otherwise modify Vishay's terms and conditions of purchase, including but not limited to the warranty expressed therein.

Except as expressly indicated in writing, Vishay products are not designed for use in medical, life-saving, or life-sustaining applications or for any other application in which the failure of the Vishay product could result in personal injury or death. Customers using or selling Vishay products not expressly indicated for use in such applications do so at their own risk. Please contact authorized Vishay personnel to obtain written terms and conditions regarding products designed for such applications.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document or by any conduct of Vishay. Product names and markings noted herein may be trademarks of their respective owners.

Revision: 13-Jun-16 Document Number: 91000 1