

Excellent Integrated System Limited

Stocking Distributor

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<u>Vishay Semiconductor/Diodes Division</u> <u>VEMI35AA-HA3-GS08</u>

For any questions, you can email us directly: sales@integrated-circuit.com

Distributor of Vishay Semiconductor/Diodes Division: Excellent Integrated System Limite

Datasheet of VEMI35AA-HA3-GS08 - IC EMI FILTER 3CH W/ESD LLP75

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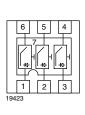


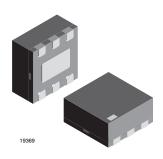
VEMI35AA-HA3

<u>GREEN</u> (5-2008)**

Vishay Semiconductors

3-Channel EMI-Filter with ESD-Protection



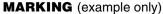


FEATURES

- Ultra compact LLP75-7A package
- 3-channel EMI-filter and ESD-protection
- Low leakage current
- Line resistance $R_S = 100 \Omega$
- Typical cut off frequency f_{3dB} = 100 MHz
- ESD-protection acc. IEC 61000-4-2
 ± 30 kV contact discharge
 ± 30 kV air discharge



 Compliant to RoHS directive 2002/95/EC and in accordance to WEEE 2002/96/EC





Dot = pin 1 marking

YY = type code (see table below)

XX = date code

ORDERING INFORMATION					
DEVICE NAME	ORDERING CODE	TAPED UNITS PER REEL (8 mm TAPE ON 7" REEL)	MINIMUM ORDER QUANTITY		
VEMI35AA-HA3	VEMI35AA-HA3-GS08	3000	15 000		

PACKAGE DATA						
DEVICE NAME	PACKAGE NAME	TYPE CODE	WEIGHT	MOLDING COMPOUND FLAMMABILITY RATING	MOISTURE SENSITIVITY LEVEL	SOLDERING CONDITIONS
VEMI35AA-HA3	LLP75-7A	9C	5 mg	UL 94 V-0	MSL level 1 (according J-STD-020)	260 °C/10 s at terminals

ABSOLUTE MAXIMUM RATINGS						
PARAMETER	TEST CONDITIONS	SYMBOL	VALUE	UNIT		
Peak pulse current	All I/O pin to pin 7; acc. IEC 61000-4-5; $t_p = 8/20 \mu s$; single shot	I _{PPM}	4	А		
ESD immunity	Contact discharge acc. IEC61000-4-2; 10 pulses	\/	± 30	kV		
	Air discharge acc. IEC61000-4-2; 10 pulses	V_{ESD}	± 30	KV		
Operating temperature	Junction temperature	T _J	- 40 to + 125	°C		
Storage temperature		T _{STG}	- 55 to + 150	°C		

Document Number: 81715 Rev. 1.3, 14-Sep-09 For technical questions, contact: EMIFilter@vishay.com

^{**} Please see document "Vishay Material Category Policy": www.vishay.com/doc?99902

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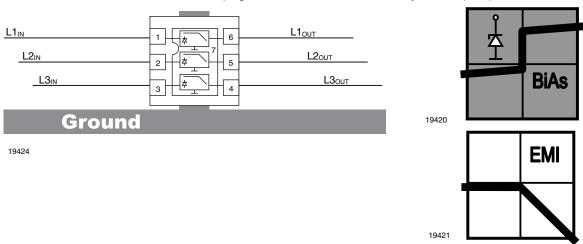
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3-Channel EMI-Filter with ESD-Protection



APPLICATION NOTE

With the VEMI35AA-HA3 3 different signal or data lines can be filtered and clamped to ground. Due to the different clamping levels in forward and reverse direction the clamping behavior is <u>Bi</u>directional and <u>Asymmetric</u> (BiAs).



The 3 independent EMI-filter are placed between

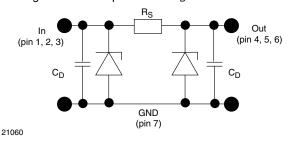
pin 1 and pin 6

pin 2 and pin 5, and

pin 3 and pin 4.

They all are connected to a common ground pin 7 on the backside of the package. Each filter is symmetrical so that all ports (pin 1 to 6) can be used as input or output.

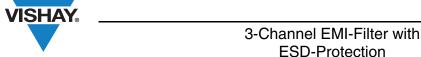
The circuit diagram of one EMI-filter-channel shows two identical Z-diodes at the input to ground and the output to ground. These Z-diodes are characterized by the breakthrough voltage level (V_{BR}) and the diode capacitance (C_{D}). Below the breakthrough voltage level the Z-diodes can be considered as capacitors. Together with these capacitors and the line resistance R_{S} between input and output the device works as a low pass filter. Low frequency signals ($f < f_{3dB}$) pass the filter while high frequency signals ($f > f_{3dB}$) will be shorted to ground through the diode capacitances C_{D} .



Each filter is symmetrical so that both ports can be used as input or output.

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VEMI35AA-HA3

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PARAMETER	TEST CONDITIONS/REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT
Protection paths	Number of channels which can be protected	N _{channel}	-	-	3	channel
Reverse stand off voltage	at I _R = 1 μA each input to pin 2	V_{RWM}	5	-	-	V
Reverse current	at V _R = 5 V each input to pin 2	I _R 1			1	μΑ
Reverse break down voltage	Each input to pin 2 at I _R = 1 mA	V_{BR}	6	-	-	V
Pos. clamping voltage	at I _{PP} = 1 A applied at the input, measured at the output; acc. IEC 61000-4-5	V _{C-out}	-	-	7.8	V
	at I _{PP} = I _{PPM} = 4 A applied at the input, measured at the output; acc. IEC 61000-4-5	V _{C-out}	-	-	8	V
Neg. clamping voltage	at I _{PP} = - 1 A applied at the input, measured at the output; acc. IEC 61000-4-5	V _{C-out}	- 1	-	-	V
	at I _{PP} = I _{PPM} = - 4 A applied at the input, measured at the output; acc. IEC 61000-4-5	V _{C-out}	- 1.2	-	-	V
Input capacitance	at V _R = 0 V; f = 1 MHz	C _{IN}	-	60	-	pF
	at V _R = 2.5 V; f = 1 MHz	C _{IN}	-	37	-	pF
ESD-clamping voltage	at ± 30 kV ESD-pulse acc. IEC 61000-4-2	V _{CESD} - 7.5		-	V	
Line resistance	Measured between input and output; I _S = 10 mA	R _S 90 100		110	Ω	
Cut-off frequency	$V_{IN} = 0 \text{ V}$; measured in a 50 Ω system	f _{3dB}	-	100	-	MHz

Note

Ratings at 25 °C, ambient temperature unless otherwise specified.

TYPICAL CHARACTERISTICS

T_{amb} = 25 °C, unless otherwise specified

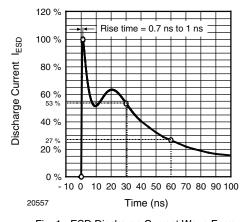


Fig. 1 - ESD Discharge Current Wave Form acc. IEC 61000-4-2 (330 $\Omega/150$ pF)

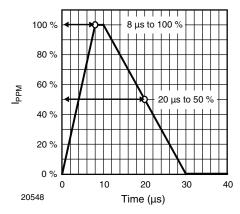


Fig. 2 - 8/20 µs Peak Pulse Current Wave Form acc. IEC 61000-4-5

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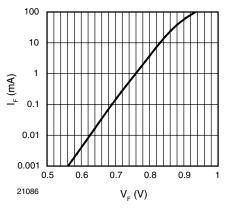


Fig. 3 - Typical Forward Current I_F vs. Forward Voltage V_F

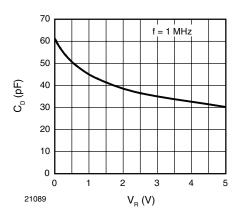


Fig. 6 - Typical Capacitance C_D vs. Reverse Voltage V_R

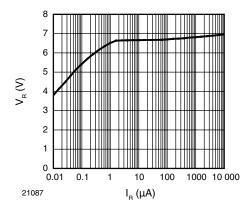


Fig. 4 - Typical Reverse Voltage V_R vs. Reverse Current I_R

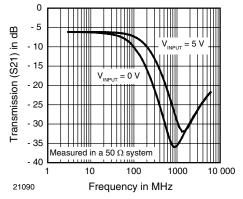


Fig. 7 - Typical Small Signal Transmission (S21) at $Z_0 = 50 \Omega$

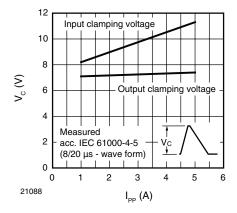


Fig. 5 - Typical Peak Clamping Voltage V_C vs. Peak Pulse Current I_{PP}

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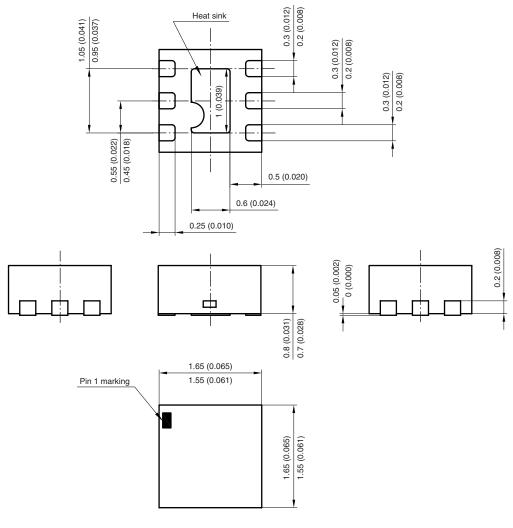


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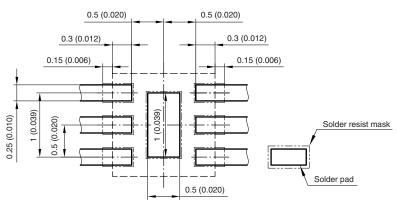
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PACKAGE DIMENSIONS in millimeters (inches): LLP75-7A



Foot print recommendation:



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