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<u>Vishay Semiconductor/Opto Division</u> <u>IL388T</u>

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Distributor of Vishay Semiconductor/Opto Division: Excellent Integrated System Limited Datasheet of IL388T - OPTOISO 2.13KV 2CH TRANS 8SOP

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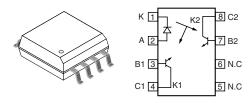


IL388

COMPLIANT

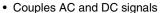
Vishay Semiconductors

Linear Optocoupler, PCMCIA Package



FEATURES

- 2.3 mm high SMD package
- High sensitivity (K1) at low operating LED current



- · Low input-output capacitance
- Isolation test voltage, 2130 V_{DC}
- Low distortion, below 80 db
- · 0.4 mm internal insulation thickness
- Component in accordance to RoHS 2002/95/EC and WEEE 2002/96/EC

APPLICATIONS

- Optical DAA for V.34 FAX/modem PCMCIA cards
- Digital telephone line isolation

DESCRIPTION

The IL388 family of linear optocoupler consist of an IRLED optically coupled to two photodiodes. The emitter is located such that both photodiodes receive approximately an equal amount of infrared light. The diodes produce a proportional amount of photocurrents. The ratio of the photocurrents stays constant with high accuracy when either the LED current changes or the ambient temperature changes. Thus one can control the output photodiode current.

The IL388 optocouplers can be used with the aid of operational amplifiers in closed loop conditions to achieve highly linearly and electrically AC and or DC signals amplifiers.

ORDER INFORMATION				
PART	REMARKS			
IL388	Couples AC and DC signals, SOP-8			

Note

For additional information on the available options refer to option information.

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
INPUT					
Reverse voltage		V_{R}	≤ 3.0	V	
Forward current		I _F	≤ 30	mA	
Surge current pulse width < 10 μs		I _{PK}	≤ 150	mA	
Power dissipation		P _{diss}	≤ 150	mW	
Derate linearly from 25 °C			≤ 2.0	mW/°C	
Junction temperature		T _j	≤ 100	°C	
OUTPUT	•	•		•	
Reverse voltage		V_{R}	≤ 15	V	
Power dissipation		P _{diss}	≤ 50	mW	
Derate linearly from 25 °C			≤ 0.65	mW/°C	
Junction temperature		T _j	≤ 100	°C	

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ABSOLUTE MAXIMUM RATINGS				
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
COUPLER				
Isolation test voltage		V _{ISO}	≤ 2130	V_{DC}
Total package power dissipation		P _{tot}	≤ 250	mW
Derate linearly from 25 °C			≤ 2.8	mW/°C
Storage temperature		T _{stg}	- 40 to + 150	°C
Operating temperature		T _{amb}	0 to + 75	°C
Lead soldering time at 260 °C			≤ 10	S
laslatian maintana	V _{IO} = 500 V, T _{amb} = 25 °C	R _{IO}	≥ 10 ¹²	Ω
Isolation resistance	V _{IO} = 500 V, T _{amb} = 100 °C	R _{IO}	≤ 10 ¹¹	Ω

Note

T_{amb} = 25 °C, unless otherwise specified.

Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute maximum ratings for extended periods of the time can adversely affect reliability.

PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
INPUT			•	<u>'</u>		•	•
Forward voltage	I _F = 10 mA		V _F		1.8	2.1	V
Reverse current	V _R = 3.0 V		I _R		0.01	10	μΑ
V _F temperature coefficient			ΔV _F /Δ °C		- 2.2		mW/°C
Junction capacitance	V _F = 0 V, f = 1.0 MHz		C _j		15		pF
Dynamic resistance	I _F = 10 mA		$\Delta V_F/\Delta I_F$		6.0		Ω
OUTPUT							
Junction capacitance	V _F = 0 V, f = 1.0 MHz		C _j		12		pF
AC Characteristics photovoltaic mode							
Frequency response	I_{P1} = 25 mA modulation current ΔI_{P1} = ± 6.0 μA		BW (- 3 db)		1.0	50	MHz
Phase response	$I_{P1} = 25$ mA modulation current $\Delta I_{P1} = \pm 6.0$ μ A				45		٥
Rise time	I_{P1} = 25 mA modulation current ΔI_{P1} = ± 6.0 μA		t _r		350		ns
COUPLER							
Capacitance (input to output)	V _F = 0 V, f = 1.0 MHz		C _{IO}		1.0		pF
Common mode capacitance	V _F = 0 V, f = 1.0 MHz		Ссм		0.5		pF
Coupled characteristics							
K ₁	$I_F = 2.0 \text{ mA}, V_D = 0 \text{ V}$	_	K1	0.007			
THD	$f_0 = 316$, $I_{PI} = 35 \mu A$, $V_D = 0 V$			- 83			db
$K_3 = K_2/K_1$	$I_F = 2.0 \text{ mA}, V_D = 0 \text{ V}$			0.690		1.311	

Note

 $T_{amb} = 25$ °C, unless otherwise specified.

Minimum and maximum values were tested requierements. Typical values are characteristics of the device and are the result of engineering evaluations. Typical values are for information only and are not part of the testing requirements.

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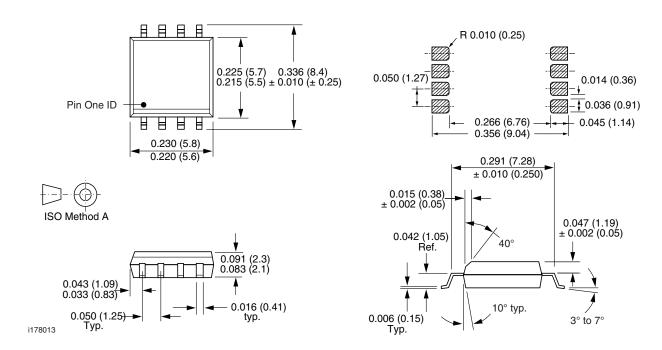
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BIN TABLE FOR K ₃				
BIN	MIN.	MAX.		
С	0.690	0.773		
D	0.765	0.859		
E	0.851	0.955		
F	0.945	1.061		
G	1.051	1.181		
Н	1.169	1.311		

Note

Binning categorization is for information only. Product cannot be ordered by this categorization.

PACKAGE DIMENSIONS in inches (millimeters)





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OZONE DEPLETING SUBSTANCES POLICY STATEMENT

It is the policy of Vishay Semiconductor GmbH to

- 1. Meet all present and future national and international statutory requirements.
- 2. Regularly and continuously improve the performance of our products, processes, distribution and operating systems with respect to their impact on the health and safety of our employees and the public, as well as their impact on the environment.

It is particular concern to control or eliminate releases of those substances into the atmosphere which are known as ozone depleting substances (ODSs).

The Montreal Protocol (1987) and its London Amendments (1990) intend to severely restrict the use of ODSs and forbid their use within the next ten years. Various national and international initiatives are pressing for an earlier ban on these substances.

Vishay Semiconductor GmbH has been able to use its policy of continuous improvements to eliminate the use of ODSs listed in the following documents.

- 1. Annex A, B and list of transitional substances of the Montreal Protocol and the London Amendments respectively.
- 2. Class I and II ozone depleting substances in the Clean Air Act Amendments of 1990 by the Environmental Protection Agency (EPA) in the USA
- 3. Council Decision 88/540/EEC and 91/690/EEC Annex A, B and C (transitional substances) respectively.

Vishay Semiconductor GmbH can certify that our semiconductors are not manufactured with ozone depleting substances and do not contain such substances.

We reserve the right to make changes to improve technical design and may do so without further notice.

Parameters can vary in different applications. All operating parameters must be validated for each customer application by the customer. Should the buyer use Vishay Semiconductors products for any unintended or unauthorized application, the buyer shall indemnify Vishay Semiconductors against all claims, costs, damages, and expenses, arising out of, directly or indirectly, any claim of personal damage, injury or death associated with such unintended or unauthorized use.

Vishay Semiconductor GmbH, P.O.B. 3535, D-74025 Heilbronn, Germany

www.vishay.com

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Vishay Intertechnology, Inc. hereby certifies that all its products that are identified as RoHS-Compliant fulfill the definitions and restrictions defined under Directive 2011/65/EU of The European Parliament and of the Council of June 8, 2011 on the restriction of the use of certain hazardous substances in electrical and electronic equipment (EEE) - recast, unless otherwise specified as non-compliant.

Please note that some Vishay documentation may still make reference to RoHS Directive 2002/95/EC. We confirm that all the products identified as being compliant to Directive 2002/95/EC conform to Directive 2011/65/EU.

Vishay Intertechnology, Inc. hereby certifies that all its products that are identified as Halogen-Free follow Halogen-Free requirements as per JEDEC JS709A standards. Please note that some Vishay documentation may still make reference to the IEC 61249-2-21 definition. We confirm that all the products identified as being compliant to IEC 61249-2-21 conform to JEDEC JS709A standards.

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