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Diodes Incorporated DMN3300U-7

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DMN3300U

#### N-CHANNEL ENHANCEMENT MODE MOSFET

#### **Product Summary**

V <sub>(BR)DSS</sub>	R <sub>DS(ON)</sub>	Package	I <sub>D</sub> T <sub>A</sub> = +25°C
30V	0.15Ω @ V <sub>GS</sub> = 4.5V		2A
	0.2Ω @ V <sub>GS</sub> = 2.5V	SOT23	1.6A
	0.25Ω @ V <sub>GS</sub> = 1.8V	30123	1.4A
	0.3Ω @ V <sub>GS</sub> = 1.5V		1.2A

#### Description

This new generation MOSFET has been designed to minimize the on-state resistance  $(R_{\text{DS}(\text{on})})$  and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

## Applications

- **DC-DC** Converters
- **Power Management Functions**
- Battery Operated Systems and Solid-State Relays
- Drivers: Relays, Solenoids, Lamps, Hammers, Displays, Memories, Transistors, etc

SOT23



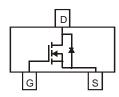
Top View

#### Features

- Low On-Resistance
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Small Surface Mount Package
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

#### **Mechanical Data**

- Case: SOT23
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020D
- Terminal Connections: See Diagram
- Terminals: Finish Matte Tin annealed over Copper leadframe. Solderable per MIL-STD-202, Method 208 🖲
- Weight: 0.008 grams (approximate)



Internal Schematic

#### Ordering Information (Note 4)

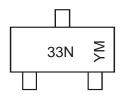
Packaging
3000/Tape & Reel
-

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. Notes:

2. See http://www.diodes.com for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds. 4. For packaging details, go to our website at http://www.diodes.com.

## **Marking Information**



33N = Marking Code YM = Date Code Marking Y = Year (ex: U = 2007) M = Month (ex: 9 = September)

Date Code Key							<u>.</u>					
Year	2007	2008	2009	2010	201	1 20	012	2013	2014	2015	2016	2017
Code	U	V	W	Х	Y		Z	А	В	С	D	E
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Auc	I Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D





#### DMN3300U

# Maximum Ratings (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units		
Drain-Source Voltage	V <sub>DSS</sub>	30	V		
Gate-Source Voltage	V <sub>GSS</sub>	±12	V		
Continuous Drain Current (Note 5) $V_{GS}$ = 4.5V	Steady State	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	ID	1.5 1.2	А
Continuous Drain Current (Note 6) $V_{GS}$ = 4.5V	ID	2.0 1.6	A		
Pulsed Drain Current (10µs pulse, duty cycle = 1%)	I <sub>DM</sub>	8	A		
Maximum Body Diode Continuous Current (Note 6)	Is	1.6	A		

#### **Thermal Characteristics**

Characteristic		Symbol	Value	Units	
Total Dowar Dissipation	(Note 5)	D-	0.7	W	
Total Power Dissipation	(Note 6)	PD	1.3		
Thermal Registeres, Junction to Ambient	(Note 5)	D	176		
Thermal Resistance, Junction to Ambient	(Note 6)	$R_{ ext{ heta}JA}$	102	°C/W	
Thermal Resistance, Junction to Case	(Note 6)	$R_{\theta JC}$	45		
Operating and Storage Temperature Range		T <sub>J,</sub> T <sub>STG</sub>	-55 to +150	°C	

#### Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	30	37	_	V	$V_{GS} = 0V, I_D = 100 \mu A$
Zero Gate Voltage Drain Current	IDSS	_	—	1	μΑ	$V_{DS} = 30V, V_{GS} = 0V$
Gate-Source Leakage	I <sub>GSS</sub>	_	_	±10	μΑ	$V_{GS} = \pm 12V$ , $V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	V <sub>GS(th)</sub>	0.5		1	V	$V_{DS} = V_{GS}$ , $I_D = 250 \mu A$
			100	150		V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 4.5A
Static Drain-Source On-Resistance	P	_	140 185	200 250	mΩ	V <sub>GS</sub> = 2.5V, I <sub>D</sub> = 3.5A
	R <sub>DS (ON)</sub>					V <sub>GS</sub> = 1.8V, I <sub>D</sub> = 1.5A
			240	300		V <sub>GS</sub> = 1.5V, I <sub>D</sub> = 0.5A
Forward Transfer Admittance	Y <sub>fs</sub>		5	_	S	V <sub>DS</sub> =5V, I <sub>D</sub> = 2.4A
Diode Forward Voltage	V <sub>SD</sub>	_	0.8	1.1	V	V <sub>GS</sub> = 0V, I = 0.5A
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C <sub>iss</sub>		193	—	pF	
Output Capacitance	C <sub>oss</sub>	_	35	—	pF	V <sub>DS</sub> = 10V, V <sub>GS</sub> = 0V f = 1.0MHz
Reverse Transfer Capacitance	C <sub>rss</sub>	_	23		pF	1 - 1.00012
Turn-On Delay Time	t <sub>d(on)</sub>		7			
Rise Time	tr	_	24	_	ns	$V_{DD} = 10V, R_{L} = 10\Omega$
Turn-Off Delay Time	t <sub>d(off)</sub>	_	24		ns	$I_D = 1A$ , $V_{GEN} = 4.5V$ , $R_G = 6\Omega$
Fall Time	t <sub>f</sub>	_	12			

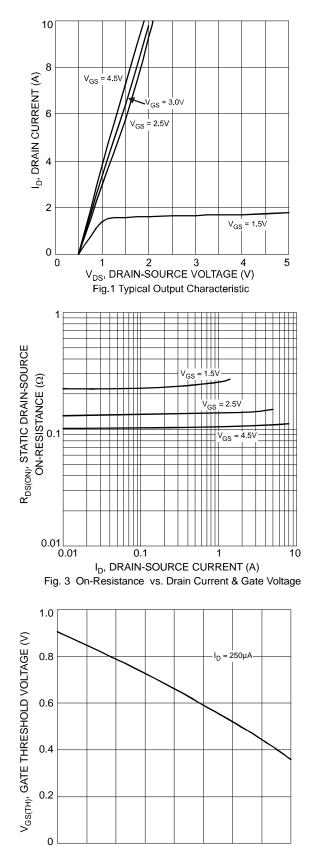
Notes:

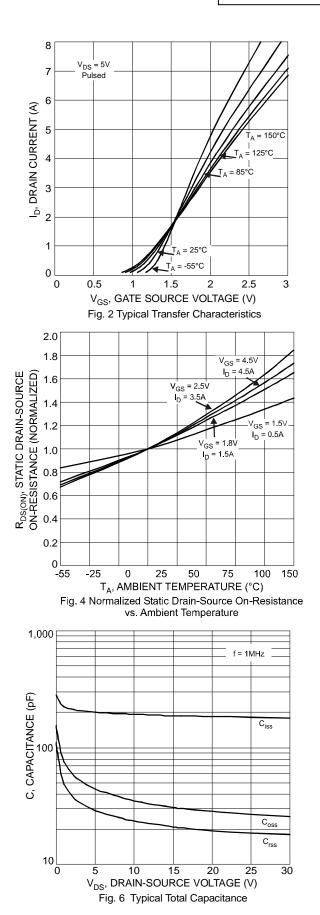
Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.
Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper pad layout
Short duration pulse test used to minimize self-heating effect.

8. Guaranteed by design. Not subject to production testing



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T<sub>A</sub>, AMBIENT TEMPERATURE (°C)

Fig. 5 Gate Threshold Variation vs. Ambient Temperature

75

100

125 150

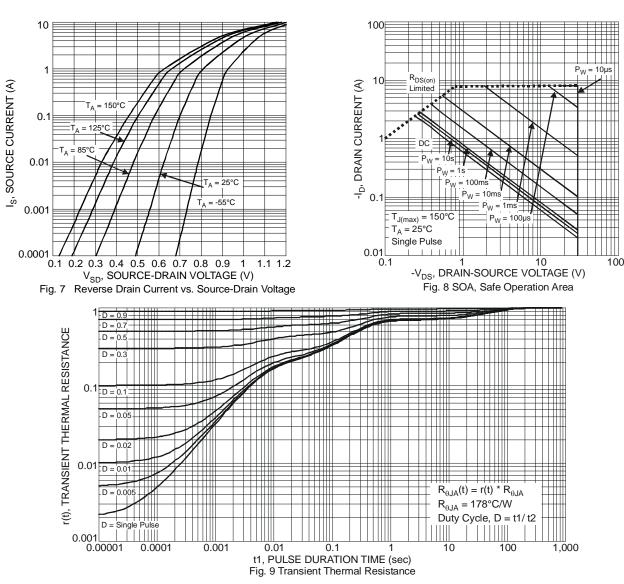
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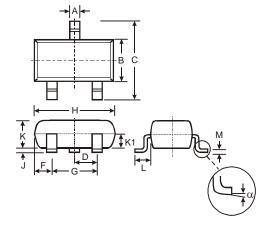






## **Package Outline Dimensions**

Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.



SOT23									
Dim	Min	Max	Тур						
Α	0.37	0.51	0.40						
В	1.20	1.40	1.30						
С	2.30	2.50	2.40						
D	0.89	1.03	0.915						
F	0.45	0.60	0.535						
G	1.78	2.05	1.83						
н	2.80	3.00	2.90						
J	0.013	0.10	0.05						
K	0.903	1.10	1.00						
K1	-	-	0.400						
L	0.45	0.61	0.55						
Μ	0.085	0.18	0.11						
α	0°	8°	-						
All	All Dimensions in mm								

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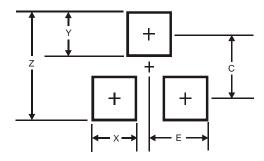




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## Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
Z	2.9
Х	0.8
Y	0.9
С	2.0
E	1.35

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