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STEVAL-IPC001V1

Electronic fiscal cash register

Data Brief

Features

- 4-Mbyte Flash, 32-Mbyte NAND and 1-Mbyte SRAM on board
- Two smartcard readers
- STM1404 secure chip, FIPS 140-2 compliant
- Ethernet 10Base-T, 3 UARTs, 1 RS-485, 1 PS/2, 1 USB full speed, 4 x I2C, 1 x SPI, 2 LCDs
- Expandable connector for upgrading to a 64-Mbyte Flash or 64-Mbyte SRAM
- Firmware available
- Cash register demo
- TCP/IP stack
- PS/2 firmware driver
- RFID CRX14 driver
- RFID manager
- Smartcard manager for reader and writer
- Fidelity card demo

Description

The STEVAL-IPC001V1 is an integrated system to allow designers to evaluate a complete and ready-to-use cash register application. It is intended for the low to medium-end POS/cash register market and consists of a hardware board, firmware and PC GUI (graphical user interface). At the core of the board, the STR710FZ2 microcontroller controls all tasks of the system. The STR710FZ2 gives the board a wide scope of communication capabilities, incorporating four UARTs, one 10T-LAN, one PS/2 interface and various IC and SPI connectors. The firmware is a modular suite which simultaneously implements a typical cash register application and provides a suitable library to develop an optimized and customized final application.



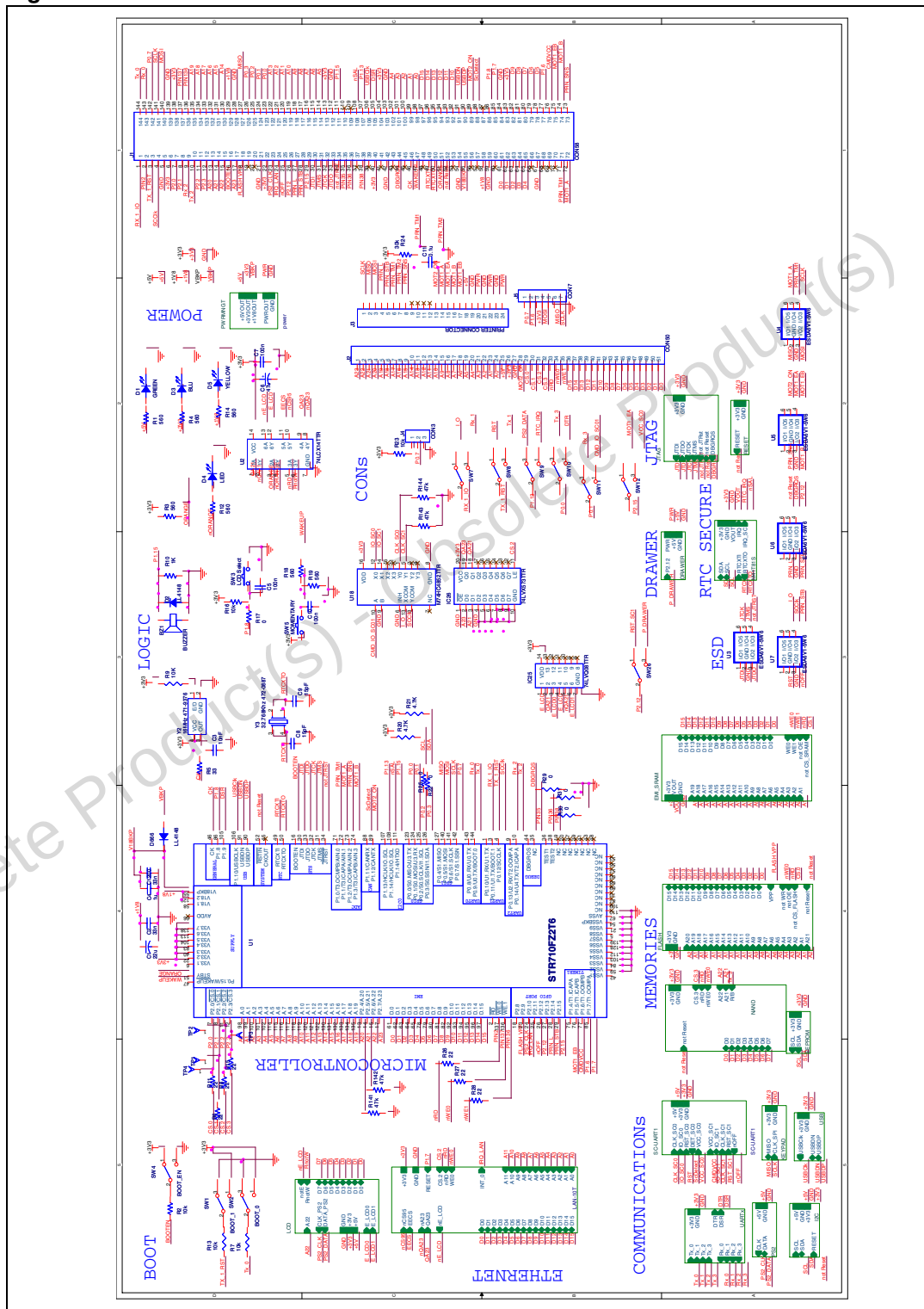
The PC GUI is a user-friendly PC application to manage both goods information and board parameters through a dedicated protocol.

Board schematic

STEVAL-IPC001V1

1 Board schematic

Figure 1. Schematic



2 Revision history

Table 1. Document revision history

Date	Revision	Changes
26-Nov-2007	1	Initial release
01-Sep-2008	2	Minor text changes, to improve readability

STEVAL-IPC001V1

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