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Diodes Incorporated ZXTN25100DGTA

For any questions, you can email us directly: <u>sales@integrated-circuit.com</u>







A Product Line of

100V NPN HIGH GAIN TRANSISTOR IN SOT223



ZXTN25100DG

Features

- BV_{CEX} > 180V
- $BV_{CEO} > 100V$
- $BV_{ECO} > 6V$
- I_C = 3A High Continuous Current
- Low Saturation Voltage V_{CE(sat)} < 100mV @ 1A
- $R_{CE(sat)} = 85m\Omega$
- Complementary PNP Type: ZXTP19100CG
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- **PPAP Capable (Note 4)**

Mechanical Data

- Case: SOT223
- Case Material: Molded Plastic, "Green" Molding Compound;
- UL Flammability Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Plated Leads; Solderable per MIL-STD-202, Method 208 @3
- Weight: 0.112 grams (Approximate)

Applications

- PSU Start-Up Circuit
- **DC-DC Converters**
- Motor Drive
- Relay, Lamp and Solenoid Drive



Ordering Information (Notes 4 & 5)

Product	Compliance	Marking	Reel size (inches)	Tape width (mm)	Quantity per reel
ZXTN25100DGTA	AEC-Q101	ZXTN25100D	7	12	1,000
ZXTN25100DGQTA	Automotive	ZXTN25100D	7	12	1,000
Notes: 1. EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. All applicable RoHS exemptions applied.					

1. EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. All applicable RoHS exemptions applied.

2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and

<1000ppm antimony compounds. 4. Automotive products are AEC-Q101 qualified and are PPAP capable. Automotive, AEC-Q101 and standard products are electrically and thermally the same, except where specified. For more information, please refer to http://www.diodes.com/quality/product_compliance_definitions/.

5. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information



ZXTN25100D = Product Type Marking Code YWW = Date Code Marking Y or \overline{Y} = Last Digit of Year (ex: 5= 2015) WW or $\overline{W}W$ = Week Code (01~53)





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Absolute Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	V _{CBO}	180	V
Collector-Emitter Voltage (forward blocking)	V _{CEX}	180	V
Collector-Emitter Voltage	V _{CEO}	100	V
Emitter-Collector Voltage (reverse blocking)	V _{ECO}	6	V
Emitter-Base Voltage	V _{EBO}	7	V
Continuous Collector Current	IC	3	A
Base Current	Ι _Β	1	А
Peak Pulse Current	I _{CM}	3.5	A

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit		
	(Note 6)		1.2 9.6		
Power Dissipation	(Note 7)		1.6 12.8	w	
Linear Derating Factor	(Note 8)	PD	3 24	mW/°C	
	(Note 9)		5.3 42		
	(Note 6)		104		
Thermal Desistance, hunstice to Archiest	(Note 7)	ΠΓ	78	°C/W	
I nermal Resistance, Junction to Ambient	(Note 8)	R _{0JA}	42		
	(Note 9)	7	23.5		
hermal Resistance, Junction to Lead (Note 10)		R _{θJL}	16		
Operating and Storage Temperature Range	T _{J.} T _{STG}	-55 to +150	°C		

ESD Ratings (Note 11)

Characteristic	Symbol	Value	Unit	JEDEC Class
Electrostatic Discharge - Human Body Model	ESD HBM	4,000	V	3A
Electrostatic Discharge - Machine Model	ESD MM	400	V	С

Notes: 6. For a device mounted with the collector lead on 15mm x 15mm 1oz copper that is on a single-sided 1.6mm FR4 PCB; device is measured under still air conditions whilst operating in steady-state. Same as Note 6, except the device is mounted on 25mm x 25mm 1oz copper.

7.

Same as Note 6, except the device is mounted on 50mm x 50mm 2oz copper. 8.

9. Same as Note 8 measured at t<5 seconds.

10. Thermal resistance from junction to solder-point (at the end of the collector lead).

11. Refer to JEDEC specification JESD22-A114 and JESD22-A115.





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Electrical Characteristics (@T _A = +25°C, unless otherwise specified.)						
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Collector-Base Breakdown Voltage	BV _{CBO}	180	220	-	V	I _C = 100μA
Collector-Emitter Breakdown Voltage (forward blocking)	BV _{CEX}	180	220	_	V	$I_{C} = 100 \mu A$, $R_{BE} < 1k\Omega$ or -1V< $V_{BC} > 0.25V$
Collector-Emitter Breakdown Voltage (Note 12)	BV _{CEO}	100	130	-	V	$I_{\rm C} = 10 {\rm mA}$
Emitter-Collector Breakdown Voltage (reverse blocking)	BV _{ECX}	6	8.2	-	V	I_{C} = 100μA, R _{BC} <1kΩor 0.25V< V _{BC} > -0.25V
Emitter-Collector Breakdown Voltage (reverse blocking)	BV _{ECO}	6	8.7	-	V	I _E = 100μA
Emitter-Base Breakdown Voltage	BV _{EBO}	7	8.3	-	V	I _E = 100μA
Callester Cut Off Current		-	< 1	50	nA	V _{CB} = 180V
Collector Cut-Off Current	ICBO	-	-	0.5	μA	V _{CB} = 180V, T _A = 105°C
Collector-Emitter Cut-Off Current	ICEX	-	-	100	nA	$V_{CE} = 100V, R_{BE} < 1k\Omega \text{ or}$ -1V < V _{BC} > 0.25V
Emitter Cut-Off Current	I _{EBO}	-	< 1	50	nA	V _{EB} = 5.6V
		-	120	170	mV	$I_{\rm C} = 0.5 \text{A}, I_{\rm B} = 10 \text{mA}$
Collector Emitter Seturation Values (Note 12)	N/	-	80	100	mV	$I_{\rm C} = 1$ A, $I_{\rm B} = 100$ mA
Collector-Emiller Saturation voltage (Note 12)	VCE(sat)	_	215	345	mV	$I_{\rm C} = 2.5 \text{A}, I_{\rm B} = 250 \text{mA}$
		-	200	500	mV	$I_{\rm C} = 3A, I_{\rm B} = 600 {\rm mA}$
Base-Emitter Saturation Voltage (Note 12)	V _{BE(sat)}	-	1020	1100	mV	$I_{C} = 3A, I_{B} = 600mA$
Base-Emitter Turn-On Voltage (Note 12)	V _{BE(on)}	-	905	1000	mV	$I_C = 3A, V_{CE} = 2V$
		300	450	900	-	$I_{C} = 10 \text{mA}, V_{CE} = 2 \text{V}$
DC Current Gain (Note 12)	h	120	170	-	-	$I_{C} = 0.5A, V_{CE} = 2V$
	TIFE	40	60	-	-	$I_C = 1A$, $V_{CE} = 2V$
		-	10	-	Ι	$I_C = 3A, V_{CE} = 2V$
Current Gain-Bandwidth Product (Note 12)	fT	-	175	-	MHz	$V_{CE} = 10V, I_C = 50mA,$ f = 100MHz
Input Capacitance (Note 12)	C _{ibo}	-	154	250	pF	V _{EB} = 0.5V, f = 1MHz
Output Capacitance (Note 12)	C _{obo}	-	8.7	15	pF	$V_{CB} = 10V$, f = 1MHz
Delay Time	td	-	16.4	-	ns	
Rise Time	tr	-	115	-	ns	$I_{C} = 500 \text{mA}, V_{CC} = 10 \text{V},$
Storage Time	ts	-	763	-	ns	$I_{B1} = -I_{B2} = 50 \text{mA}$
Fall Time	t _f	-	158	-	ns	

Note: 12. Measured under pulsed conditions. Pulse width \leq 300µs. Duty cycle \leq 2%.







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Package Outline Dimensions

Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.



Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
С	2.30
C1	6.40
Х	1.20
X1	3.30
Y	1.60
Y1	1.60
Y2	8.00

Note: For high voltage applications, the appropriate industry sector guidelines should be considered with regards to creepage and clearance distances between device terminals and PCB tracking.







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