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<u>Vishay Semiconductor/Diodes Division</u> <u>VBUS051BD-HD1-GS08</u>

For any questions, you can email us directly: sales@integrated-circuit.com

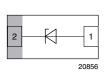


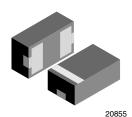


VBUS051BD-HD1

Vishay Semiconductors

Low Capacitance, Single-Line ESD-Protection Diode





FEATURES

- Ultra compact LLP1006-2L package
- Low package profile < 0.4 mm
- 1-line ESD-protection
- High surge current acc. IEC 61000-4-5
 I_{PPM} > 3 A
- Low leakage current I_R < 0.1 μA
- Low load capacitance C_D = 0.9 pF
- ESD-protection acc. IEC 61000-4-2
 ± 15 kV contact discharge
 ± 15 kV air discharge
- Soldering can be checked by standard vision inspection; no X-ray necessary
- Pin plating NiPdAu (e4) no whisker growth
- e4 precious metal (e.g. Ag, Au, NiPd, NiPdAu) (no Sn)
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

MARKING (example only)



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Bar = cathode marking

Y = type code (see table below)

X = date code

ORDERING INFORMATION				
DEVICE NAME	ORDERING CODE	TAPED UNITS PER REEL (8 mm TAPE ON 7" REEL)	MINIMUM ORDER QUANTITY	
VBUS051BD-HD1	VBUS051BD-HD1-GS08	8000	8000	

PACKAGE DATA						
DEVICE NAME	PACKAGE NAME	TYPE CODE	WEIGHT	MOLDING COMPOUND FLAMMABILITY RATING	MOISTURE SENSITIVITY LEVEL	SOLDERING CONDITIONS
VBUS051BD-HD1	LLP1006-2L	Α	0.72 mg	UL 94 V-0	MSL level 1 (according J-STD-020)	260 °C/10 s at terminals

ABSOLUTE MAXIMUM RATINGS VBUS051BD-HD1						
PARAMETER	TEST CONDITIONS	SYMBOL	VALUE	UNIT		
Peak pulse current	Acc. IEC 61000-4-5; t _p = 8/20 μs; single shot	I _{PPM}	3	А		
Peak pulse power	Acc. IEC 61000-4-5; $t_p = 8/20 \mu s$; single shot	P _{PP}	45	W		
FOD :	Contact discharge acc. IEC 61000-4-2; 10 pulses	V	± 15	kV		
ESD immunity	Air discharge acc. IEC 61000-4-2; 10 pulses	V_{ESD}	± 15	kV		
Operating temperature	Junction temperature	T _J	-55 to +145	°C		
Storage temperature		T _{STG}	-55 to +150	°C		

PATENT(S): www.vishay.com/patents

Rev. 2.3, 07-Jun-16

This Vishay product is protected by one or more United States and International patents.

1 Document Number: 81785 For technical questions, contact: ESDprotection@vishay.com





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ELECTRICAL CHARACTERISTICS VBUS051BD-HD1 (Ratings at 25 °C ambient temperature, unless otherwise specified)							
PARAMETER	TEST CONDITIONS/REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT	
Protection paths	Number of lines which can be protected	N _{channel}	-	-	1	lines	
Reverse stand-off voltage	Max. reverse working voltage	V_{RWM}	-	-	5	V	
Reverse voltage	at I _R = 0.1 μA; pin 2 to pin 1	V _R	5	-	-	V	
Reverse current	at V _R = V _{RWM} = 5 V; pin 2 to pin 1	I _R	-	< 0.01	0.1	μA	
Reverse breakdown voltage	at I _R = 1 mA; pin 2 to pin 1	V_{BR}	6.9	7.9	8.7	V	
Reverse clamping voltage	at I _{PP} = 3 A; acc. IEC 61000-4-5; pin 2 to pin 1	V _C	-	-	16	V	
Forward clamping voltage	at I _F = 3 A; acc. IEC 61000-4-5; pin 1 to pin 2	V _F	-	3.4	4	V	
Capacitance	at V _R = 0 V; f = 1 MHz; pin 2 to pin 1	C_D	-	0.9	1.3	pF	

APPLICATION NOTE

The VBUS051BD-HD1 is an ESD-protection device with the characteristic of a Z-diode with a high ESD-immunity and a very low capacitance which makes it usable for high frequency applications like USB2.0 or HDMI.

With the VBUS051BD-HD1 one high speed data line can be protected against transient voltage signals like ESD (electro static discharge). Connected to the data line (pin 2) and to ground (pin 1) negative transients will be clamped close below the ground level while positive transients will be clamped close above the 5 V working range. The clamping behavior of the VBUS051BD-HD1 is bidirectional but asymmetrical (BiAs) and so it offers the best protection for applications running up to 5 V.

TYPICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

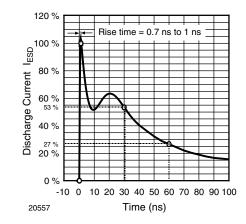


Fig. 1 - ESD Discharge Current Wave Form acc. IEC 61000-4-2 (330 Ω /150 pF)

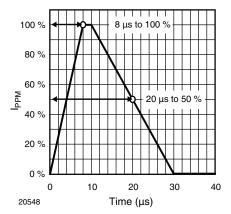


Fig. 2 - 8/20 µs Peak Pulse Current Wave Form acc. IEC 61000-4-5



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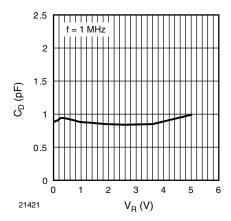


Fig. 3 - Typical Capacitance C_{D} vs. Reverse Voltage V_{R}

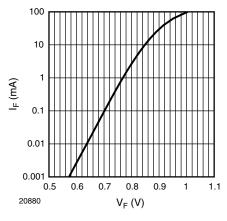


Fig. 4 - Typical Forward Current I_F vs. Forward Voltage V_F

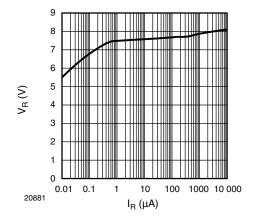


Fig. 5 - Typical Reverse Voltage V_{R} vs. Reverse Current I_{R}

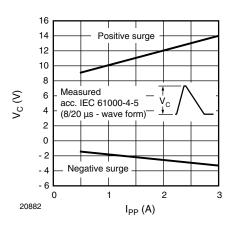


Fig. 6 - Typical Peak Clamping Voltage $V_{\rm C}$ vs. Peak Pulse Current $I_{\rm PP}$

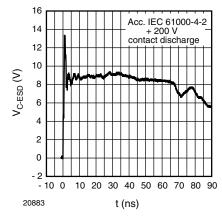


Fig. 7 - Typical Clamping Performance at + 200 V Contact Discharge (acc. IEC 61000-4-2)

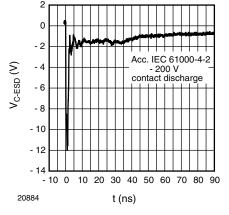


Fig. 8 - Typical Clamping Performance at - 200 V Contact Discharge (acc. IEC 61000-4-2)





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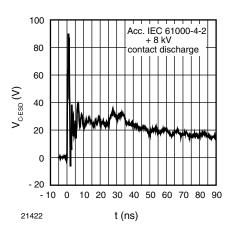


Fig. 9 - Typical Clamping Performance at + 8 kV Contact Discharge (acc. IEC 61000-4-2)

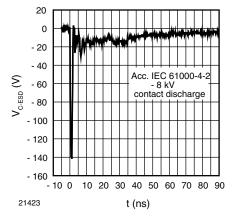


Fig. 10 - Typical Clamping Performance at - 8 kV Contact Discharge (acc. IEC 61000-4-2)

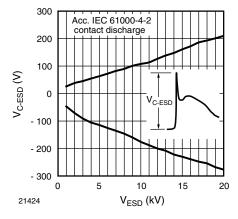


Fig. 11 - Typical Peak. Clamping Voltage at ESD Contact Discharge (acc. IEC 61000-4-2)

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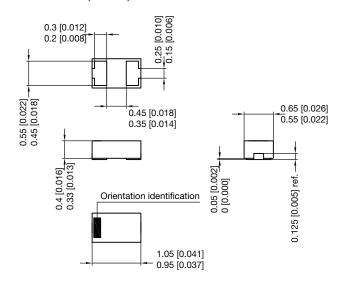


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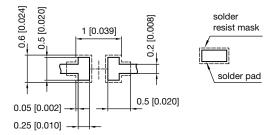
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PACKAGE DIMENSIONS in millimeters (inches) LLP1006-2L



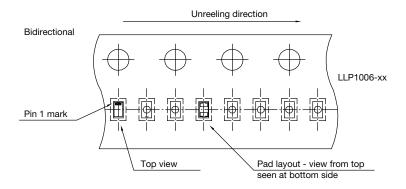
Foot print recommendation:



Pad Design Patented: (P)US 9.018.537 B2)

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