

# **Excellent Integrated System Limited**

Stocking Distributor

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Sumida Corporation CPFC74NP-PS10H2A15

For any questions, you can email us directly: <u>sales@integrated-circuit.com</u>



**Distributor of Sumida Corporation: Excellent Integrated System Limited** Datasheet of CPFC74NP-PS10H2A15 - CHOKE COMMON MODE 1.5A SMD Contact us: sales@integrated-circuit.com Website: www.integrated-circuit.com

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#### DC COMMON MODE CHOKE COIL <SMD Type:CPFC Series>

# Type: CPFC74

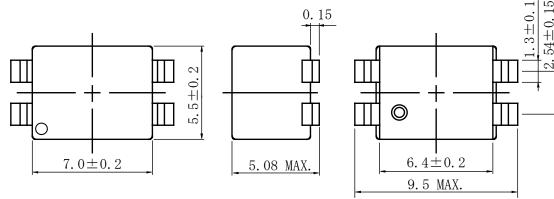
#### Product Description

• 9.5 $\times$ 5.7mm Max.(L $\times$ W), 5.1mm Max. Height .

### ♦ Feature

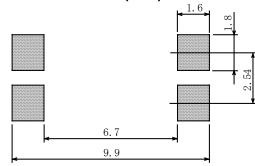
- Ideally used in CAN BUS ,AV,OA equipment.
- RoHS Compliance

## Dimensions (mm)

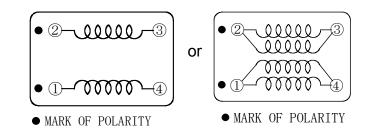


\* Dimension does not include solder used on coil.

#### Land Pattern (mm)



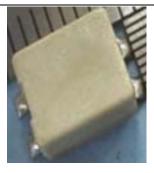
### Schematics (Bottom)



### Specification (For CAN bus)

Part Name.	Stamp	Impedance ( $\Omega$ ) (L1,L2 Parallel) (10~100MHz)	Insulation Resistance (M Ω )(Coil-Coil) (DC80V 1min)	Withstanding Voltage (coil-coil) (5sec)	D.C.R. ( Ω ) (1-2)at 20℃ (3-4) short ※
CPFC74NP-CB1ØM4	C10M	1000 MIN.	100 MIN.	200V DC	0.6 MAX.
CPFC74NP-CBØ8M6	C08M	800 MIN.	100 MIN.	200V DC	0.5 MAX.

※ D.C.R. is measured by 2 lines as series because impedance will be deteriorated when D.C.R. is measured by 1 line.





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#### DC COMMON MODE CHOKE COIL <SMD Type:CPFC Series>

# Type: CPFC74

## • Specification (For Power supply)

Part Name.	Stamp	Impedance (Ω) (L1,L2 Parallel)	Insulation Resistance (M Ω )(Coil-Coil) (DC100V 1min.)	Withstanding Voltage (Coil-Coil) (5sec)	D.C.R. (m Ω ) (1-2)at 20℃ (3-4) Short ※2	Rated Current (1-2)(A) (3-4) Short ※1
CPFC74NP-PS1ØH2A15	P15H	700 MIN. (100 MHz)	10 MIN.	D.C.125V	120	1.5
CPFC74NP-PSØ2H2A2Ø	P20H	200 MIN. (20~300MHz)	10 MIN.	D.C.125V	120	2.0
CPFC74NP-PSØ3H2A25	P25H	300 MIN. (160 MHz)	10 MIN.	D.C.125V	120	2.5
CPFC74NP-PSØ1H2A3Ø	P30H	100 MIN.	10 MIN.	D.C.125V	60	3.0

%1: Rated current: The DC current at which the temperature rise is  $\triangle t=40^{\circ}$ C.(Ta=20°C).

%2: D.C.R is measured by 2 lines as series because impedance will be deteriorated when D.C.R. is measured by 1 line.