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STMicroelectronics 2STA2510

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2STA2510

High power PNP epitaxial planar bipolar transistor

Features

- High breakdown voltage V_{CEO} = -100 V
- Complementary to 2STC2510
- Typical f_t = 20 MHz
- Fully characterized at 125 °C

Application

Audio power amplifier

Description

The device is a PNP transistor manufactured using new BiT-LA (Bipolar transistor for linear amplifier) technology. The resulting transistor shows good gain linearity behaviour.

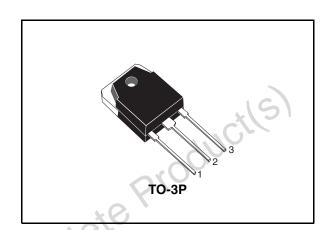


Figure 1. Internal schematic diagram

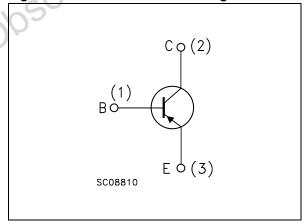


Table 1. Device summary

Order code	Marking	Package	Packaging
2STA2510	2STA2510	TO-3P	Tube

November 2008 Rev 3 1/8



Electrical ratings 2STA2510

1 Electrical ratings

Table 2. Absolute maximum rating

Symbol	Parameter	Value	Unit
V _{CBO}	Collector-base voltage (I _E = 0)	-100	V
V _{CEO}	Collector-emitter voltage (I _B = 0)	-100	٧
V _{EBO}	Emitter-base voltage (I _C = 0)	-6	V
I _C	Collector current	-25	Α
I _{CM}	Collector peak current (t _P < 5 ms)	-50	Α
P _{TOT}	Total dissipation at T _c = 25 °C	125	O w
T _{stg}	Storage temperature	-65 to 150	°C
TJ	Max. operating junction temperature	150	°C

Table 3. Thermal data

	Symbol	Parameter	, ete	Value	Unit
	R _{thj-case}	Thermal resistance junction-case	max	1	°C/W
0050		product(s)			



2STA2510 Electrical characteristics

2 Electrical characteristics

(T_{case} = 25 °C; unless otherwise specified)

Table 4. Electrical characteristics

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
I _{CBO}	Collector cut-off current (I _E = 0)	V _{CB} = -100 V			-10	μΑ
I _{EBO}	Emitter cut-off current (I _C = 0)	V _{EB} = -6 V			-10	μА
V _{(BR)CEO} ⁽¹⁾	Collector-emitter breakdown voltage (I _B = 0)	I _C = -50 mA	-100	. 10		V
V _{(BR)CBO}	Collector-base breakdown voltage (I _E = 0)	I _C = -100 μA	-100	20.		٧
V _{(BR)EBO} ⁽¹⁾	Emitter-base breakdown voltage $(I_C = 0)$	I _E = -1 mA	-6			V
V _{CE(sat)} (1)	Collector-emitter saturation voltage	$I_C = -12 \text{ A}$ $I_B = -1.2 \text{ A}$			-1.5	V
V _{BE} ⁽¹⁾	Base-emitter voltage	$V_{CE} = -4 \text{ V}$ $I_{C} = -12 \text{ A}$			-1.8	V
h _{FE}	DC current gain	$I_C = -12 \text{ A}$ $V_{CE} = -4 \text{ V}$	40		80	
f _T	Transition frequency	$I_C = -0.5 \text{ A}$ $V_{CE} = -12 \text{ V}$		20		MHz

^{1.} Pulsed duration = 300 μs, duty cycle ≤ 1.5 %



Electrical characteristics 2STA2510

2.1 Electrical characteristic (curves)

Figure 2. Safe operating area

Figure 3. Derating curve

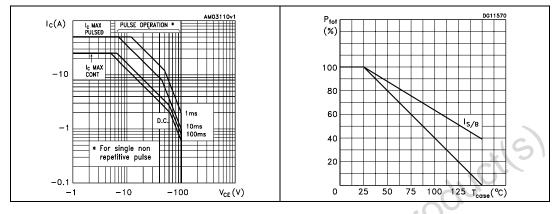


Figure 4. Output characteristics

Figure 5. DC current gain

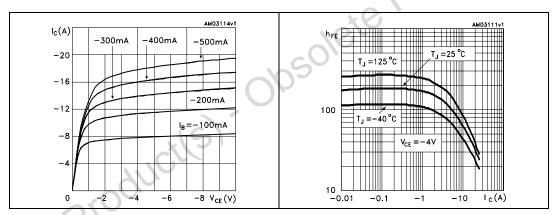
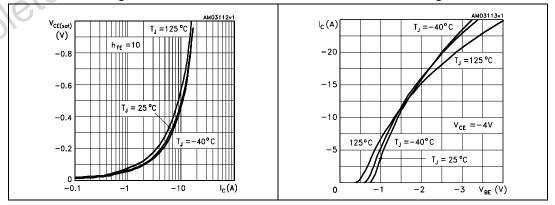


Figure 6. Collector-emitter saturation voltage

Figure 7. Collector current vs baseemitter voltage





2STA2510

Package mechanical data

3 Package mechanical data

In order to meet environmental requirements, ST offers these devices in ECOPACK® packages. These packages have a Lead-free second level interconnect . The category of second level interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available at: www.st.com

Obsolete Product(s).

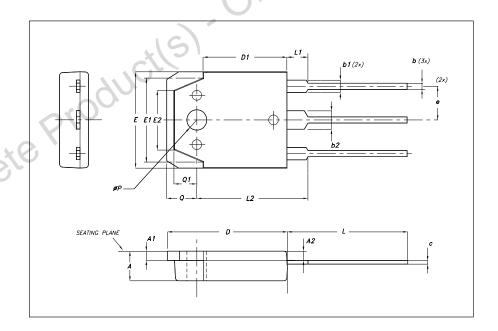


Package mechanical data

2STA2510

TO-3P Mechanical	data	а
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DIM.		mm.	
DIWI.	MIN.	TYP	MAX.
Α	4.6		5
A1	1.45	1.50	1.65
A2	1.20	1.40	1.60
b	0.80	1	1.20
b1	1.80		2.20
b2	2.80		3.20
С	0.55	0.60	0.75
D	19.70	19.90	20.10
D1		13.90	1116
E	15.40		15.80
E1		13.60	100
E2		9.60	
е	5.15	5.45	5.75
L	19.50	20	20.50
L1		3.50	0
L2	18.20	18.40	18.60
Р	3.10		3.30
Q		5	
Q1		3.80	





2STA2510 Revision history

4 Revision history

Table 5. Document revision history

Date
26-Nov-2007
16-May-2008
14-Nov-2008
14-Nov-2008





Distributor of STMicroelectronics: Excellent Integrated System Limited

Datasheet of 2STA2510 - TRANS PNP 100V 25A TO-3P

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