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Diodes Incorporated 2N7002K-7

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N-CHANNEL ENHANCEMENT MODE MOSFET

2N7002K

# **Product Summary**

V <sub>(BR)</sub> dss	R <sub>DS(ON)</sub> max	I <sub>D</sub> max T <sub>A</sub> = +25°C
60V	2Ω @ V <sub>GS</sub> = 10V	380mA
	$3\Omega @ V_{GS} = 5V$	310mA

## Description

This MOSFET has been designed to minimize the on-state resistance (R<sub>DS(on)</sub>) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

# Applications

- Motor Control
- **Power Management Functions**
- Backlighting

# Features and Benefits

- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- ESD Protected Up To 2kV
- Totally Lead-Free & Fully RoHS compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

## **Mechanical Data**

Case: SOT23

Drain

Source

Equivalent Circuit

- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin annealed over Alloy 42 leadframe. Solderable per MIL-STD-202, Method 208 3
- Weight: 0.008 grams (approximate)

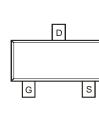




Top View

SOT23

Gate Gate Protection Diode



Top View

# Ordering Information (Note 4)

Part Number	Compliance	Case	Packaging
2N7002K-7	Standard	SOT23	3000/Tape & Reel
2N7002KQ-7	Automotive	SOT23	3000/Tape & Reel
2N7002K-13	Standard	SOT23	10000/Tape & Reel
2N7002KQ-13	Automotive	SOT23	10000/Tape & Reel

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. Notes: 2. See http://www.diodes.com/guality/lead\_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green"

and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds

For packaging details, go to our website at http://www.diodes.com/products/packages.html

# Marking Information

П	<u> </u>
К7К Ĕ	к7к ⋛
Chengdu A/T Site	Shanghai A/T Site

K7K = Product Type Marking Code YM = Date Code Marking for SAT (Shanghai Assembly/ Test site) YM = Date Code Marking for CAT (Chengdu Assembly/ Test site) Y or  $\overline{Y}$  = Year (ex: A = 2013)

M = Month (ex: 9 = September)

Date Code Kev

Year	2006	2007	2008	2009	2010	2011	2012	2013	2014	2015	2016	2017
Code	Т	U	V	W	Х	Y	Z	А	В	С	D	E
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D





2N7002K

# Maximum Ratings (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Units		
Drain-Source Voltage		V <sub>DSS</sub>	60	V		
Gate-Source Voltage			V <sub>GSS</sub>	±20	V	
		T <sub>A</sub> = +25°C T <sub>A</sub> = +70°C	ID	380 300	mA	
Continuous Drain Current (Note 6) $V_{GS} = 10V$	t<5s	T <sub>A</sub> = +25°C T <sub>A</sub> = +70°C	ID	430 340	mA	
Continuous Drain Current (Note C) V	Steady State	T <sub>A</sub> = +25°C T <sub>A</sub> = +70°C	ID	310 240	mA	
Continuous Drain Current (Note 6) $V_{GS} = 5V$	t<5s	T <sub>A</sub> = +25°C T <sub>A</sub> = +70°C	ID	350 270	mA	
Maximum Continuous Body Diode Forward Current	nt (Note 6)	Is	0.5	А		
Pulsed Drain Current (10µs pulse, duty cycle = 1%	I <sub>DM</sub>	1.2	А			

# Thermal Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Units	
Total Power Dissipation (Note 5)		PD	370	mW	
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	P	357	°C/W	
mermai Resistance, Junction to Ambient (Note 5)	t<5s	$R_{\theta JA}$	292	0/10	
Total Power Dissipation (Note 6)		PD	540	mW	
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	$R_{\thetaJA}$	240		
Thermal Resistance, Sunction to Amblent (Note 0)	t<5s		197	°C/W	
Thermal Resistance, Junction to Case (Note 6)		R <sub>0JC</sub>	91		
Operating and Storage Temperature Range		T <sub>J,</sub> T <sub>STG</sub>	-55 to +150	°C	

## Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	60	—		V	$V_{GS} = 0V, I_D = 10\mu A$
Zero Gate Voltage Drain Current	I <sub>DSS</sub>		_	1.0	μA	$V_{DS} = 60V, V_{GS} = 0V$
Gate-Source Leakage	I <sub>GSS</sub>	_	_	±10	μA	$V_{GS} = \pm 20V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	V <sub>GS(th)</sub>	1.0	1.6	2.5	V	$V_{DS} = 10V, I_D = 1mA$
Static Drain-Source On-Resistance	P		—	2.0	Ω	$V_{GS} = 10V, I_D = 0.5A$
	R <sub>DS(ON)</sub>		—	3.0	12	$V_{GS} = 5V, I_D = 0.05A$
Forward Transfer Admittance	Y <sub>fs</sub>	80	_		ms	V <sub>DS</sub> =10V, I <sub>D</sub> = 0.2A
Diode Forward Voltage	V <sub>SD</sub>		0.75	1.1	V	$V_{GS} = 0V, I_{S} = 115mA$
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C <sub>iss</sub>	_	30	50	pF	
Output Capacitance	C <sub>oss</sub>		4.2	25	рF	V <sub>DS</sub> = 25V, V <sub>GS</sub> = 0V f = 1.0MHz
Reverse Transfer Capacitance	C <sub>rss</sub>		2.9	5.0	рF	1 = 1.000112
Gate Resistance	Rg	-	133	—	Ω	$f = 1MHz$ , $V_{GS} = 0V$ , $V_{DS} = 0V$
Total Gate Charge	Qg	_	0.3	_	nC	
Gate-Source Charge	Q <sub>gs</sub>	_	0.2		nC	$V_{GS} = 4.5V, V_{DS} = 10V,$
Gate-Drain Charge	Q <sub>gd</sub>		0.08		nC	I <sub>D</sub> = 250mA
Turn-On Delay Time	t <sub>D(on)</sub>	_	3.9	—	ns	
Turn-On Rise Time	tr		3.4	—	ns	$V_{DD} = 30V, V_{GS} = 10V,$
Turn-Off Delay Time	t <sub>D(off)</sub>		15.7	—	ns	$R_{G} = 25\Omega, I_{D} = 200 \text{mA}$
Turn-Off Fall Time	tf		9.9	—	ns	1

Notes:

Device mounted on FR-4 PCB, with minimum recommended pad layout
Device mounted on 1" x 1" FR-4 PCB with high coverage 2oz. Copper, single sided.
Short duration pulse test used to minimize self-heating effect.

8. Guaranteed by design. Not subject to product testing.



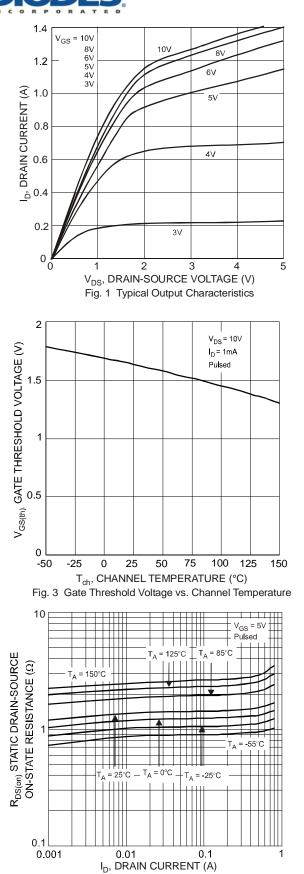


Fig. 5 Static Drain-Source On-Resistance vs. Drain Current

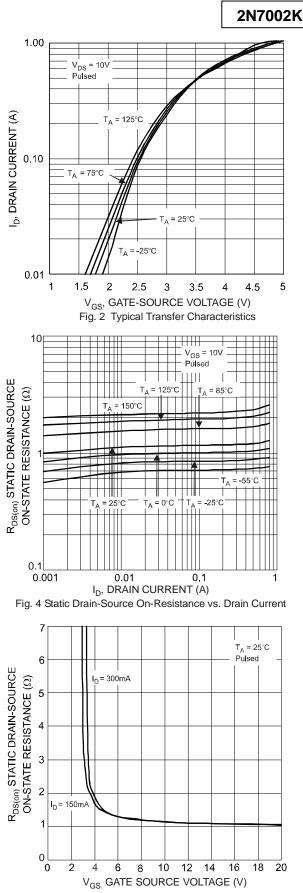
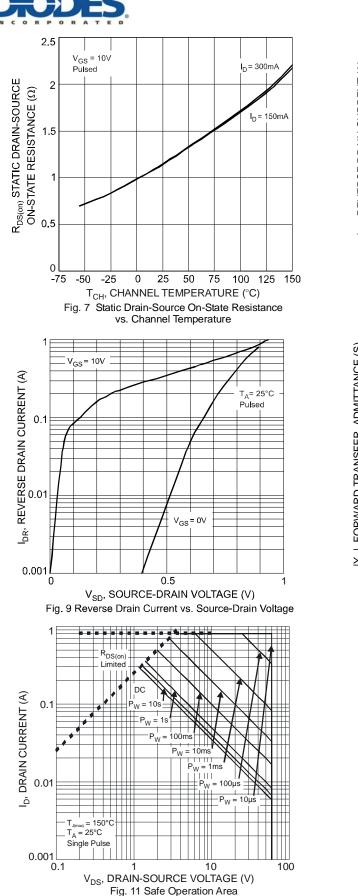
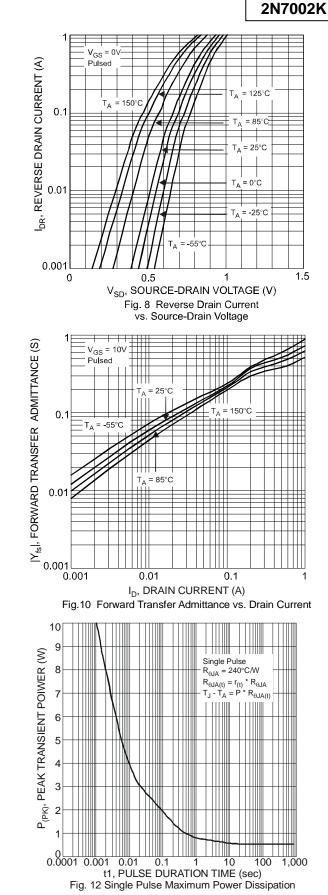


Fig. 6 Static Drain-Source On-Resistance vs. Gate-Source Voltage





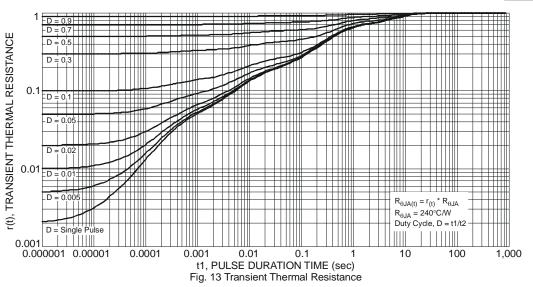


2N7002K Document number: DS30896 Rev. 14 - 2



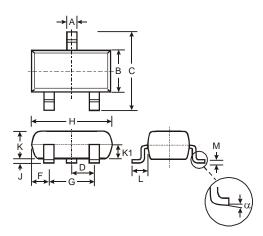


## 2N7002K



# **Package Outline Dimensions**

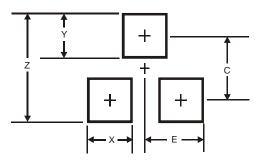
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.



SOT23							
Dim	Min	Max	Тур				
Α	0.37	0.51	0.40				
В	1.20	1.40	1.30				
С	2.30	2.50	2.40				
D	0.89	1.03	0.915				
F	0.45	0.60	0.535				
G	1.78	2.05	1.83				
Н	2.80	3.00	2.90				
J	0.013	0.10	0.05				
Κ	0.903	1.10	1.00				
K1	-	-	0.400				
L	0.45	0.61	0.55				
М	0.085	0.18	0.11				
α	0°	8°	-				
All	Dimens	ions in	mm				

# Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version



Dimensions	Value (in mm)
Z	2.9
Х	0.8
Y	0.9
С	2.0
E	1.35





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